

New Horizon College of Engineering

Department of Electronics and Communication Engineering

BOARD OF STUDIES MEETING-2

DATE: 24/06/2016

VENUE: Boardroom, MBA Department

Time: 10 AM to 1:30 PM

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AGENDA

1. Discussion on Scheme of B.E Courses from 2nd to 4th year,
2. Approval of B.E. Syllabus for 2nd year,
3. Discussion on Scheme of M.Tech (VLSI) Courses for 2nd year,
4. Discussion on Scheme of M.Tech (CMS) Courses for 2nd year,
5. Approval of M.Tech (VLSI) Syllabus for 2nd year,
6. Approval of M.Tech (CMS) Syllabus for 2nd year.

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10.	Vote of thanks by Chairman of BOS	

MEMBERS OF THE BOARD OF STUDIES

Sl.No.		Name
1.	Faculty with different specialization	Dr. Sanjay Jain, HOD,ECE, NHCE
2.		Mr.Aravinda K, Sr. Asst. Professor,ECE,NHCE
3.		Ms. Dharmambal, Sr. Asst. Professor, ECE,NHCE
4.		Dr. Mohan Kumar Naik, Sr. Associate Professor,ECE,NHCE
5.		Dr. Nisha KCR, Associate Professor, ECE, NHCE
6.		Ms. P. Sreeramani, Sr. Asst. Professor, ECE,NHCE
7.		Ms. Ishani Mishra, Assistant Professor, ECE,NHCE
8.	Subject Expert	Dr. Seshachalam, HOD,BMS
9.		Dr. B K Sujatha, Professor,MSRIT
10.	VTU Nominee (Expert-VC)	Dr. Preeta Sharan, Professor, Oxford College of Engineering
11.	Industry	Mr. Ravishankar, Director, Anora laboratories Pvt Ltd.
12.		Mr. Nagaraj Subramaniam, Eklakshya, Ex GM, TI
13.		Dr. Prasad, MD, Audience Communication
14.	Co-opted member	Ms. Lipsa Dash, Assistant Professor, ECE,NHCE
15.		Ms. Divya Sharma, Assistant Professor, ECE,NHCE
16.	Student-PG Alumni	Mr. Praveen Kumar J, M.Tech, e silicon Tech

MEMBERS PRESENT

1.	Dr. Sanjay Jain, HOD,ECE, NHCE
2.	Mr.Aravinda K, Sr. Asst. Professor, ECE,NHCE
3.	Ms. Dharmambal, Sr. Asst. Professor, ECE,NHCE
4.	Dr. Nisha KCR, Associate Professor, ECE, NHCE
5.	Ms. P. Sreeramani, Sr. Asst. Professor, ECE,NHCE
6.	Ms. Ishani Mishra, Assistant Professor, ECE,NHCE
7.	Dr. B K Sujatha, Professor, MSRIT
8.	Dr. Preeta Sharan, Professor, Oxford College of Engineering
9.	Mr. Nagaraj Subramaniam, Eklakshya, Ex GM, TI
10.	Dr. Prasad, MD, Audience Communication
11.	Ms. Lipsa Dash, Assistant Professor, ECE,NHCE
12.	Ms. Divya Sharma, Assistant Professor, ECE,NHCE
13.	Ms. Srividya S, Physical Design Engineer, Intel Technologies India Pvt., Student Alumni

WELCOME ADDRESS BY THE CHAIRMAN OF THE BOS AND INTRODUCTION OF MEMBERS

The chairman of the BOS Dr. Sanjay Jain (HOD, ECE Department) welcomed all the board members and introduced the scheme and syllabus for B.E 2nd year, M.Tech (VLSI and CMS) 2nd year. He invited all the members for an open discussion on the same.

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AGENDA-1

DISCUSSION ON SCHEME OF B.E COURSES FROM 2ND TO 4TH YEAR

The Scheme was presented by Prof. Aravinda K and the following suggestions are listed.

1. Mr. Nagaraj Subramaniam emphasized on following the Bloom's Taxonomy for assessment part of every course.
2. Dr. B K Sujatha suggested for including 1 credit of theory along with 1 credit of practical for Logic Design Course of III semester.
3. Dr. Preeta Sharan mentioned the requirement of Logic Design Course fundamentals from research point of view.
4. Dr. Prasad proposed the change of course name for HDL course as System Design using Verilog.
5. Dr. B K Sujatha suggested the swapping of Electives for V semester Computer Organization Course under elective-1 with Real time systems of elective-2.
6. Dr. B K Sujatha focused on the Course outcome-Program outcome mapping stating the change of mapping at places showing proper relevance.
7. Dr. Prasad suggested the swapping of Embedded System Design with Optical Fibre Communication for VI semester.
8. Dr. Prasad suggested to teach Digital Signal Processing course with reference to its applications.
9. Dr. B K Sujatha advised to include basics of Wireless Sensor Networks as a module of Wireless Communication course.
10. Dr. B K Sujatha suggested Intellectual property rights course to be included as an elective in VII semester.
11. Dr. Prasad suggested the swapping of RTOS course of VII semester with Embedded Linux of VI semester.
12. Dr. B K Sujatha advised the inclusion of IOT course instead of Adhoc wireless network.

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AGENDA-2

APPROVAL OF B.E. SYLLABUS FOR 2ND YEAR

The syllabus was presented by Prof. Aravinda K and the following suggestions were proposed.

1. Dr. Prasad emphasized focusing more on the system design part thereby learning the Language such as Verilog for HDL course.
2. Mr. Nagaraj emphasized on inclusion of SPICE Simulator for carrying out practical associated with Electronics Circuits Lab highlighting its significance for analog designers.
3. Dr. Sanjay Jain advised the removal of MATLAB block level experiments and introduction of hardware-based and SPICE simulator based experiments.
4. Mr. Nagaraj mentioned the grouping of electives as streams such as VLSI, Communication Systems, Embedded Systems Programming, Signal Processing would provide greater clarity to students.

NEW HORIZON COLLEGE OF ENGINEERING

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2ND BOARD OF STUDIES MEETING

2ND BOARD AGENDA-3

**DISCUSSION ON SCHEME OF M.TECH (VLSI) COURSES FOR 2ND
YEAR**

1. Mr. Nagaraj suggested to prioritize on embedded product design in the curriculum.
2. He also suggested that the VLSI lab should have the complete flow, from RTL till GDS.

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AGENDA-4

**DISCUSSION ON SCHEME OF M.TECH (CMS) COURSES FOR 2ND
YEAR**

1. Dr. B K Sujatha advised to include only reference books for M.Tech courses instead of textbooks.
2. Dr. B K Sujatha highlighted the disintegration of course and lab with different course codes.

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AGENDA-5

APPROVAL OF M.TECH (VLSI) SYLLABUS FOR 2ND YEAR

1. Dr. Preeta Sharan emphasized on including Intelligent Softwares like Commsoft for RF MEMS course.

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AGENDA-6

APPROVAL OF M.TECH (CMS) SYLLABUS FOR 2ND YEAR

1. Dr. Preeta suggested on using FEKO simulator for electromagnetic simulations.

RECOMMENDATIONS OF THE BOARD

Scheme of B.E Courses from 2nd to 4th year

1. Change of course name for HDL course as System Design using HDL focusing more on design and less on the language.
2. Swapping of Electives for V semester Computer Organization Course under elective-1 with Real time systems of elective-2.
3. Swapping of Embedded System Design with Optical Fibre Communication for VI semester.
4. Swapping of RTOS course of VII semester with Embedded Linux of VI semester.

Syllabus for B.E 2nd year

1. Conduction of Network Analysis based experiments using PSPICE laboratory.

Scheme of M.Tech (VLSI) Courses for 2nd year

1. Reference books to be mentioned for M.Tech courses rather than textbooks

Scheme of M.Tech (CMS) Courses for 2nd year

1. Reference books to be mentioned for M.Tech courses rather than textbooks.

Syllabus for 2nd year M.Tech (VLSI)

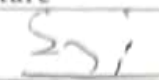

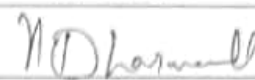


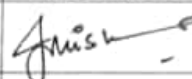
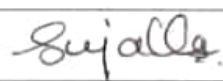

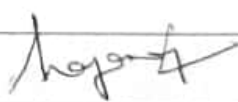

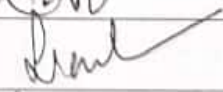

1. Usage of Intelligent Softwares like Commsoft for RF MEMS course.

VOTE OF THANKS BY THE CHAIRMAN-BOS

The meeting concluded with the vote of thanks by the chairman, Dr. Sanjay Jain (HOD, ECE Department). He appreciated the comments from all the experts, faculty and student alumni for their valuable inputs and suggestions.

NAME AND SIGNATURES OF ALL THE ATTENDEES

DATE: 24-06-2016

Sl.No.	Name	Signature
1.	Dr. Sanjay Jain, Chairman-BOE,HOD-ECE, NHCE	
2.	Mr.Aravinda K, Associate Professor, ECE,NHCE	
3.	Ms. Dharmambal, Professor, ECE,NHCE	
4.	Dr. Mohan Kumar Naik, Sr. Associate Professor, ECE,NHCE	
5.	Dr. Nisha KCR, Associate Professor, ECE, NHCE	
6.	Ms. P. Sreeramani, Associate Professor, ECE,NHCE	
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16.	Mr. Praveen Kumar J, M.Tech, e silicon Tech, Student-PG Alumni	
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