### NEW HORIZON COLLEGE OF ENGINEERING

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year 2018-19 Total Hours Engaged: 51 Hours

Visiting Faculty Session Details for the Academic Year 2018-2019

SI. No	Name of the Industrial Expert	Company/ Organizat ion	Designation	Topic /Subject	Contribution to	Sem ester	Hours engag ed	Date
1	Mr. Harish Kumar Villuri	Intel Technologies	Component Design Engineer	CMOS VLSI Design (Timing Analysis)	Delivered Theoretical session for the subject CMOS VLSI Design	V	3	20-09- 2018
2	Mr. A Athif Shan	ABE Semiconducto r Designs	Chairman/ Managing Director	Multi core architecture and communication	Delivered Theoretical session for the subject Microprocessor & Microcontrollers	V	3	13-10- 2018
3	Mr. Arun Krishnan	Harman India	Senior Product Development Engineer	Network Analysis (Module 5: Waveform Synthesis)	Delivered Theoretical session for the subject Network Analysis	Ш	3	25-10- 2018
4	Mr. Prafulla ShyamKant Galphade	Invecas Technologies	Principal Member technical staff	Digital Electronic Circuits (Module 5)	Delivered Theoretical session for the subject Digital Electronic Circuits	III	3	17-10- 2018
5	Mr. Sunder Murthy	LS Control Systems	Proprietor	Microcontroller (Module 5, Interfacing)	Delivered Theoretical session for the subject Microcontrollers	V	3	13-10- 2018
6	Mr. Arun Krishnan	Harman India	Senior Product Development Engineer	Signals and Systems (Module 5)	Delivered Theoretical session for the subject Signals & Systems	1111	3	14-11- 2018
7	Ms Jayachandran Aradhya	Silicon Microsystem,	Chief Executive	Antenna Design	Delivered Theoretical session for the subject	VII	3	14-11- 2018

		Bangalore			Antenna & Wave Propagation			
8	Ms. Poornima Mohanachandran	Ekalakshya	CEO	Analog Electronics Product Development	Delivered Theoretical session for the subject Electronic Devices and Circuits	III	3	07-11- 2018
9	D Suresh	Renesas Electronics	System Analyst	Wireless and mobile communication	Delivered Theoretical session for the subject Wireless Mobile Communication	VII	3	03-11-2018
10	Mr. Sarang Suresh Akotkar, Mr Kumaran Sethu Raman	Intel Technology	R & D Engineer	DSP Processor & Digital Filters	Delivered Theoretical session for the subject Digital Signal Processing	IV	3	16-04- 2019
11	Mr. Shashikanth Patil	Wipro Technology	Senior Project Engineer	Synthesis on FPGA	Delivered Theoretical session for the subject System design using HDL	IV	3	13-04- 2019
12	Mr.Muralitharan	Emids technologies	IOS Developer	Exception Handling	Delivered Theoretical session for the subject Object Oriented Programming	VI	3	04-04-2019
13	Mr.Shriram S	Intel Technologies	Hardware Design Engineer	VLSI-transistor to transformation	General talk in recent advancements in VLSI	IV	3	09-02-2019
14	Mr. Bipin Malhan	Invecas Technologies	Managing Director	Career growth in electronics Industry	General talk about recent innovations and developments in electronics		3	24-10- 2018
15	Mr. Akshatha Pai	Sirena Technologies	Project Manager	Robotics; History, Current trends, future	General talk on current trends in robotics	V	3	22-09-2018
16	Ms. Neha Bharti	Altran	ASIC Design	Pathway to semiconductor	General talk on career	VII	2	18-08-

		Technologies	Engineer	Industry	opportunities in semiconductor industry			2018
17	Mr.Shriram S	Intel Technologies	Hardware Design Engineer	Hardware Design flow for Chip Design	Delivered Theoretical session for the subject CMOS VLSI Design	V	2	11-08- 2018
18	Mr.Shriram S	Intel Technologies	Hardware Design Engineer	Art of Electronics	General talk about product development in electronics- steps and process flow	Ш	2	11-08- 2018

Coordinator

HOD



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### OFFER LETTER

Ref. No.: NHCE/OFFER/077/2018

September 20, 2018

Mr. Harish Kumar Villuri, Component Design Engineer Intel Technologies Bangalore.

Dear, Mr. Harish Kumar Villuri,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2018-19. New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality

The subject allotted to you is CMOS VLSI Design (Module 4: Timing Analysis).

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terrus discussed.

Signature: V. Hasish Kumas

the above lecture (expert) Date: 20/09/2018
on 20/9/18 Forwarded
for hind approval & grelease of payment



## Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

#### OFFER LETTER

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Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms discussed.

Signature: V. Hasish Kuman,

Date: 20 09 2018

S.NO	DET	AILS REQUIRED
1	Name of the Resource person	Harish Kumar Villuri
2	Years of Experience	8 years
3	Currently working in	Intel Technologies, Bangalore
4	Designation	Component Design Engineer
5	Number of Lecture Hours	2
6	Date of Lecture	20/09/2018
7	PAN Number	APIPV9932R
8	Aadhar Number	5020 8454 3468
9	Date of Birth	22-01-1989
10	Educational Qualification	B Tech
11	Bank Name	State Bank of India
12	Account Number	00000020106981121
13	IFSC Code & Branch	SBIN0017042, THUBARAHALLI BRANCH

### NEW HORIZON COLLEGE OF ENGINEERING

**Department of Electronics and Communication Engineering** 

17.09.2018

### **EXPERT LECTURE**

The ECE students are hereby informed that EXPERT lecture is scheduled on

"TIMING ANALYSIS IN VLSI DESIGN" on 20.09.2018.

By Mr. Harish Kumar Villuri Component Design Engineer, Intel Technologies, Bangalore.

Timing schedule is as follows

Course	Timings	Semester
ECE53 CMOS VLSI DESIGN	01.50 pm-03.50 pm	5 -

Note: All Faculty members are invited to attend the same (without affecting regular classes)

Venue: MBA Seminar Hall

93

Faculty In-Charges: Ms. Neethu Johny, Ms. Monika Gupta

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### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING Expert Lecture Attendance V Sem "A" Sec

Course ! Eress (Mos VISI DESIGN

	course :	ECESS CMOS VICI DESIG	114		
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4	1NH16EC004	AFZAL HUSSAIN	EXE	A1	Afral
5	1NH16EC005	AKHIL JOSEF	EXE	A1	- She
6	1NH16EC006	AKHILESH VARMA KALIDINDI	EXE	A1	Adz
7	1NH16EC007	AKSHAY RAO	PROF	A1	-
8	1NH16EC008	ANMOL KUMAR	EXE	A1	
9	1NH16EC009	ANOOP.A	PROF	A1	And
10	1NH16EC010	ANUSHA H N	EXE	A1	344
11	1NH16EC011	ARUN.P	GLOBAL	A1	
12	1NH16EC012	ATHIRA AJAYA KUMAR KULLULLY	EXE	A1	2
3	1NH16EC014	B SRIKANTH REDDY	EXE	A1	Su
	1NH16EC015	BHASKAR CHOUDHURY	EXE	A1	PROCESS .
15	1NH16EC016	BIPIN DIXIT.H	EXE	A1	Bible
16	1NH16EC017	KIRAN C A	EXE	A1	CALINO
17	1NH16EC018	ARUNA C	EXE	A1	( X) m
18	1NH16EC019	C.LEHARIKA	PROF	A1	1
19	1NH16EC020	CHETHAN S	EXE	A1	Po
20	1NH16EC023	DEEPAK KU. PRADHAN	EXE	A2	9
2 21	1NH16EC024	DEEPTHI R	EXE	A2	
122	1NH16EC025	DENNIS VINCENT PAULRAJ	PROF	A2	Samo Parlan
23	1NH16EC028	ELLINKI JAHNAVI	PROF	A2	W 1 2 4 4 9
24	1NH16EC030	G AVINASH	EXE	A2	X 1
25	1NH16EC031	GIRISH JATTU GOUDA	EXE	A2	-Aymach.
26	1NH16EC032	HARI PRASAD.R	EXE	A2	Howal
27	1NH16EC033	HARSH SRIVASTAVA	EXE	A2	1 Hard
18	1NH16EC034	JAGADEESH.D	EXE	A2	The state of the s
يكم	1NH16EC035	JANANI.B.R	EXE	A2	A-BIE
30	1NH16EC036	GIRIVARDHAN K	GLOBAL	A2	12
31	1NH16EC037	KALAMADI SREELEKHA	GLOBAL	A2	MAN
32	1NH16EC039	KIRAN N			Win W
33	1NH16EC040	KISHORE N	EXE	A2	VALARON
34	1NH16EC040		EXE	A2	puono al l
35	1NH16EC041	K. VENKATA MOUNISH REDDY	EXE	A2	W //-
36		KORVI CHANDRA KIRAN REDDY	EXE	A2	ralle au
	1NH16EC043	KUSHI PONNAMMA.K.P	EXE	A2	total of the est
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41	1NH16EC049	MANJULA.S	PROF	A3	Maryus
42	1NH16EC050	MANJUNATH N	PROF	A3	ov A
43	1NH16EC052	MAURYA REDDY	EXE	A3	1 gergoo
44	1NH16EC053	MEGHASHREE H M	EXE	Λ3	Dwh.
45	1NH16EC054	MEGHANA V	EXE	A3	deghana.11.

46	1NH16EC055	MEGHASHREE R	EVE		1
47	1NH16EC056	MISBAH TABASSUM AEJAZ	EXE	A3	Meghushne
48	1NH16EC057	MITHUN.V	EXE	A3	mo
49	1NH16EC058	MOHAMMED ANAS	EXE	A2	Mitten.
50	1NH16EC059	MOHAMMED FAROOQH PASHA	EXE	A3	LA
51	1NH16EC060	MOHIT R	EXE	A3	Seas
52	1NH16EC061	MOHIT SAHU	PROF	A3	THE STATE OF THE S
53	1NH16EC062		EXE	А3	Melitah
54	1NH16EC063	MUJEER PASHA	PROF	A3	- BADAUC
55	1NH16EC083	MAHESHWARI N	+ EXE	A3	11
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59	1NH17EC400	AKASH PRASAD C	EXE	A3	TOURS -
60	1NH17EC402	ARATHI .V	EXE		A C W . )
61	1NH17EC403	BHAVANI . V	PROF	A3 (	Availur
62	1NH17EC412	KIRAN . R		A3	Baar.
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			EXE	A3 .	04/

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### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING Expert Lecture Attendance V Sem "B" Sec

SL.No	USN	NAME	Stream	ВАТСН	Sign
1	1NH16EC064	NEHA MAHESH	EXE	B1	Kleli-
2	1NH16EC066	NISHU DUBEY	EXE	B1	- STARO
3	1NH16EC067	NITESH K	PROF	B1	1 621
4	1NH16EC068	NITHIN.E	EXE	B1	Nich
5	1NH16EC070	MAHALAKSHMI P	EXE	B1	2 wall
6	1NH16EC071	DEEPAK P S	EXE	B1	Despets
7	1NH16EC072	PARITHOSH VEMA	PROF	BI	Par The
8	1NH16EC075	POOJA SHANTARAM NAYAK	EXE	B1	Longues
9	1NH16EC076	PRAJWAL.T.J	PROF	B1	A.A.
10	1NH16EC077	PRANAY REDDY S	EXE	B1	29/20
11	1NH16EC078	PRASHANTH GOWDA.R.S	PROF	B1	Balindhills
12	1NH16EC079	PRAVEEN KUMAR S	PROF	B1	Quel
13	1NH16EC080	PRESHIKA J M	EXE	B1	preshika
14	1NH16EC081	PRITHIPA A	EXE	BI	Olliel
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16	1NH16EC083	RAJVARDHAN	EXE	B1	Ref
17	1NH16EC084	RAKSHITHA N	EXE	B1	D-4
18	1NH16EC085	ROSHINI.M	EXE	B1	Rosh En
19	1NH16EC086	RUSHAB.S.G	EXE	BÍ	(D)
20	1NH16EC087	SAI KIRAN	EXE	B2	
21	1NH16EC088	SAI PRASHANTH	PROF	B2	\$ 6
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23	1NH16EC090	SAGAR	EXE	B2	aux
24	1NH16EC092	SANNAPANENI PAVANI	GLOBAL	B2	W /2
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27	1NH16EC095	SHASHANK B	EXE	B2	10
28	1NH16EC096	SHRAVIN.R.SEKHAR	EXE	B2	0
29	1NH16EC097	SHRIPAD AITHAL	EXE	B2	+
30	INH16EC098	SIDDESH JALAGERI	EXE	B2	
31	1NH16EC099	JAGAN MOHAN REDDY	EXE	B2	Fort
32	1NH16EC100	SIDRAMAPPA	EXE	B2	Masabhaa
33	1NH16EC101	SINDHU C R	EXE	B2	Condlan
34	1NH16EC102	SIVA CHALLA	EXE	B2	dylow
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38	1NH16EC107	T.SAI DEEPTHI	EXE	B2	DecPthe

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41	1NH16EC112	VEENA K	EXE	В3	
42	1NH16EC113	VIDYA VISHNU +	PROF	В3	
43	1NH16EC114	VIJAY C	EXE	В3	
44	1NH16EC115	VIJAY KUMAR C	EXE	В3	
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56	1NH16EC132	PREM KUMAR +	EXE	В3	1
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58	1NH17EC401	AKSHAY GAMBHIR SADALAGI	EXE	В3	-
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61	1NH17EC426	SACHIN . M	EXE	В3	
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62	1NH17EC427	SUSHMITHA	EXE	В3	Dignillo

HOD - ECE

**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRÔNICS & COMMUNICATION ENGINEERING Expert Lecture Attendance V Sem "C" Sec

SL.No	ÚŠN	NAME 1	Stream	BATCH	Sign
1	1NH14EC001	Abhijith S Raj	EXE	C3 *-	-
2	1NH14EC717	KEERTHANA A	EXE	C3	
3	1NH14EC726	P Likith Chowdary	EXE	- C3	Alex M.
4	1NH15EC007	A VIKRAM KUMAR REDDY	EXE	C3	CATAIN OF US
5	1NH15EC024	HARSHA E	EXE	C1	TN s
6	1NH15EC072	Pulibandla Harish	EXE	C2	
7	1NH15EC075	R RAGHAV SRIVATSAV -	PROF	C3	
8	1NH15EC093	Sare Sai Sandeep	EXE	C2	4
9	1NH15EC434	SUDHANYA S	EXE	C2	
10	1NH15EC701	ABHISHEK VP	EXE	CI	•
11	1NH15EC707	Deepak Tomslee	EXE	C1	1
12	1NH15EC715	Manoj Chandrashekar	EXE	C1	
13	1NH15EC717	Venkatasai Balakrishna	EXE	C2	
14	1NH15EC720	Pavan Kumar S	EXE	C3	1
15	1NH15EC736	RISHAV PAUL	EXE	C3	
16	1NH15EC747	Shreyas.H	EXE	C2	
17	1NH15EC759	Vishnu.N.U	EXE	C2	
18	1NH16EC430	Vijay Kumar Y Valmiki	Prof	Cl	. 1 2
19	1NH16EC702+	ABHISHEK KHOT	EXE	C3	(A)+
20	1NH16EC703	ABHISHEK PATTAJOSHI	EXE	C3	Moderation
21	1NH16EC704	ABŲ BAKAR SIDDIQ	EXE	C3 -	Q.
22	1NH16EC705	AK\$HAY.V	EXE	C3	- 30 .
23	1NH16EC706	AMMIDAL VISHNU VIKAS	EXE	C3	of of
24	1NH16EC707	ANITA CHAUHAN	EXE	C3	4
25	1NH16EC708+	ANJU GOPINATH	EXE	C3	-
26	1NH16EC709+	ASHŪTOSH MANISH	PROF	C3	John Se
27	1NH16EC710	B VAMSHI KRISHNA REDD¥	EXE	C3	38
28	1NH16EC711	ВАГАЛ Г	PROF	C3	
29	1NH16EC713	CHIRAG S	EXE	C3	-
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34	1NH16EC718	JEEVAN.K	EXE	C3	Teavon
35	1NH16EC719	MITHUN KUMAR *	PROF	C2	1
36	1NH16EC720	KIRAN SAGAR N P	EXE	C2	
37	1NH16EC722	LINGRAJ JAMKHANDI	PROF	C2	() Jagos
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45	1NH16EC730	MOUNIKA E	EXE	C2	
46	1NH16EC731	THILAK PRASAD	PROF	C2	
47	1NH16EC733	NAWAZ KHAN	PROF	C2	Naya
48	1NH16EC734	NIŞHA ANANDU NAIK	EXE	C2	Quaik
49	1NH16EC735	NIVEDITHA N	EXE	C2	
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51	1NH16EC738	PAVITHRA.N	PROF	C2	Paristran
52	1NH16EC739	PAWAN KUMAR SRIVASTAVA	ĚXE	C2	
53		POOJA P CHOUHAN +	-		Paluca
54	INH16EC741		EXE	C1	
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55	1NH16EC742.		PROF	C2	Kachane
56	1NH16EC743	K RAGHAVENDRA	EXE	C2	Roge
57	1NH16EC744	RAMYA.R	EXE	C2	
58	1NH16EC745	RĘDDY SHEKAR.B.S	EXE	C3	Reddy
59	1NH16EC746	RUCHIKA PRADEEP	PROF	Cl	Pruchika
60	1NH16EC747	S.SARAVANA	EXE	C3	Bull
61	1NH16EC748 +	S SHYAM	EXE	C3	
62	1NH16EC749	SHRIYA.Ģ	EXE	C1	
63	1NH16EC750	SINDHU A	PROF	C1	2 mlin
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65	INH16EC752	SWATHI K	EXE	CI	Santhick
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67	1NH16EC754	TARUN SAI REDDY +	EXE	C3	
68	1NH16EC755	ULTHI KEDARNATH	EXE	C1	1
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74	1NH17EC405	B . N CHITHRASREE			
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77	1NH17EC411	B.J.KOWSALYA	EXE	C1	
78	1NH17EC415	MANASA . C	EXE	C1	
79	1NH17EC416	MANOJ.R	ĖXE	CI	removio
80	1NH17EC418	NAVEEN KUMAR . J	EXE	C1	-
81	1NH17EC421	RAGHU . R	EXE	C1	
82	1NH17EC422	RAHUL JAIN	EXE	C1	Ciain
83	1NH17EC424	RAKESH KUMAR . T	EXE	C1	
84	1NH17EC431	VASANTH.R	EXE	C1	10gant

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#### NEW HORIZON COLLEGE OF ENGINEERING, BANGALORE

(Autonomous Institution affiliated to VTU, Accredited by NBA & NAAC with Grade 'A')

#### Department of Electronics and Communication Engineering

#### Report on Expert Lecture for CMOS VLSI Design

The Department of Electronics & Communication Engineering organized an expert lecture for the course CMOS VLSI Design by Mr. Harish Kumar Villuri, Component Design Engineer, Intel Technologies, Bangalore on 20<sup>th</sup> September 2018. The session dealt with 'Timing Analysis in VLSI Design'.

The lecture started off with an introduction which was about the basics of MOSFET. He explained about the different types of MOSFET including its advantages and applications.

After the introduction, he explained about the design flow in VLSI where he mentioned that VLSI design flow is a very solid and mature process. The overall VLSI design flow and the various steps within the VLSI design flow have proven to be both practical and robust in multi-millions VLSI designs until now.Next, he explained about analog design flow and digital design flow. A comparison was made between both the design flows which gave us a clear idea about both the processes and how one differed from the other.He then talked about the Cadence software and the different tools that it uses.

The next topic was latches. A brief introduction was given about latches where he explained that a latch is an example of a bistable multivibrator, that is, a device with exactly two stable states. These states are high-output and low-output. A latch has a feedback path, so information can be retained by the device. Therefore latches can be memory devices, and can store one bit of data for as long as the device is powered. He took an example of D latch and explained the working of it. He then explained about flipflops and took an example of D flipflop to explain the working of it. To understand the working of flipflops and latches he explained about level triggered and edge triggered process and how one varies from the other.

Timing constraints, being an important topic in VLSI, was explained next. He talked about how the designer creates timing constraints for synthesis which are a series of constraints applied to a given set of paths or nets that dictate the desired performance of a design.

The next topic was Setup and Hold time. Setup time is defined as the minimum amount of time before the clock's active edge that the data must be stable for it to be latched correctly. Any violation may cause incorrect data to be captured, which is known as setup violation.

Hold time is defined as the minimum amount of time after the clock's active edge during which data must be stable. Violation in this case may cause incorrect data to be latched, which is known as a hold violation.

He then spoke about Unate where he explained about its three types with an example. In inverter having input A and output Y, Y is negative unatewith respect to A. Positive unate means cell output logic is same as that of input. For example, a clock that passes through an XOR gate is not unate because there are non unate arcs in the gate.

He concluded the seminar with a brief explanation about the fabrication process in VLSI and how the chips are manufactured.

It was a very informative session where we understood many topics in detail. As it was an interactive session, many students took up the opportunity to put forth their points.

We are thankful for the college for conducting such an informative session which was very useful and has given us so much knowledge.



#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic): VLSI youst Lecture

Instruction: Please put a tick mark at the appropriate column.

DATE: 22-09-18

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Average	=		-		
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Comments/Remarks/Suggestions:

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Sai Sander Name & USN No.(optional)

NH 1560 093 Signature (optional)

NAME OF THE EVENT/CLUB: Guest Lee here on VISI

FEEDBACK ON (Mention the topic): Stark timing Analysis DATE: 2209/18
Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	V		~		~
Average	A			7	- ,
Satisfactory					

Comments/Remarks/Suggestions:

1. was useful

2. Would be more effective

3.

101

Name & USN No. (optional) Bhoomika T-R IN1+16EC #53

Signature (optional) Bhoomika TR

NAME OF THE EVENT/CLUB: Guest Lecture on VISI

FEEDBACK ON(Mention the topic):

DATE: 22/07/18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Good	✓ n	/	<b>\</b>	~/ =	/
Average	1	2 1			
Satisfactory		,			

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Comments	/Kema	rks/Supp	estions:

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2. Fot ideas on d'ming analysts 3. It could be nove effective

Name & USN No. (optional) Nivedilla. R, 1 N 1416EC#36

Signature (optional) Wreditia. R

NAME OF THE EVENT/CLUB: QUEST LECTURE ON VLSI

FEEDBACK ON (Mention the topic): Time Amolysis Instruction: Please put a tick mark at the appropriate column.

DATE: 22-09-18

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good		<u></u>			
Average	14	7 ::			
Satisfactory	1		у 2		

Comments/Remarks/Suggestions:

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2. would be made mose interesting by including more x visuals than speech

Name & USN No. (optional) MOUNIKA. E[1NHIGEC730]

NAME OF THE EVENT/CLUB: \( \struct \) \$\( \tau \)

FEEDBACK ON(Mention the topic): TIMING ANALYSIS DATE: 22 09/18
Instruction: Please put a tick mark at the appropriate column.

	Olevites	Visuals	Skills	Guidance	Overall
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Excellent			3-		
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Average	1		1		
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Comments/Remarks/Suggestions:

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Name & USN No.(optional) SHRIYA. Gr (INH16 EC 749)

NAME OF THE EVENT/CLUB: Guest leture (VLSI)

FEEDBACK ON (Mention the topic): TIMING ANALYSIS (Module 4) ATE: 22/9/18
Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent		2			
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Comments/Remarks/Suggestions:

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Name & USN No. (optional) SWATHIK

(INHIGEC752)

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON (Mention the topic): guest lecture on VLS DATE: 23/9/18
Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Name & USN No. (optional) M. Anisha

NAME OF THE EVENT/CLUB: Cruest Lecture On VLSI

FEEDBACK ON(Mention the topic):

DATE: 22 09 18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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NAME OF THE EVENT/CLUB: VLS1 quest Lecture

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DATE: 20/9/18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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NAME OF THE EVENT/CLUB:

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FEEDBACK ON(Mention the topic):

DATE: 22 09 18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good					
Average					
Satisfactory	K	,	- x - 3		

Comments/Remarks/Suggestions:

- Lecture was not so clear, however, some points cure good
- The visuals are not exploshe mark
- 3.

Name & USN No. (optional) D. Jagadeesh 4 (NH16 6 CO34



NAME OF THE EVENT/CLUB:	Expund CMOS	VLSI
FEEDBACK ON(Mention the topic Instruction: Please put a tick mark	e): A description of the appropriate column.	DATE: 20/18.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent		15			3,0144
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Satisfactory	9	7		13	

Comments/Remarks/Suggestions:

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#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic):

DATE: 20 9 18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Satisfactory	3 1				1/

Comments/Remarks/Suggestions:

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Name & USN No.(optional)

A Anil Bhotath, Signature (optional)

NAME OF THE EVENT/CLUB:

Guest Lecture Intel - VLS!

FEEDBACK ON(Mention the topic):

DATE: 20/9/18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	V		· /		
Average	-	V			
Satisfactory	9, =		, ,		

Comments/Remarks/Suggestions:

- 1. Text was not visible on the shide...
  2. Covered a lot in less span of time ...
  3. Went in depth, tough to grasp...

Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Expert Lecture du CMOS VLSI Design

FEEDBACK ON (Mention the topic): Expert Letwie (VLSI) DATE: 20 9 18 Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					Overan
Good					
Average	-				
Satisfactory					

Comments/Remarks/Suggestions:

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Name & USN No. (optional) Meglia Stree HM INH16E(053

on (mos VLSI'

DATE: 20 9/8 NAME OF THE EVENT/CLUB: Expert lactuse

FEEDBACK ON(Mention the topic):

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Good	1	7			1
Average					Y
Satisfactory					

Comments/Remarks/Suggestions:

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Name & USN No.(optional) IN 416 EC023



NAME OF THE EVENT/CLUB: Grues? Lecture on CMOS VLSI

FEEDBACK ON(Mention the topic):

DATE: 20/9/18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Average					
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Comments/Remarks/Suggestions:

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NAME OF THE EVENT/CLUB: PART Expert Lecture

FEEDBACK ON(Mention the topic):

DATE: 20.09.18

Instruction: Please put a tick mark at the appropriate column.

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NAME OF THE EVENT/CLUB: EXPENT LECTURES

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Instruction: Please put a tick mark at the appropriate column.

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Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Comments/Remarks/Suggestions:

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NAME OF THE EVENT/CLUB:

Expert

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DATE: 29-09-18

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Rating	Clarity	Visuals	Skills	Guidance	Overall
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Comments/Remarks/Suggestions:
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121 2.

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Name & USN No.(optional)

Expert lecture of CMDS VISI NAME OF THE EVENT/CLUB:

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DATE: 20 9 18

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Name & USN No.(optional) Meghana. V
Signature (optional) Meghana. V

### NAME OF THE EVENT/CLUB:

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#### NAME OF THE EVENT/CLUB:

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NAME OF THE EVENT/CLUB: VLGI

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DATE: 24 09 18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Good		,			
Average					
Satisfactory					

Comments/Remarks/Suggestions:

1. He was not analyte to all,

2. He was not interacting with students, but not somuch. 3.

Name & USN No.(optional)

### NAME OF THE EVENT/CLUB:

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DATE: 94/09/18,

Instruction: Please put a tick mark at the appropriate column.

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#### NAME OF THE EVENT/CLUB:

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# Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

#### **OFFER LETTER**

Ref. No.: NHCE/OFFER/096/2018

October 13, 2018

Mr. A Athif Shan Chairman/ managing Director ABE Semiconductor Designs Bangalore.

Dear, Mr. A Athif Shan,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2018-19. New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality education.

• The subject allotted to you is Analog Communication (Module 5).

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms disc

Signature .

Date: 3. 08-2018

s.no	DET	AILS REQUIRED
1	Name of the Resource person	A ATHIF SHAH
2	Years of Experience	15+ years
3	Currently working in	ABE Semiconductor Designs
4	Designation	Chairman/Managing Director
5	Number of Lecture Hours	3 Hours
6	Date of Lecture	13/10/2018
7	PAN Number	ASQPA0738F
	Aadhar Number	325242807477
9	Date of Birth	18/08/81
10	Educational Qualification	ME
11	Bank Name	HDFC BANK
12	Account Number	50200008253292
13	IFSC Code & Branch	HDFC0000441 CHENNAI-29

NOSETTEMENT OF THE PROPERTY OF



Department of Electronics and Communication Engineering
11.10.2018

### EXPERT LECTURE

The ECE students are hereby informed that **Expert Lecture** is scheduled on

"Multi Core Architectures and Communication"
On 13.10.2018.

By A.ATHIF SHAH,
Chairman/Managing Director,
ABE Semiconductor Designs, Chennai

Timing schedule is as follows

S. No	Timings	Semester
1	10.00 AM – 01.00 PM	5 <sup>th</sup> (A, B&C)

Respective subject teachers are hereby informed to be present in the Seminar Hall during the schedule.

Venue: Class Room (A214, A215 & A216)

Note: All Faculty members are invited to attend the same (without affecting regular classes)

Expert Lecture Faculty In charge

HoD - ECE

### New Horizon College of Engineering, Bangalore Department of Electronics and Communication Engineering

**Expert Lecture Attendance** 

Course Name

: Analog Communication

Course Code: ECE 51

Resource Person

: Mr.A.ATHIF SHAH

Topic

: Multicose communication

Module

: : Unit 5 - Noise

Sem / Sec

: : V 'A'

Date:

: 13.10.2018

Time:10.00 AM - 01.00 PM

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0	1NH16EC006	AKHILESH VARMA KALIDINDI	years.
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### New Horizon College of Engineering, Bangalore Department of Electronics and Communication Engineering

**Expert Lecture Attendance** 

Course Name

: Analog Communication

Course Code: ECE 51

Resource Person

: Mr.A.ATHIF SHAH

Topic

: Multi core communication

Module

: Unit 5 - Noise

Sem / Sec

: V 'B'

Date:

: 13.10.2018

Time:10.00 AM - 01.00 PM

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51	1NH16EC125	YESHWANTH.M.L	Yey
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### New Horizon College of Engineering, Bangalore Department of Electronics and Communication Engineering

**Expert Lecture Attendance** 

Course Name

: Analog Communication

Course Code: ECE 51

Resource Person

: Mr.A.ATHIF SHAH

Topic

: Multicore Communication

Module

: : Unit 5 - Noise

Sem / Sec

: : V 'C

Date:

: 13.10.2018

Time:10.00 AM - 01.00 PM

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Faculty Incharge

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### **Department of Electronics and Communication Engineering**

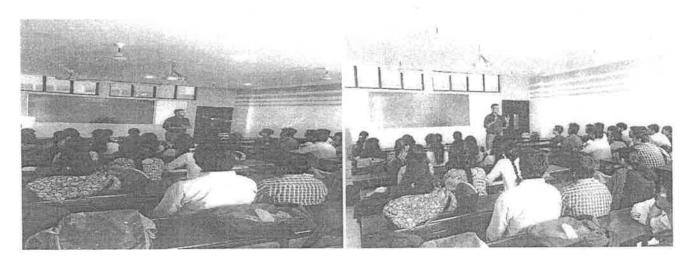
Course code / Name: ECE51- Analog Communication

### Report on Expert Lecture

-A .Athif Shah, Chairman and Managing Director, ABE Semiconductors Design, Chennai.

Dr.A.Athif Shah is a speaker having 15+ years of experience in embedded systems, embedded web servers etc. He is a specialist in Embedded Linux and android development. Currently he is working on ARM based SOC in mobile applications. He enlightened the day by introducing himself and he is currently working on AI. He also claimed that he owns 49 patents in IoT.He started the talk by asking "how to integrate GPS tracker on our ID card without the knowledge of an individual". Many answers and suggestions were poured regarding the batteries, size etc. But he answered by telling us that using nano solar cells and lithium ion battery of a small size we can achieve this.

Further, he asked the difference between microprocessors and microcontrollers. Many of the students were giving answers based on the text book descriptive. But he wanted a practical example of it. Microcontroller has a limited application but has faster operation like a normal food meal but a processor has large no of applications but the speed is reduced like a buffet meal. By this he made us clear that we should be able to learn things with practicality.



Then he started the discussion on DSP. We use this processing for converting analog to digital and vice versa for all audio, video etc wherever the signals are concerned. The industry is now working on SOC (system on chip) like Qualcomm. Older days the phones were having two processors and these days all processors, camera, dsp unit etc are all on one chip. He started the communication systems part where people are working on Cognitive Radio's. The technology is basically switching between any cell tower and any cell carrier for a faster cell reception.

Other countries are now working on 7.5G. IoT is used for collecting our surrounding data and processing it for an optimal solution for the people. It is interaction between humans and machine's using technology. Machine learning comes where everyday data is stored and the machine learns things by itself to perform its task appropriately.

As a electronics engineer, for a step towards learning loT and machine learning he suggested us to start learning about Raspberry Pi or PICON. Tensor flow is an application used for AI and machine learning. It is a open source and an interesting thing to study. And for communication we should start researching on Cognitive radios. And he concluded saying this is whole process of a product.

Further, he was open for questions. Students asked few questions relating to the technology on the used in various applications he spoke. He advised us to learn things by starting with examples and then the theory for a innovative and enjoyable classes. He told us to learn AI and machine learning using a mentor and we should go slowly. He also told he can mentor students with interest which was a good advantage for all of us.

The session ended with a token of thanks. The guest lecture was a great use to brush up our brains from his inspirational talk apart from our regular and mundane lectures.

Expert Lecture Faculty incharge

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HoD



Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

### OFFER LETTER

Ref. No.: NHCE/OFFER/108/2018

October 25, 2018

Mr. Arun Krishnan Senior Product Development Engineer Harman India Bangalore.

Dear, Mr. Arun Krishnan,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2018-19. New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality education.

• The subject allotted to you is Network Analysis (Module 5:, Waveform

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms discussed.

Signature: <u>Arunk</u>
Date: 30/10/2018.

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### NEW HORIZON COLLEGE OF ENGINEERING

**Department of Electronics and Communication Engineering**26.10.2018

### EXPERT LECTURE

The ECE students are hereby informed that an expert lecture on Network Analysis (ECE35) for

"Waveform Synthesis"

is scheduled on

30.10.2018.

By

### Mr. Arun Krishnan

Senior Product Development Engineer, Harman Connected Services, Bangalore.

Timing schedule is as follows

S. No	Timings	Semester
1	9.00 AM-11.00 AM	3 A,B,C

Respective subject teachers are hereby informed to be present in Chanakya Seminar Hall during the session.

Note: Interested Faculty members are invited to attend the same (without affecting regular classes)

Venue: Chanakya Seminar Hall

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Expert Lecture Faculty in Charge

HOD ECE

S.NO	DETAILS REQUIRED		
1	Name of the Resource person	Arun Krishnan	
2	Years of Experience	7 years	
3	Currently working in	Harman India	
4	Designation	Senior Product Development Engineer	
5	Number of Lecture Hours	2	
6	Date of Lecture	30/10/2018	
7	PAN Number	CRCPK5394Q	
8	Aadhar Number	403169473446	
9	Date of Birth	10/04/1991	
10	Educational Qualification	B.tech in Electronics and Communication	
1:1	Bank Name	HDFC Bank	
12	Account Number	50100195894875	
13	IFSC Code & Branch	HDFC0004386,B.M.E.L Gate Circle, Bangalore	

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III Sem "A" Sec

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3	1NH17BT002 1NH17BT005	ANISHA DEVI	4misha
4		BATHULA SRI SAI KRISHNA	B. Saikroh
5	1NH17BT007	GANGADHARA VARSHITHA	12atola >
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7	1NH17BT023	SAI KARTHIK P R	Sal Korthon
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24 -	1NH17EC015	BOREDDY NAGA MAHESWAR REDDY	M. C.
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### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "B" Sec

EXPERT LECTURE ATTENDANCE ON 3

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# NEW HORIZON COLLEGE OF ENGINEERING, BANGALORE (Autonomous Institution affiliated to VTU, Accredited by NBA & NAAC with Grade 'A') Department of Electronics and Communication Engineering

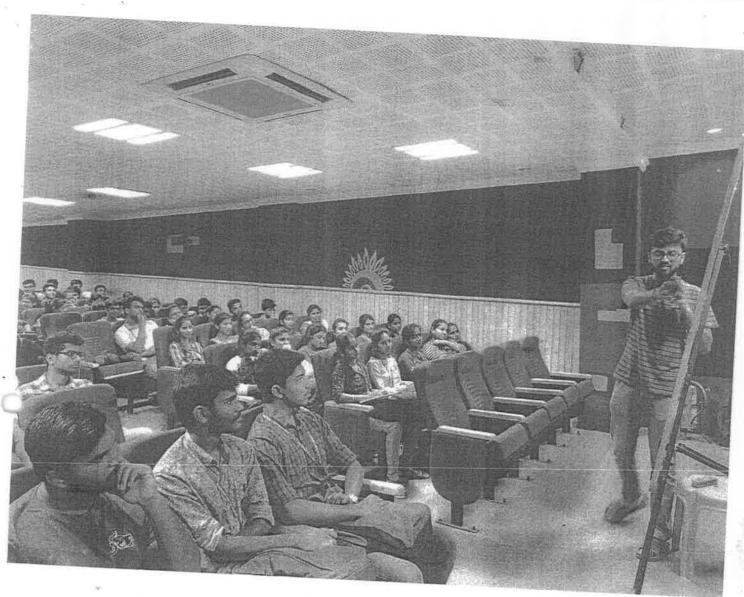
### Expert lecture report on waveform synthesis (Network Analysis ECE35)

The speaker of today's lecture was Mr. Arun Krishnan. He was very informative. The lecture was held at Chanakya seminar hall at New Horizon College of Engineering. Mr. Arun Krishnan works at the Harman company under the domain of digital signal processing (DSP). He introduced the students about practical applications of topics related to our core subjects. In the lecture, the guest detailed the students about the various types of signals present around them. He very much related subjects like network analysis, signals and systems, digital electronics in practical use. He explained us about applications of fourier series and fourier transform in the field of signal processing. Hebriefed us about audio, video, speech signals and their necessities and effectsin today's era.

He told about the different effects about audio signals and gave the students a brief knowledge about different types of audio compressions existing. He made the students understand the importance of audio compression. He also made the students understand the various types of audio compressions that are existing like MP3, WMA etc. He also explained about the importance of noise cancellation and the video processing. Image processing was also briefed about. He made us analyse the difference between audio, video, image processing signals. He explained the importance and the conversion of time domain signal to frequency domain signal, this got us a clear cut idea about many topics related to the course Signals and Systems.

He also introduced the studentsabout the automotive processing where his major sub-domain lies under. In DSP, Automotive processing includes engine sound system and engine noise cancellation. He also briefed the students about the importance of DSP in machine learning. He explained the evolution of speech synthesis in the field of artificial intelligence(AI).

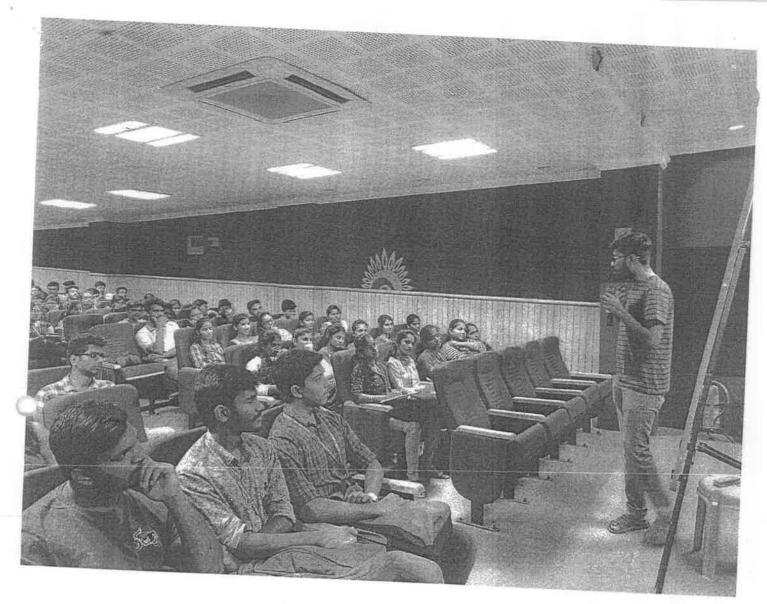
Hence I conclude thatthe lecture was very helpful and helped the students gain a lot more interest in this topic. It was very explanatory and relatable. It made the students understand various topics practically.

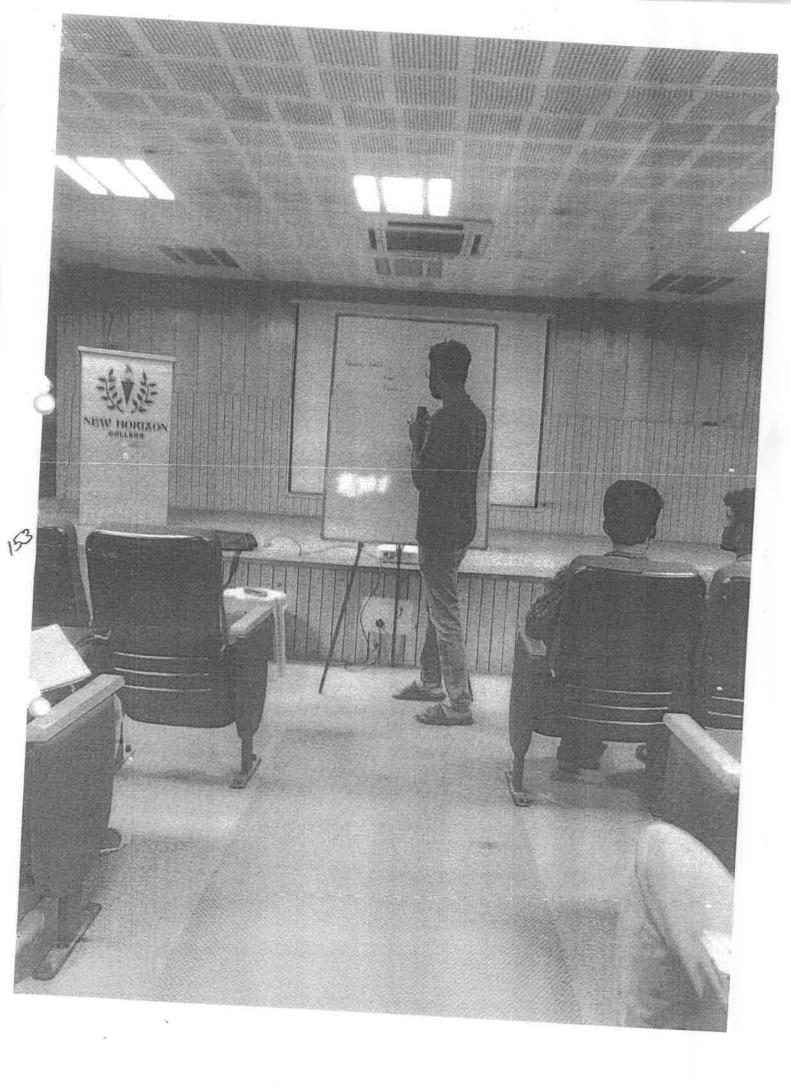


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Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

### OFFER LETTER

Ref. No.: NHCE/OFFER/106/2018

October 17, 2018

Mr. Prafulla Galphade Principal, Member Technical Staff, Invecas Technologies Bangalore.

Dear, Mr. Prafulla Galphade,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2018-19. New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality

The subject allotted to you is Digital Electronics Circuits (Module 5).

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms discussed.

Signature:

Name of the Resource pe	DETAILS REQUIRED
Experience	Prafulla Galphade
Currently working in	18 Years
Designation	Invecas Technology Pvt Ltd
Number of Lecture Hours	PMTS PMTS
Date of Lecture	3 hrs
PAN Number	31.10.18
Aadhar Number	AGRPG8149H
Date of Birth	885393508788
Educational Qualification	28/08/1971
Bank Name	MBA, M. Tech
Account Number	
GSC Code & Branch	State Bank of India 31380707026

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Department of Electronics and Communication Engineering 27.10.2018

### EXPERT LECTURE

The ECE students are hereby informed that the **Expert Lecture** is scheduled for

"Digital Electronics Circuits- Finite State Machine" on 31.10.2018

By Mr. Prafulla Galphade, Principal Member Technical staff, Invecas Technologies, Bengaluru, India.

Ş. No	Timings	Semester
1	9 AM – 12 PM	3 <sup>rd</sup> (A, B & C

Venue: Falconry Seminar Hall

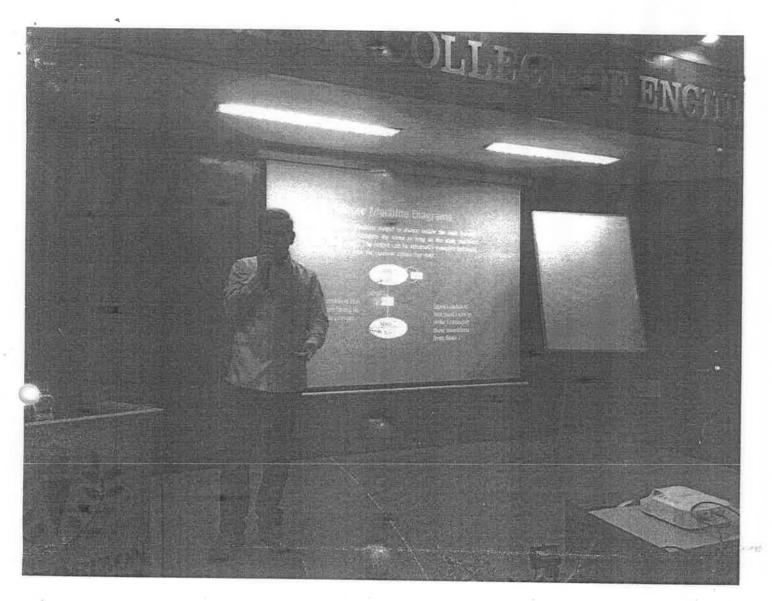
Note: All Faculty members are invited to attend the same (without affecting regular classes)

Faculty In charge:

1. Dr. Sanjeev Sharma 2. Ms. Susmitha .A 3. Ms. Rajini

HoD/ECE





### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "B" Sec

SL.No	USN	NAME	
1	1NH17EC060	NISHMITHA C SHETTY	Minin
2	1NH17EC062	NITHIN R KALLIGUDI	MITALL
3	1NH17EC063	PALAGIRI JYOTHI KRISHNA	pskrishua
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70	Diploma	Akhilesh Venkappa Kshatri	
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02	Diploma	Srilakshmi N	Silve

### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "A" Sec

31/10/2018

SL.No	USN	NAME	
_ 1	1NH16EC038	KARROTU PRUDHVI	
2	1NH17BT002	ANISHA DEVI	Amich a
3	1NH17BT005	BATHULA SRI SAI KRISHNA	80 Frash
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or showy2 10/18

NAME OF THE EVENT/CLUB: DIGITAL GUEST LECTURE

FEEDBACK ON (Mention the topic): FINITE STATEMA DATE: 31 10 18.

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	1/				1/
Average	7				
Satisfactory	-				

Comments/Remarks/Suggestions:
1. INFORMATIVE SESSION BASED ON OUR SYLLABUS

2.

3.

Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Digtal Electronics Explit lectro

FEEDBACK ON(Mention the topic): ₹5™

DATE: 31 1018

Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

1. Rupeout the topic in class.

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Name & USN No.(optional)

#### **EEEDBACK FORM** DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING NEM HOBISON COTTECE OF ENGINEERING

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON (Mention the topic): Moove & modynolong DATE: SI 10 (19)

Overall	Guidance	Skills	Visuals	Clarity	Rating
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Comments/Remarks/Suggestions:

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#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic):

Instruction: Please put a tick mark at the appropriate column.

DATE: 31-10-18

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Comments/Remarks/Suggestions:

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Vorun P

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic):

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Rating	Clarity	Visuals	Skills	Guidance	Overall
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Comments/Remarks/Suggestions:

Good Esplanation with esamples

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Name & USN No. (optional) RAGHAVA INHITE COTS

NAME OF THE EVENT/CLUB:

D&B SE DIGITAL SCHANAR

FEEDBACK ON(Mention the topic): FSM.

DATE: 3/ 10/2018

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					Overage
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Average					
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Comments/Remarks/Suggestions:

1. Could to have made it much more intellsting and interactive and engaging for the students.

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Name & USN No.(optional)

NAME OF THE EVENT/CLUB: /

FEEDBACK ON(Mention the topic):

Instruction: Please put a tick mark at the appropriate column.

DATE: 31/10/18

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Comments/Remarks/Suggestions:

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Name & USN No.(optional)

Salmya Prasad [18EC 422]
Signature (optional)

NAME OF THE EVENT/CLUB: Expert Guest Lecture

FEEDBACK ON (Mention the topic): FSM

DATE: 31/10/18

Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

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#### NAME OF THE EVENT/CLUB:

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Sonjay P
Signature (optional)

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic): DEC Seminar - 5th Module DATE: 31/10/18
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Signature (optional)

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#### NAME OF THE EVENT/CLUB:

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Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

- mments/Remarks/Suggestions:
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Name & USN No.(optional)

Signature (optional)

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON (Mention the topic): Finsh Shah Manine DATE: 31/10/18. Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

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2. No place to sit.

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Name & USN No.(optional)

#### NAME OF THE EVENT/CLUB:

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Name & USN No. (optional) NAVAJITH R REDDY

(IN H 17E C055)

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FEEDBACK ON (Mention the topic): PSM Instruction: Please put a tick mark at the appropriate column	ın.	DATE:	31/10/18

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### Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

#### OFFER LETTER

Ref. No.: NHCE/OFFER/097/2018

October 13, 2018

Mr. L Sunder Murthy No.20, Gangamma Layout Jalahalli Post, Bangalore – 560013.

Dear, Mr. L Sunder Murthy,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2018-19. New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality education.

The subject allotted to you is Microcontroller (Module 5: Interfacing).

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms discussed.

Signature:

Date: 10/11/2018

S.NO	DETAI	LS REQUIRED
1+	Name of the Resource person	L Sunder Murthy
2	Years of Experience	26 years
3	Currently working in	LS Control Systems,
4	Designation	Proprietor
5	Number of Lecture Hours	2
6	Date of Lecture	25/10/2018
7	PAN Number	AFHPM 2398M
8	Aadhar Númber	557358936922
9	Date of Birth	30-08-1969
10	Educational Qualification	BE, Electrical and Electronics Engineer
11	Bank Name	Syndicate Bank
12	Account Number	04442010027759
13	IFSC Code & Branch	IFSC SYNB0000444 Gangamma circle branch



Department of Electronics and Communication Engineering 25.10.2018

### **EXPERT LECTURE**

The ECE students are hereby informed that the **Expert Lecture** is scheduled on

"Microcontroller 8051 Interfacing"

On 10.11.2018.

By L Sunder Murthy,
Proprietor, LS Control Systems
, Bengaluru

S. No	Timings	Semester
1	9 AM -11.00 AM	5 <sup>th</sup> (A, B & C)

Respective subject teachers are hereby informed to be present in the Seminar Hall during the schedule.

Venue: Chanakya seminar hall

Note: All Faculty members are invited to attend the same (without affecting regular classes)

Faculty In charge:

1. Ms. Nayana G H, Ms Rajani, Ms Parul

HoD/ECE

### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "A" Sec

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4	1NH16EC041	K. VENKATA MOUNISH REDDY	EXE	A2	1.8.18	60
5	1NH16EC042	KORVI CHANDRA KIRAN REDDY	EXE	A2	1.8.18	Cha
6	1NH16EC043	KUSHI PONNAMMA.K.P	EXE	A2	1.0.10	
7	1NH16EC044	LALITHAMBHA S M	EXE	A2	17.10.18	Jalofhamkly
3	1NH16EC046	SACHIT M	GLOBAL	A3	6.8.18	Colette
7	1NH16EC047	MADAN GOWDA.M	PROF	A2	5.0.10	la la
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2	1NH16EC050	MANJUNATH N	PROF	A3	30.8.18	Mario
3	1NH16EC052	MAURYA REDDY	EXE	A3	24.7.18	manyh
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5	1NH16EC054	MEGHANA V	EXE			- There was

46	ıNH16EC055	MEGHASHREE R	EXE	A3	7.8.18
47	1NH16EC056	MISBAH TABASSUM AEJAZ	EXE	A3	
48	1NH16EC057	MITHUN.V	EXE	A2	25.7.18
49	1NH16EC058	MOHAMMED ANAS	EXE	A3	
50	1NH16EC059	MOHAMMED FAROOQH PASHA	EXE	A3	2.8.18
51	1NH16EC060	MOHIT R	PROF	A3	
52	1NH16EC061	MOHIT SAHU	EXE	A3	
53	1NH16EC062	MUJEER PASHA	PROF	А3	
54	1NH16EC063	MAHESHWARI N	EXE	A3	
55	1NH16EC135	AKULA ANIL BHARATH	EXE	A3	
56	1NH16EC137	ANUPOJU SAI VENKAT THRIMURTHY	GLOBAL	A3	
57	1NH16EC138	ASHISH KUMAR . S	PROF	А3	
58	1NH16EC139	KARTHIK . K. S	EXE	А3	30.7.18
59	1NH17EC400	AKASH PRASAD C	EXE	A3	12.9.18
60	1NH17EC402	ARATHI.V	EXE	A3	8.8.18
61	1NH17EC403	BHAVANI. V	PROF	A3	8.8.18
62	1NH17EC412	KIRAN . R	EXE	A1	2.8.18
63	1NH17EC423	RAJESHA . T . A	EXE	A1	· ·
64	1NH17EC429	SUPRITHA H. D	EXE	А3	

**HOD - ECE** 



**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

### DEP ARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "B" Sec

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1	1NH16EC064	NEHA MAHESH	Nema
2	1NH16EC066	NISHU DUBEY	
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5	1NH16EC070	MAHALAKSHMI P	p. nahalal.
6	1NH16EC071	DEEPAK P S	Deyma
7	1NH16EC072	PARITHOSH VEMA	Ranks
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31	1NH16EC099	JAGAN MOHAN REDDY	-(A)-
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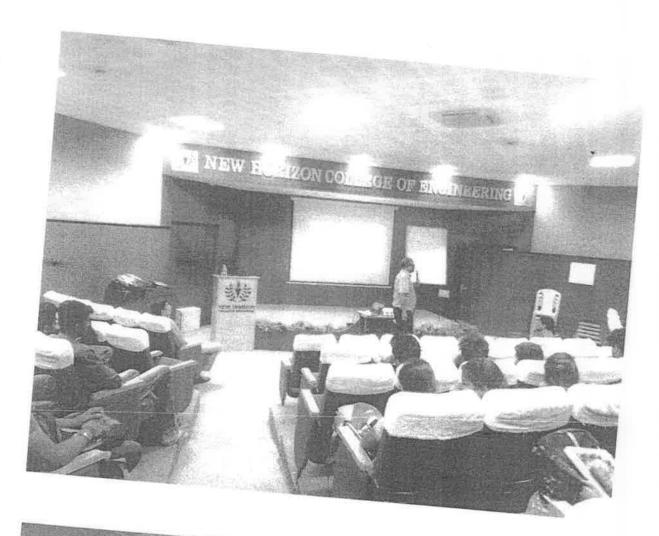
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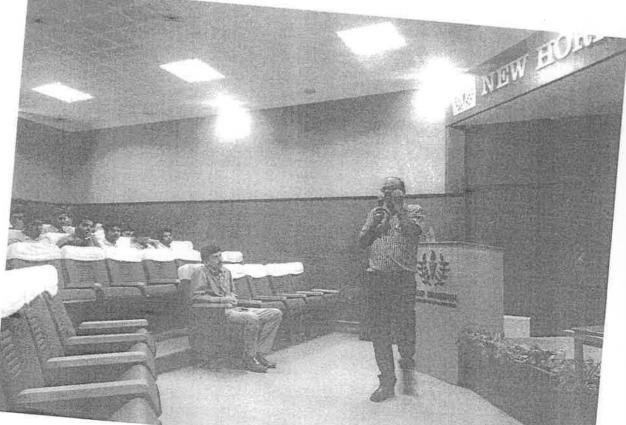


## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "C" Sec

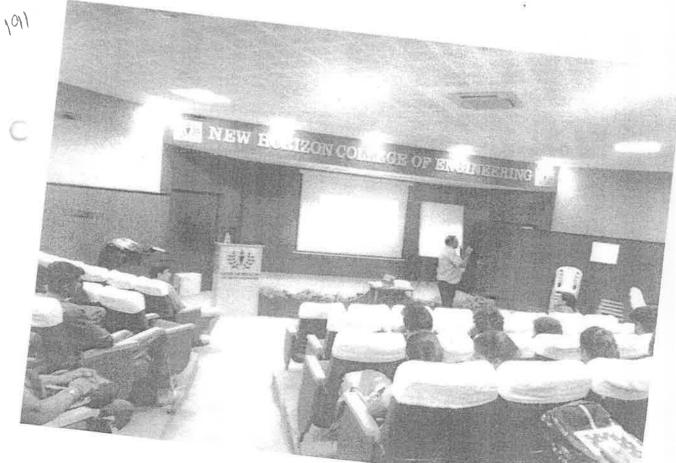
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45 INH16EC730 MOUNIKA E EXE C2  46 INH16EC731 THILAK PRASAD PROF C2  47 INH16EC733 NAWAZ KHAN PROF C2  48 INH16EC734 NISHA ANANDU NAIK EXE C2  50 INH16EC735 NIVEDITHA N EXE C2  51 INH16EC736 NIVEDITHA R PROF C2  52 INH16EC739 PAWAN KUMAR SRIVASTAVA EXE C2  53 INH16EC740 POOJA P CHOUHAN EXE C1  54 INH16EC741 PRAVEEN KUMAR PROF C2  55 INH16EC742 RACHANA.S PROF C2  56 INH16EC742 RACHANA.S PROF C2  57 INH16EC744 RAMYA.R EXE C2  58 INH16EC745 REDDY SHEKAR.B.S EXE C3  59 INH16EC746 RUCHIKA PRADEEP PROF C1  60 INH16EC748 S SHYAM EXE C3  61 INH16EC749 SHRIYA.G EXE C3  61 INH16EC749 SHRIYA.G EXE C3  63 INH16EC750 SINDHU A	Thule congress
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76 INHI7EC409 JAYANTH V	
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81 INHI7EC421 RAGHUR EXE CI (NO. 1971)	CC
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83 INH17EC424 RAKESH KUMAR T	20
84 INHI7EC431 VASANTH R EXE CI VE CI	4











# Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

#### OFFER LETTER

Ref. No.: NHCE/OFFER/124/2018

November 14, 2018

Mr. Arun Krishnan Senior Product Development Engineer Harman India Bangalore.

Dear, Mr. Arun Krishnan,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2018-19. New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality education.

• The subject allotted to you is Signals and Systems (Module 5).

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms discussed.

Signature: Asunk

Date: 15/11/2018



Department of Electronics and Communication Engineering 14.11.2018

### EXPERT LECTURE

The ECE students are hereby informed that the Expert Lecture is scheduled on

"Signals and Systems"

On 15.11.2018

By Mr. Arun Krishnan, Senior Product Development Engineer, Harman, Bengaluru, India.

S. No		
	Timings	Semester
1	8 AM – 10 AM	3rd (A D c
		3 <sup>rd</sup> (A, B & C

Respective subject teachers are hereby informed to be present in the

Venue: Classrooms 215, 219

Note: All Faculty members are invited to attend the same (without affecting regular classes)

Faculty In charge:

#### Arun Krishnan

DOB: 10<sup>th</sup> April 1991

Profile: Senior Software Engineer

Email ID: arunkrish101991@gmail.com

Phone: 9539117009

#### Summary:

- 5 years and 11 months of hands-on domain and technology experience in DSP algorithms
- Five plus years of experience in the field of embedded sof ware development.
- Experience in porting and optimization of image processing algorithms.
- Experience in design, development, testing and mair enance of audio codecs.
- Experience in design, development, testing and maintenance of audio parsers.
- Experience in design, porting and maintenance, of various device drivers.
- Experience in optimization of audio and video codecs to increase performance at C and Assembly level
- Experience in porting and optimization of codes into new platform architecture (ARM Cortex
- Experience in integration of existing codecs into Openmax Framework.
- Experience in design and dever upment of Gstreamer Plugins.
- Experience in integration of rodecs into ADSP Framework:
- Experience in design deve oprnent and testing of directshow filters. Experience in QNX
- Experience in NXP gudio frameworks.

### Employment Profi'e

- Currently working as Senior Product Development Engineer in Harman Connected
- Previously worked as Senior Software Engineer in the DSP Group of PathPartner Previously worked as Design and Development Engineer in Tata Elxsi, Trivandrum

ACADEMIC QUALIFICATION	INSTITUTE	BOARD/UNIVERSITY	VEAD	
B.tech	Adi Shankara		YEAR OF PASSING	MARKS
	Institute Of Engineering and Technology, Kalady	Mahatma Gandhi University	2012	78% (Distinction)
	Chinmaya Vidyalaya	CBSE	2008	
	Tripunithura		2000	84%
	Chinmaya	CBSE		(Distinction)
	Vidyalaya Tripunithura		2006	85.4% (Distinction)

#### Skill Summary

- Programming Languages: C,Embedded C,Assembly(ARMV7, Neon, PIC Series, PIC. 8085, 8051), ZSP Assembly, Xtensa Intrinsics. Cadence Vision P6 Intrinsics.
- Operating Systems: Linux, Windows, ulTRON, Windows Embedded Automotive7
  - Development Tools: GCC-GDB, HEW, Microsoft Visual Studio (2008, 2010), RVDS, Cygwin, makefile, Eclipse, ARM-DS5, ZVIEW, Xtensa Xplorer.
- Platforms: Tegra-4(Cortex A15), SH-2A Series, RCAR-E2, RCAR-M2, RCAR-
- Others: JIRA, Tortoise svn, Beyond compare, Audacity, Parabola Explorer, Open office, MS Word, MS Excel, Putty, WinSCP, KlocWork, Valgrind, GraphEdit

#### Work Summary

### Project Executed at Harman Connected Services:

Project Name	AudioWorX (Current Project)
Project Summary	This project involves the bring up of Audio objects and frameworks on iMx8 hardware.
Tools & Environment	C , QNX Momentics
Responsibilities	<ul> <li>Study of existing framework.</li> <li>Port existing audio objects and frameworks in QNX.</li> <li>Design and development of QNX resource manager to interact with audio framework and tuning device.</li> <li>Integration of audio objects with NXP framework running on Hifi4</li> </ul>

Project Name	Engine Order Cancellation Algorithm
Project Summary Tools & Environment	This project involves the bring up of Engine Order Cancellation on Xtensa C, Xtensa Intrinsics
Responsibilities	<ul> <li>Study of EOC Algorithm.</li> <li>Feature Addition to support 5 orders,4 channels and 6 speakers.</li> <li>Design, integration and testing of True Audio Functionality.</li> <li>Design, integration and testing of Stability Control.</li> <li>Design of new wrapper for integration with framework.</li> <li>Design, integration and testing of Impulse Response Generator.</li> <li>Design, integration and testing of Impulse Response Measuremen</li> </ul>

### Project Executed at PathPartner:

Optimization of Various Image Processing Kernels for Vision P6
This project aims at porting and optimization of various image processing
<ul> <li>Xtensa Xplorer , C ,Xtensa Intrinsics,Vision P6 Intrinsics</li> <li>Study of Cadence Vision P6.</li> <li>Optimization of various algorithms using Vision P6 intrinsics.</li> <li>Fixing issues reported from customer side.</li> </ul>

Project Name	Porting, Optimization and Tuning of S
Project Summary	Porting, Optimization and Tuning of SmartAmp on Xtensa Hifi-3  This project aims at porting and optimization of TI SmartAmp Algorithm into Xtensa HIFI3, and also to fix any integration issues reported while tuning the algorithm in hardware.
<b>Tools &amp; Environment</b>	Xtensa Xplorer Audorit. D.
Responsibilities	Xtensa Xplorer , Audacity, Purepath Console 3, C ,Xtensa Intrinsics  Study of Xtensa HIFI3 Architecture. Optimization of various smart amp modules.  Integration of new wrapper layer to the smart amp library.  Fixing issues reported from customer side.

#### Project Executed at Tata Elxsi:

Project Name	Design and Development of DirectShow Filters
Decises 0	This project deals with the regime
Project Summary	This project deals with the requirement capture, design and development of decoder post processing filters for Windows DirectShow Framework. It also deals with the porting of the directshow filters, onto Renesas RCAR-H2 platform and to implement and test various playback functionalities such as Play, Pause, Seek, Fast-forward, FastRewind, etc.

Tools & Environment	Microsoft Visual C++(2008), GraphStudio, WinCE7.0, Windows Embedded
Responsibilities	<ul> <li>Study of Windows DirectShow Framework.</li> <li>Study of Renesas RCAR-H2</li> <li>Design, development and testing of WMA9 and WMA10Pro Decoder DirectShow filter.</li> <li>Design, development and testing of Dolby AC3 Parser Filter.</li> <li>Design, development and testing of Dolby AC3 DecoderFilter.</li> <li>Porting of DirectShow Filters into Renesas RCAR-H2 board.</li> <li>Testing of various playback functionalities for directshow filters.</li> </ul>

Project Name	Porting and Ontimization
Project Summary	Porting and Optimization of DTS Neural Upmix in VSI-ZSP  This project includes the porting of DTS-Neural Upmix (2channel to 7 channel upmixer, and 5 channel to 7 channel upmixer) into VSI-ZSP900A ZSP Assembly level optimization of the modules, to into the modules.
Tools & Environment	ZSP Assembly level optimization of the modules, to improve performance.  Assembly  Assembly  Assembly
Responsibilities	<ul> <li>Study of ZSP Architecture and Instruction set</li> <li>Study of DTS Neural Upmix</li> <li>Porting of DTS codec into ZSP900A processor</li> <li>C level and ZSP Assembly optimization of neural upmix modules to improve performance.</li> </ul>

Project Name	GStreamer Audio FrameWork Development This project deals with the
Project Summary	Framework and to do integration of Audio Codecs into the Opens
Tocls & Environment	Support playback function att
Responsibilities	Microsoft Visual C++(2008), Linux, C, GStreamer, OpenMax  Development of OMX IL Layer for DSD Decoder. Development of OMX IL Layer for MP3 Decoder. Develop Gstreamer Plugins for MP3, WMA and EHEAAC decoders Develop Gstreamer Plugin for MP3 Parser. Develop Gstreamer Plugin for DSD Parser. Develop a command line Player application for playback support in RCAR-E2/RCAR-M2.

Project Name	DownMixer
Project Summary Tools & Environmen	This project deals with the design, development and optimization of Lt-Rt Downmix to downmix a maximum of 7.1 channels into stereo.
Responsibilities	Microsoft Visual C++(2008), Beyond Compare DS-5, C, ARM Assembly  Requirement capture of Downmixer.  Study of Lt-Rt Downmix.  Design, development of downmixer.  C level and ARM optimization of downmixer to improve performance.



Project Name	Integration of Codecs into ADSP FrameWork This project deals with the selection of Codecs into ADSP FrameWork
Project Summary	RCAR E2/M2 on ARM side development of OpenMax framework for R
Tools & Environment	C , OpenMax, ADSP
Responsibilities	<ul> <li>Study of ADSP framework.</li> <li>Study of HIFI plugin.</li> <li>Design and development of OpenMax framework for ARM-side.</li> <li>Integration of OpenMax framework with ADSP and integration of AAC-LC Encoder with this framework.</li> </ul>

Project Name	DTV Compliant AAC LC M III
Project Summary	DTV Compliant AAC-LC Multichannel Decoder  This projec deals with the requirement capture, design, development and testing of AAC- LC multichannel decoder
Tools & Environment	testing of AAC- LC multichannel decoder.  C, Microsoft Visual C++(2008)
Responsibilities	Study of AAC LC specification.     Study of DTV compliance.
	Requirement canture of AAC LO
1	<ul> <li>Design and development of AAC Header Parser.</li> <li>Design and development of AAC downmixer module.</li> </ul>

Project Name	Media SDK for SH7269/SH7264
Project Summary	automotive company to port Media framework from SH7264 to SH7269 like drivers, filesystem for a security platform based on SuperH architecture which includes entire system portion
Tools & Environment	reported by end customers.  HEW, Cygwin, makefile, C, UITRON
Responsibilities	Setting up command-line build system.     Testing and maintenance of various device Drivers (SRC, WDT USB, ADC etc.)
	<ul> <li>Testing and maintenance of audio codecs (PCM, MP3, WMA8, AAC).</li> <li>Testing and maintenance of various middleware (Automount, FileID cache).</li> </ul>

Project Name	HEVC Encoder
Project Summary	Design, Development into
Tools & Environment	Design, Development, integration of HEVC (High Efficiency Video Coding)  C. Microsoft Viscol Quantum Coding)
Responsibilities	C, Microsoft Visual C++(2008)  Study of HEVC Technical Specification document. Requirement capture of Entropy Coding module (CABAC) of HEVC Design, Implementation, integration and testing of HEVC Entropy Coder – CABAC (Context Adaptive Binary Arithmetic Coding.

Project Name	HEVC Decoder
Project Summary	Porting and Optimization of HEVC Decoder on Tegra4(Cortex A15)



Tools & Environment Responsibilities	C, Microsoft Visual C++(2008),RVDS,ARM Assembly     Study of Cortex A15 Architecture and ARM,Neon instruction set.     Porting of HEVC Decoder onto Tegra Processor.     C level and Assembly optimization of HEVC decoder modules to increase fps.
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#### DECLARATION

I hereby declare that the above furnished information is true to best of my knowledge and belief.

Arun Krishnan

S.	NO	
	1 Name of the D	DETAILS REQUIRED
- 2	Name of the Resource person Years of Experience	Arun Krishnan
3		6 years
4		Harman India
5	Number of Lecture Hours	Senior Product Development Engineer
6	Date of Lecture	3
7	PAN Number	15/11/2018
8	Aadhar Number	CRCPK5394Q
9	Date of Birth	403169473446
0	Educational Qualification	10/04/1991
1	Bank Name	B.tech in Electronics and Communication
2	Account Number	HDFC Bank
3	IFSC Code & Branch	50100195894875
	and the brightness	HDFC0004386, B.M.E.L Gate Circle, Bangalor



### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "A" Sec

SL.No	College ID	USN	NAME	
1		1NH16EC038	KARROTU PRUDHVI	
2		1NH17BT002	ANISHA DEVI	
3		1NH17BT005	BATHULA SRI SAI KRISHNA	Bacal
4		1NH17BT007	GANGADHARA VARSHITHA	
5		1NH17BT016	MURAKONDA AVINASH	Marie
6		1NH17BT023	SAI KARTHIK P R	P.R. 52 2
7		1NH17BT025	SUHAIL PASHA	
8		1NH17BT026	SUMAN PAL	Suman
9		1NH17BT031	THURPUNATI SURYA PRAKASH REDDY	T. sonya Prakosh
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72	1NH18EC406-T	Diploma	Swaroop M R	
73	1NH18EC407-T	Diploma	Naveen N	
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75	1NH18EC413-T	Diploma	Nagesh N Reddy	0,00
76	1NH18EC414-T	Diploma	Yashwanth N K	
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80	1NH18EC427-T		Pragati Shrinivas Naik	
81	1NH18EC428-T		Sandeep Reddy V	
82	1NH18EC429-T		Spoorthi S T	
83	1NH18EC430-T	Diploma	Santosh B Gonal	

## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "B" Sec

SL.No	College ID	USN	NAME	
1		1NH17EC060	NISHMITHA C SHETTY	
2		1NH17EC062	NITHIN R KALLIGUDI	A LARENTA
3		1NH17EC063	PALAGIRI JYOTHI KRISHNA	Mand IC
4		1NH17EC064	PANUGANTI VIKAS	PTKYPShma
5		1NH17EC065	PARTHASARATHI N	7-000
6		1NH17EC066	PASSAVULA PHANINDRA	Part
7		1NH17EC068	PRANAY CHATURVEDY	Phas
8		1NH17EC069	PRAVEEN KUMAR S	
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17		1NH17EC078	RAVI KIRAN M B	Batuly /
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20		1NH17EC081	ROHITH P GOWDA	- Papaproleau
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71	1NH18EC404T	Diploma	Akhilesh Venkappa Kshatri	C9
72	1NH18EC410-T	Diploma	Pavan Y	au
73	1NH18EC441-T	Diploma	Aakash Singh N	1 Sem
4	1NH18EC412-7	Diploma	Shreekanta K S	that
5	1NH18EC420-T	Diploma	Sagar V	Beileante
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### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "C" Sec

SL.No	College - ID	USN	NAME	
1		1NH14EC715	Karthik D P	ra-Altr. D.F
2		1NH15EC015	DEBBRAT MAHAPATRA	Debbrut
3		1NH15EC067	PANDETI SAI KUMAR	1
4		1NH16EC021	D Gowtham	
5		1NH16EC022	DARSHAN RAJ S	
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7		1NH16EC029	VICTOR ABHILASH	9.001 W.
8		1NH16EC065	NILOTHPAL (30.7.18)	N09101200
9		1NH16EC074	PAVAN KUMAR S	141004
10		1NH16EC091	SAM LEANDER D	Marri J
11		1NH16EC121	YATHISH K	4
12		1NH16EC136	ALVIRA SUZANA	
13		1NH16EC701	A NAVYA SHREE	
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18		1NH17EC700	AATHIRA VIJAYAN	- War
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20		1NH17EC702	AKSHAJA SHIMALNA O K	Asia
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72		1NH17EC755	USTHEPALLI ABHIRAM	200
73		1NH17EC756	VARUN P	Ma 0
74		1NH17EC757	VELURI SRI HARSHAVARDHAN REDDY	Vers
75		1NH17EC758	Y SUCHARITHA	Sucherithay
76		1NH17EC759	YERRABALLI GURU SAI KUMAR	DIDE
77	1NH18EC415-T	Diploma	Tojagujaj T.D.	Teraumine &
78	1NH18EC417-T	Diploma	Harshitha M	to a
79	1NH18EC419-T	Diploma	Roja Parameshwar Uilikashi	Traemily V
80	1NH18EC423-T	Diploma	Aiyappa K K	01~
81	1NH18EC424-T	Diploma	Sathyaprasada G P	@ #A
82			P-Likith Chowlasy	1

### NEW HORIZON COLLEGE OF ENGINEERING

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

11/11/2018

#### **GUEST LECTURE**

This is to inform to VII semester students of ECE that the guest lecture is arranged on 14-11-2018 at Chanakya seminar hall between 10:00AM to 1:00PM.

Topic to be delivered: Antenna Design

Name of the Speaker: Ms. Jayachandran Aradhya

Designation: Chief Executive Company: Silicon Microsystem

Respective subject teachers are hereby informed to be present at the Chanakya seminar hall during the lecture.

All the faculty members are invited to attend the same without affecting the regular classes.

Guest Lecture coordinator

HoD-ECE



Department of Electronics and Communication Engineering 7.11.2018

#### **EXPERT LECTURE**

The ECE students are hereby informed that the **Expert Lecture** is scheduled on

"Analog Electronics Product Development"

On 09.11.2018

By Ms.Poornima Mohanachandran, CEO,

Eklakshya, Bengaluru, India.

S. N	О	Timings	Semester	
1		9 AM – 11 AM	3 <sup>rd</sup> (A, B&C)	

Respective subject teachers are hereby informed to be present in the venue during the schedule.

Venue: Falconry Hall

Note: All Faculty members are invited to attend the same (without affecting regular classes)

Faculty In charge:

Mc Navana 2 Mc Monika 3 Prof Kesar Singh

DECE.

## NEW HORIZON COLLEGE OF ENGINEERING PARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "C" Sec

L.No	USN	NAME	Stream	Batch	Date
1		Karthik D P	EXE	C1	Re-registered
2		DEBBRAT MAHAPATRA	PROF	C2	Re-registered
3		HARSHA E	EXE	C2	Re-registered
4	1NH15EC067	PANDETI SAI KUMAR	PROG-G	C3	Rejoinee (20.8.18)
5	1NH16ĒC021	D Gowtham	EXE	C3	Rejoinee
6	1NH16EC022	DARSHAN RAJ S	EXE	C3	Re-registered
7	1NH16EC029	VICTOR ABHILASH	EXE	C3	Re-registered //
8	1NH16EC065	NILOTHPAL	EXE	C3	Rejoinee
9	1NH16EC074	PAVAN KUMAR S	EXE	C2	Re-registered
10	1NH16EC091	SAM LEANDER D	EXE	C3	Re-registered Course
11	1NH16EC121	YATHISH K	PROF	C3	Re-registered Z
12	1NH16EC701	A NAVYA SHREE	EXE	C3	Rejoinee
13	1NH16EC712	CHETHAN D R	EXE	C2	Re-registered
	1NH16ME042	Karthik K	PROF	C3	Rejoinee Corti
15	1NH17EC700	AATHIRA VIJAYAN	EXE	C1	in the second
16	1NH17EC701	AJAYS S	EXE	C1	Augus S.
17	1NH17EC702	AKSHAJA SHIMALNA O K	EXE	C1	13.8.18 Ansher
18	1NH17EC703	ARAVIND P M	PROF	C1	Acount 1
19	1NH17EC704	ASHIKA P	EXE	C1	2.8.18 April
20	1NH17EC705	BHARGAVI	EXE	C1	6.8.18 Bhanga
21	1NH17EC706	CHANDU V J	PROF-G	C1	duithet
22	1NH17EC707	DHANUSH S	EXE	C1	Dhum
	1NH17EC707	DHINAKARAN S	EXE	C1	
23	1NH17EC708	DISHANTH M P	PROF	C1	
25	1NH17EC709	G DEEPIKA	EXE	C1	3.8.18 thelph
26	1NH17EC711	GAGANASHREE P S	EXE	C1	9.8.18
27	1NH17EC712	GRACE GLADYS A	EXE	C1	2.8.18
-6	1NH17EC713	HARSHITH NAIDU J	EXE	C1	
29	1NH17EC714	BHAVANA	EXE	C1	11.8.18 JBherr
	1NH17EC715	JAYADEEP G P	EXE	C1	1.8.18
30	1NH17EC716	IILLELLA RUPASAGAR REDDY	EXE	C1	9.8.18 7 (14)
32	1NH17EC717	K AISWARYA	EXE	C1	1 my
33	1NH17EC718	THE PARKS TO A LIVE	EXE	,C1	1.8.18 1 valar
34	1NH17EC719		PROF	C1	Bourley!
	1NH17EC720		EXE	C2	7.8.18
35	1NH17EC720		EXE	C2	13.8.18 Mary she
36	1NH17EC721		EXE	C2	
37	1NH17EC722		PROF	C2	3.8.18
38	1NH17EC723	The state of the s	EXE	C2	7.8.18 Bhoustley
39			EXE	C2	4.8.18
40	1NH17EC725		EXE	C2	8.8.18
41	1NH17EC726		EXE	C2	1/1/2
42	1NH17EC727	NEHA KRISHNA			2012

1NH17EC731	PALVADHI VENKATA SAI PAVAN KALYAN	PROF-G	C2	P-s-Pl
1NH17EC732	PAVAN U	EXE	C2	3.8.18
, 1NH17EC733	PRACHI VERMA	PROF	C2	1.8.18 Prachi
1NH17EC734	PRAJWAL P	EXE	C2	1.8.18 Phan
1NH17EC736	RAHUL BALAJI P S	EXE	C2	3.8.18
1NH17EC737	RISHIKA POTEPALLI	EXE	C2	China
1NH17EC738	RITWIK SHOME	PROF	C2	2.8.18
1NH17EC739	S DENILA GRACELIN	EXE	C2	10.8.18 July
1NH17EC740	S RAHUL YADAV	EXE	C2	
1NH17EC741	S SAI HARSHA	EXE	C3	13.8.18 horeha.
1NH17EC742	SAHIB ARORA	EXE	C3	6.8.18
1NH17EC743	SANDHYA K	EXE	C3	Mandhyaik
1NH17EC744	SHARATH T P	PROF	C3	30.7.18
1NH17EC745	SHARON MOSES	PROF-G	C3	7.8.18 Whatem
1NH17EC746	SHASHANK REDDY J	EXE	С3	26.7.18 Sha
1NH17EC747	SHASHANK S SALOTAGI	PROF	C3	7.8.18
1NH17EC748	SHIVANG DUBEY	EXE	C3	6.8.18 EMW
1NH17EC749	SIRISHA B S	EXE	C3	SILISTE
1NH17EC750	SOMA YASWANTH REDDY	EXE	C3	2.8.18 Some !
1NH17EC751	SREEKAR REDDY KALLAM	EXE	C3	
1NH17EC752	SUHĀS S	EXE	C3	4.8.18 Willis
1NH17EC753	SYED:TOUQEER	EXE	C3	-
1NH17EC754	THUMMALA SURYA DEVI VARAPRASAD	EXE	C3	23.7.18 8/904
1NH17EC755	USTHEPALLI ABHIRAM	EXE	C3	3.8.18
1NH17EC756	VARUN Ŗ	PROF	C3	PAZWIE
1NH17EC757	VELURI SRI HARSHAVARDHAN REDDY	PROF	C3	Harashaked d.
1NH17EC758	Y SUCHARITHA	EXE	C3	7.8.18 Suhaus
1NH17EC759	YERRABALLI GURU SAI KUMAR	EXE	C3	3.8.18 4. Guenson
	1NH17EC732 1NH17EC733 1NH17EC734 1NH17EC734 1NH17EC736 1NH17EC737 1NH17EC739 1NH17EC739 1NH17EC740 1NH17EC741 1NH17EC741 1NH17EC742 1NH17EC744 1NH17EC744 1NH17EC745 1NH17EC745 1NH17EC746 1NH17EC750 1NH17EC750 1NH17EC750 1NH17EC751 1NH17EC753 1NH17EC754 1NH17EC754 1NH17EC755 1NH17EC756 1NH17EC756 1NH17EC757	1NH17EC732         PAVAN U           1NH17EC733         PRACHI VERMA           1NH17EC734         PRAJWAL P           1NH17EC736         RAHUL BALAJI P S           1NH17EC737         RISHIKA POTEPALLI           1NH17EC738         RITWIK SHOME           1NH17EC739         S DENILA GRACELIN           1NH17EC740         S RAHUL YADAV           1NH17EC741         S SAI HARSHA           1NH17EC742         SAHIB ARORA           1NH17EC743         SANDHYA K           1NH17EC744         SHARATH T P           1NH17EC745         SHARON MOSES           1NH17EC746         SHASHANK REDDY J           1NH17EC747         SHASHANK S SALOTAGI           1NH17EC748         SHIVANG DUBEY           1NH17EC749         SIRISHA B S           1NH17EC750         SOMA YASWANTH REDDY           1NH17EC751         SREEKAR REDDY KALLAM           1NH17EC753         SYED-TOUQEER           1NH17EC754         THUMMALA SURYA DEVI VARAPRASAD           1NH17EC755         USTHEPALLI ABHIRAM           1NH17EC756         VARUN Q           1NH17EC757         VELURI SRI HARSHAVARDHAN REDDY           1NH17EC758         Y SUCHARITHA	1NH17EC732         PAVAN U         EXE           1NH17EC733         PRACHI VERMA         PROF           1NH17EC734         PRAJWAL P         EXE           1NH17EC736         RAHUL BALAJI P S         EXE           1NH17EC737         RISHIKA POTEPALLI         EXE           1NH17EC738         RITWIK SHOME         PROF           1NH17EC739         S DENILA GRACELIN         EXE           1NH17EC740         S RAHUL YADAV         EXE           1NH17EC741         S SAI HARSHA         EXE           1NH17EC742         SAHIB ARORA         EXE           1NH17EC743         SANDHYA K         EXE           1NH17EC744         SHARATH T P         INH17EC745           1NH17EC745         SHARON MOSES         PROF-G           1NH17EC746         SHASHANK REDDY J         EXE           1NH17EC747         SHASHANK S SALOTAGI         PROF           1NH17EC749         SIRISHA B S         EXE           1NH17EC750         SOMA YASWANTH REDDY         EXE           1NH17EC751         SREEKAR REDDY KALLAM         EXE           1NH17EC752         SUHAS S         EXE           1NH17EC754         THUMMALA SURYA DEVI VARAPRASAD         EXE           1NH17EC755	1NH17EC732         PAVAN U         EXE         C2           1NH17EC733         PRACHI VERMA         PROF         C2           1NH17EC734         PRAJWAL P         EXE         C2           1NH17EC736         RAHUL BALAJI P S         EXE         C2           1NH17EC737         RISHIKA POTEPALLI         EXE         C2           1NH17EC738         RITWIK SHOME         PROF         C2           1NH17EC739         S DENILA GRACELIN         EXE         C2           1NH17EC740         S RAHUL YADAV         EXE         C2           1NH17EC741         S SAI HARSHA         EXE         C3           1NH17EC742         SAHIB ARORA         EXE         C3           1NH17EC743         SANDHYA K         EXE         C3           1NH17EC744         SHARON MOSES         PROF-G         C3           1NH17EC745         SHARON MOSES         PROF-G         C3           1NH17EC746         SHASHANK REDDY J         EXE         C3           1NH17EC748         SHIVANG DUBEY         EXE         C3           1NH17EC750         SOMA YASWANTH REDDY         EXE         C3           1NH17EC751         SREEKAR REDDY KALLAM         EXE         C3 <tr< td=""></tr<>

**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "B" Sec

SL.No	USN	NAME	Stream	Batch	Date
1	1NH17EC060	NISHMITHA C SHETTY	EXE	B1	20.8.18
2	1NH17EC062	NITHIN R KALLIGUDI	EXE	B1	4.8.18
3	1NH17EC063	PALAGIRI JYOTHI KRISHNA	EXE	B1	
4	1NH17EC064	PANUGANTI VIKAS	EXE	B1	3.8.18
5	1NH17EC065	PARTHASARATHI N	EXE	B1	
6	1NH17EC066	PASSAVULA PHANINDRA	EXE	B1	3.8.18
7	1NH17EC068	PRANAY CHATURVEDY	EXE	B1	
8	1NH17EC069	PRAVEEN KUMAR S	EXE	B1	4.8.18
9	1NH17EC070	PREETHU C	EXE:	B1	2.8.18
10	1NH17EC071	PRINCE CHAURASIYA	PROF	B1	1.8.18
11	1NH17EC072	PUNEETH REDDY V	EXE	B1	2.8.18
12	1NH17EC073	R BARATH	EXE	B1	
13	1NH17EC074	R KISHAN KUMAR SAI	EXE	B1	7.8.18
14	1NH17EC075	R RAGHAVA	PROF-G	B1	1.8.18
15	1NH17EC076	RAHUL MONDAL	PROF	B1 -	3.8.18
16	1NH17EC077	RAHUL V	EXE	B1	->/;
17	1NH17EC078	RAVI KIRAN M B	EXE	B1	7.8.18
18	1NH17EC079	RENUKA D N	EXE	B1	7.8.18
19	1NH17EC080	RIYA RAKESH	EXE	B1	-
20	1NH17EC081	ROHITH P GOWDA	EXE	B1	13.8.18
21	1NH17EC082	RUPSA DATTA	EXE	B1	3.8.18
22	1NH17EC083	S RISHITA	EXE.	B1	3.8.18
23	1NH17EC084	SAIKAT SAMANTA	EXE	B2	2.8.18
24	1NH17EC085	SAIMANTI SAHA	EXE	B2	3.8.18
25	1NH17EC086	SAMYUKTHA V	EXE	B2	28.8.18
26	1NH17EC087	SATEESH R HEGDE	EXE	B2	
27_	1NH17EC088	SHAKTHI A	EXE	В2	7.8.18
28	1NH17EC089	SHALINI P N	EXE	B2	1.8.18
29	1NH17EC090	SHARAN KUMAR K S	EXE	B2	2.8.18
30	1NH17EC091	SHIVA S	PROF	B2	
31	1NH17EC092	SHOAIB AHMED	EXE	B2	
32	1NH17EC093	SHRESTHA PATNAIK	EXE	B2	
33	1NH17EC094	SHREYANKA S	EXE	- B2	
34	1NH17EC095	SHWETA S	EXE	B2	4.8.18
35	1NH17EC096	SOUVIK DAS	EXE	B2	1.8.18 %
36	1NH17EC097	SOWMIYA A	EXE	B2	4.8.18
37	1NH17EC098	SRINIDHI DAMODHAR	PROF	B2	
38	1NH17EC098	STANISLAUS LASRADO	PROF	B2	2.8.18
39	1NH17EC100	SUHAIB AFNAN	PROF	B2	6.8.18
40	1NH17EC100	SUHAS H G	EXE	B2	4.8.18

41	1NH17EC102 S	UHAS S P	EXE	B2	T	Onlas
42	1NH17EC103 S	UMITHRA V ARADHYA	EXE	B2	2.8.18	Sunich
43	1NH17EC104 S	UNIL PATIL	PROF	B2	6.8.18	ANX "
44	1NH17EC105 S	USHMA S	PROF	B2	2.8.18	· Culus
45	1NH17EC106 S	YALE RAJ KUMAR	EXE	B2	2.0.20	1
46	1NH17EC107 T	ALLURI VENKATA VARUN SAI	EXE	B2	3.8.18	
47	1NH17EC108+ T	EJAS K	EXE	В3	6.8.18	Telas
48	1NH17EC109 T	HEJAS U	EXE	В3		1) Third
49	1NH17EC110 T	HIKKAVARAPU SAI AVINASH R	EXE	В3		TECA
50	1NH17EC111 T	HOMMANDRU VENKATA SAI M	EXE	В3	16.8.18	luste.
51	1NH17EC112 U	PPU GIRIDHAR	EXE	В3	2.8.18	PROTES
52	→ 1NH17EC113 V	DHARANIDHAR	EXE	В3	7.8.18	What:
53	1NH17EC114 V	R SAI LAALITHYA	EXE	В3	6.8.18	Lover
54	1NH17EC115 V	S MOUNIKA	EXE	В3	21.8.18	Meurkie
55	1NH17EC116 V.	AKKALA MAHEEDHAR	EXE	В3	11.8.18	Huma
56	1NH17EC117 V	IDHYA B	EXE,	В3	1.8.18	alellers
57	1NH17EC118 V	IDHYASHREE B M	EXE	В3	1.8.18	or dead
58	1NH17EC119 V	INDHYA NARAYAN	EXE	B3	2.8.18	Usudley
59	1NH17EC120 V	ISHWAJYOTHI B RESHMI-	EXE	B3	*	Ruhmu
60		C ABHAY KUMAR	EXE	B3	7.8.18	No A
61		ADHEESH	EXE	B3	3.8.18	2
62		ASH MALVIYA	EXE	B3	3.8.18	Parhi
63		ASSER FAIZ AHMED N	EXE	B3	3.8.18	- Hall
64		ADE NAVEEN REDDY	PROF	B3	7.8.18	Noun X
65		EELA LOKESH	EXE	B3	2.8.18	Tokes
66	43444	ALLA GUNA PRADEEP	EXE	B3	2.0.10	Poude-P
67		YKAM KARTHIK REDDY	EXE	B3	1.8.18	SKartika
68		AISHALI M S	PROF	B3	2.8.18	10 slva
69		FKASH SINGH	PROF	B3	2.0.10	JAK
70		havenik ugasse BRSLAKSHMJ.M	(32)	B1		B
80	C	PSLAKSHM 2 M	PR OF	W. W.	E	
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/-		s should be informed to pay the		es.		Let.
32		ven attendance till they clear the				n .
	E	BHANU PRIYA.N	PROF	B3		Bhampliya
83	Pa	Wan .y	PROF	BI		D
84		150	ROF	63		Viens B
85		neshadhaev Hegde Y	PAB	B.2		201
36		n vo nouncer Heyor 1	NOF	-		todonala
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## NEW HORIZON COLUEGE OF ENGINEERING PARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "A" Sec

L.No	USN	NAME	Stream	Batch	D-4	
1	1NH16EC038		EXE	A1	Date	_
2	1NH17BT002	ANISHA DEVI	EXE	A1	1.0.10	4
3	1NH17BT005	BATHULA SRI SAI KRISHNA	EXE	A1	1.8.18	4 nisha
4	1NH17BT007	GANGADHARA VARSHITHA	PROF	A1		- In a state of
5	1NH17BT016	MURAKONDA AVINASH	EXE	A1	2.8.18	Amorra
6	1NH17BT023	SAI KARTHIK P R	PROF		2.0.10	
7	1NH17BT025	SUHAIL PASHA	-	A1		P.R. Sary
8	1NH17BT026	SUMAN PAL	EXE	A1	2.8.18	_
9	1NH17BT031	THURPUNATI SURYA PRAKASH REDDY	EXE	A1 A1	2.8.18	T. Surrya Drakoph
10	1NH17EC001	AARTHY P JAITH	EXE	A1	7.8.18	T. Suryapraborh  Austhy
11	1NH17EC002	ADESH V	PROF	A1	3.8.18	- a bento
12	1NH17EC003	ADITYA OMPRAKASH CHOUDHARY		A1	2.8.18	Para de la companya della companya della companya de la companya della companya d
- 14	1NH17EC004	AKSHITHA R	EXE	A1	2.8.18	AKshitha.
	1NH17EC005	ANAND T	EXE	A1		anand . F.
15	1NH17EC006	ANAND T	PROF	A1	2.8.18	from d.l
1.6	1NH17EC007	ANKIT KUMAR	EXE	A1	1.8.18	
7	1NH17EC008	ARUN Y S	EXE	A1		
.8	1NH17EC009	B S ABHISHEK	EXE	A1		
.9	1NH17EC010	BYAMINI	EXE	A1	7.8.18	102
0	1NH17EC011	BHARATH M	PROF	A1		T & JR#
1	1NH17EC012	BHARATH Y R	EXE	A1	4.8.18	Bhardhy 15
2	1NH17EC013	BHASKAR K	EXE	A1	2.8.18	Bh. Jan
3	1NH17EC014	BOGGARAPU MOURYA YASWANTH	EXE	A2	1.8.18	mowya
4	1NH17EC015	BOREDDY NAGA MAHESWAR REDDY	EXE	A2	8.8.18	Qu!
5	1NH17EC016	C O PRUTHVI	EXE	A2	4.8.18	benthing
	1NH17EC017	CHANDANA N	PROF-G	A2		Dhandung .c
7	1NH17EC018	CHETHAN B R	PROF-G	A2	2.8.18	Chethan
3	1NH17EC019	DEEKSHITH N REDDY	PROF-G	A2	4.8.18	Deddy
9	1NH17EC020	DEEPAK S	EXE	A2	6.8.18	Durpak
)	1NH17EC021	DEVASHRUTHA S	PROF	A2		Deresht
L	1NH17EC022	DHANYASHREE V REDDY	EXE	A2	1.8.18	Days Avel
2	1NH17EC023	DIVYA SAGAR REDDY	EXE	A2		Din Sife
-	1NH17EC024	DUDDUKURI VAISHNAVI	EXE	A2	6.8.18	
·	1NH17EC025	G ASAD	EXE	A2	3.8.18	And 6
	1NH17EC026	G R VARSHA	EXE	A2	3.8.18	Round
	1NH17EC027	GAGAN B K	EXE	A2	7.8.18	Gagan B.K
	1NH17EC028	GAUTAM SINHA	EXE	A2	2.8.18	1 sinha
	1NH17EC029	GIRIDHARAN P K	EXE	A2.	20.8.18	
	1NH17EC030	H K BHASKAR	EXE	A2	2.8.18	MANTON
	1NH17EC031	HARSHITHA G	EXE	A2	4.8.18	detroit,
	1NH17EC032	IVATURISSHARSHAVADDHAN	DWD			11

44	1NH17EC035	HTENDED				
15	1NH17EC036	JITENDER	EXE	A2	1.8.18	J STHOMAIN
16	1NH17EC036	K S SIRISH	PROF	A2	9.8.18	Sheholu Breholut
47	1NH17EC037	K S SNËHA LATHA	EXE	A2	6.8.18	Inchalate
48	1NH17EC039	KARAN SRINIVAS	EXE	А3	10.8.18	- Samon
49	1NH17EC039	KAVYASHREE S	EXE	А3		(Figure)
50	1NH17EC041	KEVINE PRASANNA KUMAR	PROF-G	A3	20.8.18	V. A
51	1NH17EC041	KUSHALA C	EXE	А3	8.8.18	icishols
52	1NH17EC042	M BHARAT	EXE	А3	3.8.18	1, 2
53	1NH17EC043	M HEMANTH YADAV	EXE	A3	16.8.18	M. Hend
54		MADALA HIMAJA	EXE	A3	3.8.18	Hania.
55	1NH17EC045	MADHUMITHA R	EXE	A3	3.8.18	Madlwitha.
56	1NH17EC046	MADHUPARNA BANERJEE	PROF	A3	7.8.18	1 (1)
57	1NH17EC047	MAMATHA B S	EXE	A3	7.8.18	14 4
58	1NH17EC048	MANIKANTA	PROF	A3	3.8.18	Al martes
59	1NH17EC049	MANISH B	PROF	A3	3.8.18	Dail &
	1NH17EC050	MANISHA R	EXE	A3	2.8.18	The state of the s
60	1NH17EC051	MILAN SHANKAR RAO M	EXE	A3	1.8.18	Tille
61	1NH17EC052	MOHAMMED GHASSAN	PROF	A3	1.8.18	
	1NH17EC053	MONIKA K REDDY	EXE	A3	1.0.10	monika
63	1NH17EC054	NANDURI SRILASYA	EXE	A3	30.7.18	- Man. Rec
64	1NH17EC055	NAVAJITH R REDDY	EXE	A3	30.7.18	Larga
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Note: Highlighted students should be informed to pay the Account dues.

Students should not be given attendance till they clear the dues.

#### NEW HORIZON COLLEGE OF ENGINEERING, BANGALORE

(Autonomous Institution affiliated to VTU, Accredited by NBA & NAAC with Grade 'A')

### Department of Electronics and Communication Engineering

#### Report on Expert Lecture for Analog Electronic Circuits

The Department of Electronics & Communication Engineering organized an expert lecture for the second year students in the Falconry Seminar Hall on 9th November 2018 for the course Analog Electronic Circuits by Ms. Poornima Mohan Chandra, CEO, Eklakshya . She had also worked in Texas Instruments as a Director for 25 years. The session dealt with 'Mixed Signal Product Development'.

The lecture started off with an introduction which was about the change in the lifestyle of people in last 20 years. She explained about the different types signals including their advantages and applications.

In her lecture, Poornina ma'am discussed regarding the analog and mixed signal electronics and the field of its application. She gave many examples like paytm app, GPS system, OLA app, UBER app, smart watches etc to make us understand to which extent technology and how convenient life has become due to such developments. We even discussed to which extent is communication important in the corporate field.

The foremost example she took was health care scenario. Basically the whole world population is around 7 billion. And it can be categorised as

- 1) Preventive care
- 2) Critical care
- 3) Emergency care
- 4) Follow up care.

The number of experts handling these cases are way too less when compared to that of people to be treated. Experts can be any fields like cardiology, neurology, gynecology etc. As engineers we just don't form database, but also convert it into useful information. Then we were educated regarding telemedicine concept, which was basically the use of electronic information and communication technology to provide and support health care when distance separate the participants.

Mainly we learnt regarding, Mixed Signal Product Development, which includes two specifications.

- \*Functional specifications.
- \*Parametric specifications.

The product development includes the flowing steps. All the points mentioned were quite self explanatory.

- 1)Specification.
- 2)Design.
- 3)Stimulation.
- 4)Physical design.
- 5)Fabrication.
- 6)Production.

The other important topic on which the guest lecture was based was "DATA CONVERTERS"

- \*ADC-Analog digital converter.
- 1)Continuous Time Signal.
- 2)Has Continuous values.
- \*DAC-Digital analog converter.
- 1)Discrete Time Signal.
- 2)Discrete values.

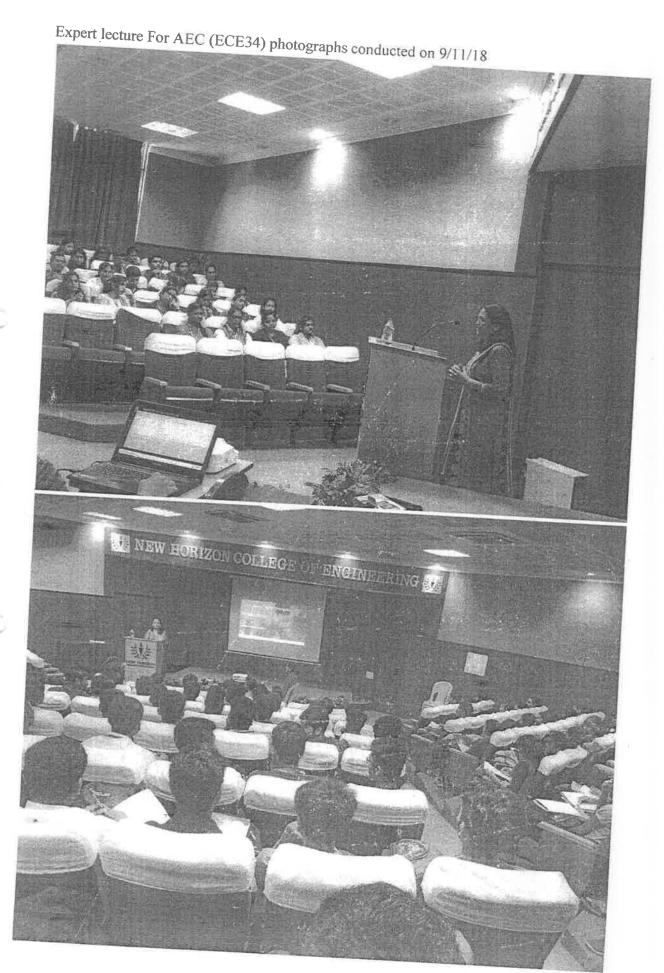
Then we even came across the other category of converters named as \*Successive Approximation

A converter that converts the continuous analog waveform into a discrete digital representation via binary search through all position quantization levels, with medium high resolution, consuming low power and operating at medium speed.

\*Pipeline converters-Applies a digital filter to form a higher resolution but lower sample

\*Pipeline delay.

While concluding, the speaker even made us aware of certain job opportunities like design engineer, verification engineer, test engineer in this particular field which was very informative



### NEW HORIZON COLLEGE OF ENGINEERING

# DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A report on Expert Lecture by Mr.D.Suresh on Wireless and Mobile

A Guest Lecture on Wireless and Mobile Communication by Mr.D.Suresh, currently working for a Japanese company enabling product to support new markets like IIOT, Edge analytics surveillance systems with cloud connectivity, secure payment and measurement systems to enable smart city and grid segment, was delivered on November 3<sup>rd</sup>, 2018 for the students of B.E (ECE) VII-sem.

The speaker started his lecture session with the need to study communication and described various technologies involved in the embedded systems like FPGA, ARM.He later gave a brief insight on radio communication system i.e. Software-defined radio (SDR).The session went on by addressing the various new technologies power CT technologies like

- 1. AI Robotics
- 2. Cloud security
- 3. Multicore IC's (heterogeneous)
- 4. Virtual reality and augmented reality
- 5. 3D printing
- 6. 5G network
- 7. Chip level optical configuration

The speaker later briefed on various communication standards like LORA, Wi-Fi, Zigbee and Bluetooth. He later addressed on various service enablers --meeting requirements like

- 1. mm Wave System/RFIC
- 2. New Channel Coding
- 3. Network Slicing
- 4. <8 GHz Massive MIMO
- 5. Massive Connectivity (IOT)
- 6. Low Latency Network

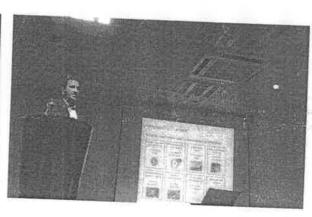
The session proceeded with a brief description of 5G technology. He gave a brief description about 5G key enabling technologies like

- 1. Network densification.
- 2. Very compact and massive MIMO antennas.
- 3. High and agile spectrum usage (wide communication bands).
- 4. Distributed mobile edge cloud and intelligence support.
- 5. Full duplex communication and advanced caching techniques.

The speaker then discussed about 5G unknown issues, bandwidth and latency requirements for generic applications and MIMO systems. He concluded the session by suggesting some of the career options in various domains like embedded AI, Industrial IOT and hardware design.

The lecture has proven to be informative and inspiring to all the students present for the session.





### New Horizon College of Engineering, Bangalore Department of Electronics and Communication Engineering

**Expert Lecture Attendance** 

Course Name

: Wireless and Mobile Communication

Course Code: ECE 71

Resource Person : Mr.D.Suresh

Module

: : Unit 5 - Introduction to MIMO Systems

Sem / Sec

: : VIIA

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: 03.11.18

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Faculty Incharge

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#### New Horizon College of Engineering, Bangalore Department of Electronics and Communication Engineering Expert Lecture Attendance

Course Name

: Wireless and Mobile ( Course Code: ECE 71

Resource Person

: Mr.D.Suresh

Module

: Unit 5 - Introduction to MIMO Systems

Sem / Sec

: VIIB

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: 03.11.18

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Faculty Incharge

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### New Horizon College of Engineering, Bangalore Department of Electronics and Communication Engineering

**Expert Lecture Attendance** 

Course Name

: Wireless and Mobile Communica Course Code: ECE 71

Resource Person

: Mr.D.Suresh

Module

: Unit 5 - Introduction to MIMO Systems

Sem / Sec

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## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING FEEDBACK FORM

NAME OF THE EVENT/CLUB: GUEST LECTURE WIRELESS COMMUNICATION

FEEDBACK ON(Mention the topic):

DATE: 03 /11/ 2018

Instruction: Please put a tick mark at the appropriate column.

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## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING FEEDBACK FORM

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Comments/Remarks/Suggestions:

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# DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING NEW HORIZON COLLEGE OF ENGINEERING

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DEDODE	O INO INO	Name of the Guest speaker	3	Mr. Shivananada Koteshwar	>.	Mr. Sunder Murthy	-	Mr. Srinivasan Pitchai		IV/A,B,C Mr. Shashikanth Patil	Mr Nageswararao	Mr. Arijit Das		Ms Dhanaselvi D				Dr. Vishwas Lakkundi		
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Accredited by NAAC with 'A' Grade

Department of Electronics and Communication Engineering
15.04.2019

## EXPERT LECTURE

The students of IV semester ECE department are hereby informed that the **Expert Lecture** is scheduled on

"DSP Processor and Digital Filters"

On 16.04.2019

## By

Mr. Sarang Suresh Akotkar, Deep learning R& D Engineer, Mr. Kumaran Sethuraman, Deep learning R& D Engineer, Intel Technology Pvt Ltd, Bengaluru, India.

S. No	Timings	Semester
1	10.30 AM - 12.30 PM	4th (A, B & C)

Venue: MBA seminar hall

Note: All the Faculty members are invited to attend the same.

Faculty In charge:

1. Dr. Jayanthi M

2. Ms. Ishani Mishra Aus

3. Ms. Thanuja

HoD/ECE

#### Kumaran Sethuraman

Profile Summary: Delivered excellent contributions towards multiple product delivery include Android based Intel Mobile Phones, Tablets. Recent years focused on Speech, Audio Visual Neural Networks, Fusion methods, Deep Learning, Deep learning hardware accelerators..

Education

: M.Tech

#### **Area of Excellence Include:**

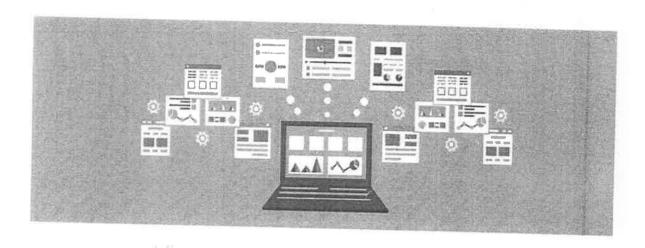
- Multimodal Deep learning system using Audio Visual modality
- Machine Learning

#### **Professional Experience:**

17 years of experience in the field of DSP algorithms and complete audio stack development and Machine learning. Expertise in system analysis and performance optimization for real time audio software across platforms.

#### **Additional Skills:**

- Excellent in Neural networking tool.
- Have done Research on and created the end to end Audio Visual Speech Recognition system



## REPORT ON GUEST LECTURE

## ~DIGITAL SIGNAL PROCESSING AND REAL TIME APPLICATION IN INDUSTRY

Content delivered by:

Mr. Sarang Suresh Akotkar & Mr. Kumaran Sethuraman

Date: 16/04/2019

The expert lecture for DSP was held on 16 April 2019 at the MBA seminar hall from 10.30 to 12.30 pm. We had two industry experts from Intel technologies Mr. Sarang Suresh Akotkar and Mr. Kumaran Sethuraman.

They highlighted how DSP manipulates different types of signals with the intention of filtering, measuring, or compressing and producing analog signals. Some of the key points noted from the session were:

## 1. A Digital Signal Processor contains four key components:

- Computing Engine: Mathematical manipulations, calculations, and processes by accessing the program, or task, from the Program Memory and the information stored in the Data Memory.
- Data Memory: This stores the information to be processed and works hand in hand with program memory.
- Program Memory: This stores the programs, or tasks, that the DSP will use to process, compress, or manipulate data.
- I/O: This can be used for various things, depending on the field DSP is being used for, i.e. external ports, serial ports, timers, and connecting to the outside world.

#### 2. Some of the DSP Filters:

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- IIR Filters (infinite impulse response filter)-IIR filters are typically designed basing on continuous-time transfer functions. IIR filters differ from FIR filters because they always contain feedback elements in the circuit, which can make the transfer functions more complicated to work with.
- FIR Filters- FIR filters are specific to sampled systems. There is no
  equivalent in continuous-time systems. Therefore, only very specific analog
  filters are capable of implementing an FIR filter.

#### 3. Fixed Point vs Floating Point

Digital signal processing can be separated into two categories - fixed point and floating point. These designations refer to the format used to store and manipulate numeric representations of data. Fixed-point DSPs are designed to represent and manipulate integers – positive and negative whole numbers – via a minimum of 16 bits, yielding up to 65,536 possible bit patterns (2<sup>16</sup>). Floating-point DSPs represent and manipulate rational numbers via a minimum of 32 bits in a manner similar to scientific notation, where a number is represented with a mantissa and an exponent (e.g., A x 2<sup>B</sup>, where 'A' is the mantissa and 'B' is the exponent), yielding up to 4,294,967,296 possible bit patterns (2<sup>32</sup>).

#### 4. Applications of DSP:

- Audio signal processing, audio compression
- digital image processing

- video compression, speech processing, speech recognition
- digital communications, digital synthesizers, radar, sonar, financial signal processing, seismology and biomedicine.

#### Feedback:

It was very interactive session, well related to the academics and well delivered. Understood the application of Digital Signal Processing in Real time applications and what industry people expect from young engineers like us.

# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "A" Sec - Expert Lecture -16-04-19 -DSP

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#### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "C" Sec - Expert Lecture -16-04-19 -DSP

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AUTONOMOUS COLLEGE Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade

## **Notice**

Date: 10-04-2019

All the IV semester students are hereby informed to attend the Expert Lecture on "Synthesis on FPGA" for the course System Design using HDL (ECE43) at Falconry Seminar Hall at 12.30 PM on 13/04/2019.

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Faculty coordinator

HOD ECE

#### Shashikanth Patil

#634, 2<sup>nd</sup> Floor, 7<sup>th</sup> Mainroad, Vijayanagar, Bengaluru – 560040 **Email ID**: <u>shashi.vnit@gmail.com</u>; Contact: +91-9880962096

#### Career Objective:

To work in an innovative, challenging environment & help organization to achieve its objective in the most efficient manner by applying my skills, abilities and thereby growing with the organization.

#### Profile Summary:

- 8.5+ years of experience in FPGA/ASIC Design, RTL Simulation, Synthesis, Creating testbench environment & Verification. Additional 2.2+ years of experience in Training & Academics.
- Experience in IP/Block Level Verification with Constraint Random, Assertions & Functional Coverage; creating verification environment with SystemVerilog, Verilog/VHDL & UVM/OVM.
- Experience with EDA Tools such as Synopsys VCSMX, Verdi/ DVE; QuestaSim v10.1c, Xilinx ISE v12.1 and FPGA Spartan 3E, Virtex 5; Programming & Scripting with MATLAB, C & Perl.
- Working knowledge on Peripheral Interface & OnChip Protocols AMBA AXI/AHB, USB 3.x, SPI and UART.
- Working knowledge on complete Wind-Profiler Radar Sub-Systems and experience with Lab Equipments such as Signal Generator, Oscilloscope, Spectrum Analyzer and Network Analyzer.

#### Professional Experience:

- WIPRO Technologies Ltd., Bengaluru Designation: Team Lead
- Spanwave Technology Solutions Pvt. Ltd., Bengaluru Designation: Senior Engineer
- New Horizon College of Engineering., Bengaluru Designation: Teaching Staff (Lecturer)
- C2C Semiconductors Pvt. Ltd., Bengaluru Designation: Member Technical Staff

Duration: Jan 2015 to till date

Duration: July 2010 to Dec 2014

Duration: Feb 2009 to May 2010

Duration: April 2008 to Jan 2009

#### **Education Qualification:**

- M.Tech (Embedded Electronic Systems) from Visvesvaraya National Institute of Technology (NIT/REC), Nagpur (Maharastra, INDIA) during 2005 07.
- B.E. (Electronics & Communication Engineering) from Visvesvaraya Technological University, Belgaum (Karnataka, INDIA) during 2001 05.

Achievement: Qualified GATE - 2005 with 97.5 percentile (All India Rank - 915)

I hereby declare that the above particulars are true and correct to the best of my knowledge.

## Report on Expert Lecture

The department of ECE conducted an expert lecture on Synthesis of FPGA for the course System Design Using HDL by Mr Shashikanth Patil , Team Lead , WIPRO Technologies Ltd., Bengaluru on 13/04/2019. The guest speaker spoke about the basics of FPGA and other target devices. He also spoke about different types of FPGA's like xilinx, microsemi etc. But mainly he discussed about xilinx because we are using it in our labs. Firstly he spoke about the basics of verilog and VHDL and how these codes can be dumped into the FPGA. He introduced the students about practical applications of topics related to our core subjects. He started off with the difference between verilog and VHDL and why verilog is more widely used. Then he discussed about the syntax of verilog as well as the keywords and the different modelling styles in verilog. The he explained a few basic programs. He spoke about simulation and synthesis and also how the code can be converted to hardware by the process of mapping placing and routing.

He also showed us the process to dump the code in the hardware. After typing the code in the xilinx suit, then the syntax check to check if there were any errors, later after entering the ucf, he explained the process of simulation and how to map, place and route and then how to connect the hardware kit to the computer and then he taught us how to implement the written code on the FPGA kit. He also explained that verilog is a very important code in the core electronics companies and FPGA is widely used. He told us that the basics we learn are very important. He also told us the various applications of FPGA used in industry and the advantages and disadvantages as well.

Hence I conclude that the lecture was very helpful and helped the students gain a lot more interest in this topic. It was very explanatory and relatable. It made the students understand various topics practically.

# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "A" Sec

### Expert lecture attendance

Course: ECE43 System Design Using HDL

13/04/2019

SL.No	USN	NAME	Stream	Batch	Signature
1	1NH16EC038	KARROTU PRUDHVI	EXE	A1	oignatul e
2	1NH17EC001	AARTHY P JAITH	EXE	A1	
3	1NH17EC002	ADESH V	PROF	A1	
4	1NH17EC003	ADITYA OMPRAKASH CHOUDHARY	EXE	A1	
5	1NH17EC004	AKSHITHA R	EXE	A1	Ales itha
6	1NH17EC005	ANAND T	EXE	A1	TRAIN
7	1NH17EC006	ANAND T	PROF	A1	+
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9	1NH17EC008	ARUN Y S	EXE	A1	<del></del>
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24	1NH17EC023	DIVYA SAGAR REDDY	EXE	A2	Danja Hur
25	1NH17EC024	DUDDUKURI VAISHNAVI	EXE	A2	Cilly
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28	1NH17EC027	GAGAN B K	EXE	A2	Calif
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	63	1NH17EC128		UHAIL PASHA	I	EXE	A1	
6	64	1NH17EC129		ATHULA SRI SAI KRISHNA	E	XE	A1	
6	55	1NH17EC130		NISHA DEVI	E	XE	A1	
6	56	1NH17EC132		SHWIN S P	E	XE	A3	
6	7	1NH17EC132		URAKONDA AVINASH	E	XE	A1	
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# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "C" Sec

## Expert lecture attendance

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2	1NH16EC065	NILOTHPAL	EXE	C3	-
3	1NH16EC140	Karthik K	PROF	Ç3	1
4	1NH16EC701	A NAVYA SHREE	EXE	C3	Kount
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6	1NH17EC701	AJAY S	EXE	C1	-
7	1NH17EC702	AKSHAJA SHIMALNA O K	EXE	C1	K.Just
8	1NH17EC703	ARAVIND P M	PROF	C1	Marour
9	1NH17EC704	ASHIKA P	EXE	C1	11-0
10	1NH17EC705	BHARGAVI	EXE	C1	Han
11	1NH17EC706	CHANDU V J	PROF-G	C1	-
12	1NH17EC707	DHANUSH S	EXE	C1	
13	1NH17EC708	DHINAKARAN S	EXE	C1	
14	1NH17EC709	DISHANTH M P	PROF	C1	
15	1NH17EC710	G DEEPIKA	EXE	C1	0419
16	1NH17EC711	GAGANASHREE P S	EXE	C1	ADVAN
17	1NH17EC712	GRACE GLADYS A	EXE	C1	Gaer
18	1NH17EC713	HARSHITH NAIDU J	EXE	C1	This
19	1NH17EC714	J BHAVANA	EXE		Charana
20	1NH17EC715	JAYADEEP G P	EXE	C1	Joyadan
21	1NH17EC716	JILLELLA RUPASAGAR REDDY	EXE	C1	- Jugus
22	1NH17EC717	K AISWARYA	EXE	C1	Min)
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24	1NH17EC719	KOTEPALLI RAMCHARAN	PROF	C1	
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29	1NH17EC724	MUNAGALA BHARATH KUMAR	EXE	C2	
30	1NH17EC725	NAINI NARAMA	EXE	C2	
31	1NH17EC726	NAVEEN KUMAR M S	EXE	C2	
32	1NH17EC727	NEHA KRISHNA	EXE	C2	
33	1NH17EC728	NIDUJUVVI CHAKRADHAR	EXE	C2	
34	1NH17EC729	NIKHIL DWIVEDI	EXE	C2	

35	1NH17EC730	P K KANAKA SHARANYA	EXE	C2	210004
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37	1NH17EC732	PAVAN U	EXE	C2	
38	1NH17EC733	PRACHI VERMA	PROF	C2	
39	1NH17EC734	PRAJWAL P	EXE	C2	
40	1NH17EC736	RAHUL BALAJI P S	EXE	C2	R
41	1NH17EC737	RISHIKA POTEPALLI	EXE	C2	122
42	1NH17EC738	RITWIK SHOME	PROF	C2	
43	1NH17EC739	S DENILA GRACELIN	EXE	C2	Benlas
44	1NH17EC740	S RAHUL YADAV	EXE	C2	- George
45	1NH17EC741	S SAI HARSHA	EXE	C3	
46	1NH17EC742	SAHIB ARORA	EXE	C3	
47	1NH17EC743	SANDHYA K	EXE	C3	Sanethy
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66	1NH18EC411	Roja Parameshwar Ullikashi	Prof	C3	Cella
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AUTONOMOUS COLLEGE Permanently Affiliated to VTU, Approved by AICTE & UGC
Accredited by NAAC with 'A' Grade

## Notice

Date: 01-04-2019

All the VI semester students are hereby informed to attend the Guest Lecture on "EXCEPTION HANDLING" in Object Oriented Programming at "Chanakya Seminar Hall" at 9AM on 04/04/2019.

256

COORDINATOR

Ms. Rajashri Y M Sr. Asst. Prof. HODECE

Contact no: 8971568755

#### **Profile**

Over 6 years of experience in iPhone/iPad application development.

Experience in Swift (2yrs) and Objective C.

Proficient in Storyboard UI Design with Auto Layouts (Size classes).

Strong understanding of building applications that utilizes web-services (HTTP based, XML, JSON, SOAP or other services).

Hands on experience with Apple submission process, Test flight (beta) and the Adhoc app distribution.

Strong understanding of Push notifications.

Experience in integrating third party libraries Crashlytics, CorePlot..

Understanding of Apple's design principles and interface guidelines.

Experience with offline storage, threading, and performance tuning.

#### **Roles & Responsibilities**

Actively involved in design and development of an application.

- Worked closely with web service developers to discuss the data content is required for the application.
- Collaborated with cross-functional teams to define, design, and ship new features.
- Mentored junior developers.



#### **Work Experience**

Working as iOS Developer in Emids Technologies, Bangalore from Feb 2012 to till date. Previously worked as iOS developer in Numads Technologies, Bangalore from Nov 2010 to Jan

#### **Project Summary**

#### Project #1

Project Title: AlwaysOn Wellness

URL: https://itunes.apple.com/in/app/alwayson-wellness/id927685764?mt=8

#### Description:

AlwaysOn® Wellness connects you with your wellness program 24/7, so you can engage with your program anytime, anywhere. With AlwaysOn® Wellness, you can take your health assessment, connect with 80+ tracking devices and apps, track your wellness measurements and much more all in one place! Now, it is easier than ever to stay motivated.

#### Project #2

Project Title:

iRecycle Earth 911

Client: **Device:** 

iPhone/iPad

**URL:** 

https://itunes.apple.com/in/app/irecycle/id312708176?mt=8

#### Description:

iRecycle provides information for each location like hours of operation, physical address, web site, a listing of other materials they will accept. Over 110,000 recycling and disposal locations for over 240 materials all packed away in a handy little app. The app also provides access to the latest news, tips and features straight from Earth911.com.



#### Project #3

**Project Title:** 

Walgreens

Client:

Walgreens

Device:

iPad

URL:

https://itunes.apple.com/in/app/walgreens-for-ipad/id430444027?mt=8

#### Description:

Walgreens application to show the nearest Walgreens stores in the map, it shows weekly ads about Walgreens. Barcode reader for scanning the prescription number of the product and gives the details about the store. Walgreens makes printing and sharing pictures on the move fun with new easy-to-use features.

#### Project #4

259

Project Title:

Gurdwara Bangla Sahib

Device:

iDhono

**URL:** 

https://itunes.apple.com/gb/app/gurdwara-bangla-sahib/id485258866

#### **Description:**

This App is about a 'Historical Gurdwara' in the World, which has been created for tourists who visit the Holy Place in Delhi India. If you are ever visiting Delhi and do drop by the Gurdwara, please use this app to get an overview of the Gurdwara from its History to knowing about the religion in detail.

#### Project #5

**Project Title:** 

**Primary Care** 

Client:

**Netsmart Technologies** 

Device:

iPad

#### **Description:**

Primary Care aims to provide an iPad application for its physicians to review and input patient information into the Primary Care backend, which will then perform Medical Decision Making and present Risk Factors.

This project required me working on iPad application with a high end UI involving use of various controls like Scheduler views, Pull to refresh table views, Graphs, Maps and Web service interaction with the backend.

Place: Bangalore Muralitharan G

# NEW HORIZON COLLEGE OF ENGINEERING IPA TMENT OF ELECTRONICS & COMMUNICATION ENGINEERING VI Sem "A" Sec

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55	1NH16EC135	AKULA ANIL BHARATH	War
56	1NH16EC137	ANUPOJU SAI VENKAT THRIMURTHY	2111
57	1NH16EC138	ASHISH KUMAR . S	Shertly
58	1NH16EC139	KARTHIK. K. S	TO A
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9	1NH16EC076	PRAJWAL.T.J	gracial
10	1NH16EC077	PRANAY REDDY S	Bank
11	1NH16EC078	PRASHANTH GOWDA.R.S	Day
12	1NH16EC079	PRAVEEN KUMAR S	(8)
13	1NH16EC080	PRESHIKA J M	Dai
14	1NH16EC081	PRITHIPA A	Skelika
15	1NH16EC082	R.PAVAN RAJ	74
16	1NH16EC083	RAJVARDHAN	10-1-
17	1NH16EC084	RAKSHITHA N	Q.L
18	1NH16EC085	ROSHINI.M	Defle 6
19	1NH16EC086	RUSHAB.S.G	KONLENTA
20	1NH16EC087	SAI KIRAN	
21	1NH16EC088	SAI PRASHANTH	
22	1NH16EC089	S P SAAJU	cant.
23	1NH16EC090	SAGAR	
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28	1NH16EC096	SHRAVIN.R.SEKHAR	Via-
29	1NH16EC097	SHRIPAD AITHAL	pra - a
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31	1NH16EC099	JAGAN MOHAN REDDY	-1-0
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33	1NH16EC101	SINDHU C R	() Alex
34	1NH16EC102	SIVA CHALLA	STATE OF THE PARTY
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39	1NH16EC109	UDIT BAHUGUNA	
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44	1NH16EC115		
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	1NH16EC118	VISHWANATH VEERANNA	10/8/12
47	1NH16EC119	W M MAGDOOM FUAAD	Marcal
48	1NH16EC120	YATHIN S	
49	1NH16EC122	YESHWANTH J M	6
50	1NH16EC124	YESHWANTH,M	1.11
51	1NH16EC125	YESHWANTH.M.L	411
52	1NH16EC128	SIMON CHAUHAN	
53	1NH16EC129	ASHOK CHOUDHARY	S.IMP
54	1NH16EC130	M.V.K BHARATH	
55	1NH16EC131	NAMRATHA PRAKSAH	
56	1NH16EC132	PREM KUMAR	
57	1NH16EC133	SRIDHAR. P	0 14
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## NEW HORIZON COLLEGE OF ENGINEERING EPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERIN VI Sem "C" Sec

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# Guest Lecture on Exception Handling in C++ Conducted for VI<sup>th</sup> Semester Students

The EC Department had organized a guest lecture on 4<sup>th</sup> April 2019 about Exception handling in C++. The speaker for the lecture was Mr. Muralidharan G from eMids Technologies, having over 8 years of experience in iOS app development and design. He is currently making fitness applications for iPhones and iPads.

In his lecture the speaker started off by giving a brief introduction on what exception handling is, why it is important for app developers & how it affects real world situations. The session was very interactive, the speaker gave us multiple code snippets throughout the lecture for us to debug exceptions and how to use them in programs so it gives a better user experience. He showed us real world examples on how exception handling is implemented.

He discussed the various statements that can be used for exceptions in C++, their syntax, where it is implemented, examples and how it differs in other programming languages. He also told us how exception handling played a very important role in the backend of app development. Towards the end of the session the speaker gave all the students a task to make a class with exceptions such as try, throw and catch with different conditions.

The session was ended with a vote of thanks and a felicitation to the speaker by the teacher coordinators.

## NEW HORIZON COLLEGE OF ENGINEERING

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

08/02/2019

### **GUEST LECTURE**

This is to inform to IV semester students of ECE that the guest lecture is arranged on 9/02/19 at Falconary Seminar Hall between 2.50PM to 4.50PM.

Topic to be delivered: "VLSI - Transistor to Transformation"

Name of the Speaker: Mr.Shriram S

Designation : Hardware Design Engineer

Company : Intel Technologies, Bangalore

Respective subject teachers are hereby informed to be present at Falconary Seminar Hall during the lecture.

All the faculty members are invited to attend the same without affecting the regular classes.

M. Dharmanh 8/2/19
Guest Lecture Coordinator

HOD-ECE

From the desk of:

Prof & HOD - Department of Electronics and Communication Engineering

02-01-2019

To, The Principal, NHCE NHCE/ECE/LTR/001/2019

Dear Sir,

Sub: Request for Falconry Hall on 9<sup>th</sup> February 2019, from 2.00pm to 4.00pm -reg

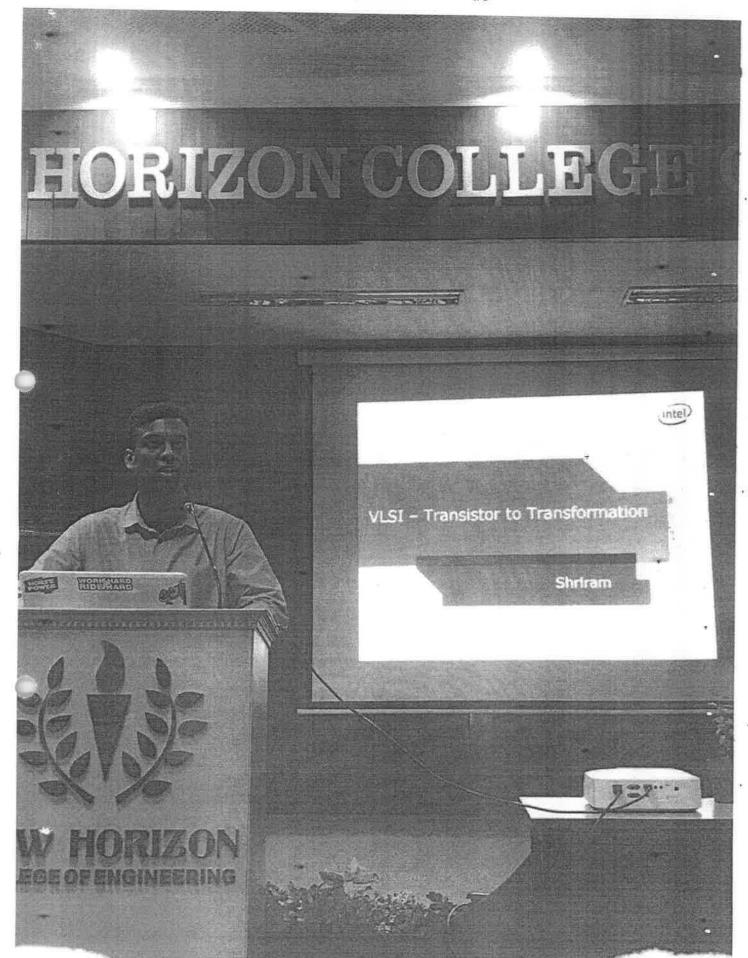
With reference to the above subject, guest lecture is scheduled on 9<sup>th</sup> February 2019 from 2.00pm to 4.00pm. I am hereby requesting you to permit us to use Falconry Seminar Hall on the above mentioned date and time.

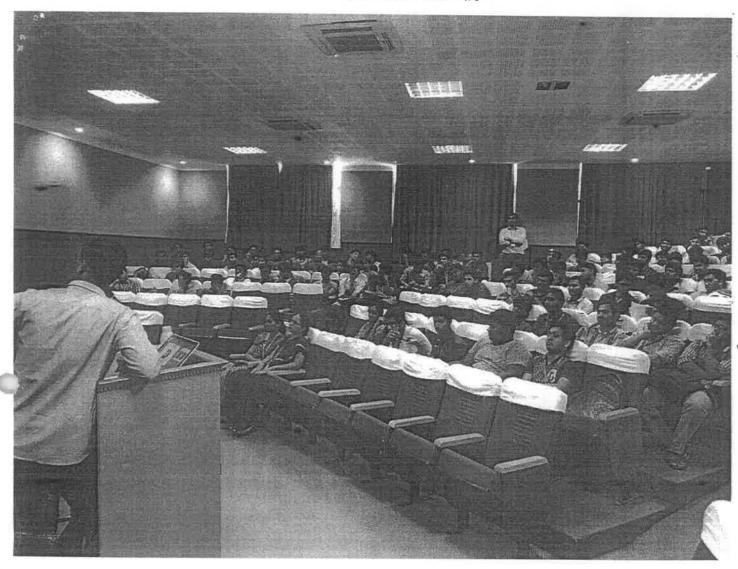
Thanking you

With Regards,

HOD-ECE

NEW HORIZON COLLEGE OF ENGINEERING





## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "B" Sec

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Note: Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "C" Sec

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Note: Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "A" Sec

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21	1NH17EC020	DEEPAK S	PROF-G	A2	Deckshith.
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68	1NH17EC133		URPUNATI SURYA PRAKASH	EX	Œ	A1	117	FIFT
69	1NH17EC135		KARTHIK P R	PRO	OF	A1	100	SUZY
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Note: Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

NAME OF THE EVENT/CLUB: VLSI - FRANSISTOR TO TRANSFORMATION

FEEDBACK ON (Mention the topic): VLST - Treansistor 70 Trongboard PATE: 9/2/19 Instruction: Please put a tick mark at the appropriate column.

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	Clarity	Clarity Visuals  /	Clarity Visuals Skills	Clarity Visuals Skills Guidance

Comments/Remarks/Suggestions:

1. The lecture was very informative and interesting we would like to have more of such audio-visual entrode in the future.

3.

Name & USN No.(optional)

NAME OF THE EVENT/CLUB: VLSI - transfer to transformation

FEEDBACK ON (Mention the topic): VISI - transistor to transformation DATE: 09/02/2019 Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Orranall
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Average				*	
Satisfactory			18		

Comments/Remarks/Suggestions:

1. It was really good leature on VLSI, and I expect more 2. lectures on these kinds of topics.

3.

Name & USN No.(optional)

Crust lecture

### NAME OF THE EVENT/CLUB:

FEEDBACK ON (Mention the topic): VLSI\_Transitor toqueting DATE: 9 -02-2019 Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

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Name & USN No.(optional)

NAME OF THE EVENT/CLUB: VLSI - Transiston to transformation

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Comments/Remarks/Suggestions:

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Name & USN No. (optional) SATHYAPRASAD, G.P. -IN +18 EC415



NAME OF THE EVENT/CLUB: VLSI - Transistor to Transformation

FEEDBACK ON(Mention the topic):

DATE: 09-02-2019

Instruction: Please put a tick mark at the appropriate column.

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Name & USN No.(optional)

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FEEDBACK ON(Mention the topic):

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NAME OF THE EVENT/CLUB: VLSI - transitor to transformation

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Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

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Comments/Remarks/Suggestions:

Very informative, would have been more interactive if it was a maning Version

3.

Name & USN No.(optional)

VLSI- Transistor to Transformation

Speaker: Mr. Sriram, Intel Corporation

Technology has evolved exponentially over the last century, from the invention of the 1st transistor in 1947 to having several billion transistors on a single chip today, all in accordance to Moore's Law.

Over the years, technology progressed from Small scale integration to medium scale integration to large scale integration and we currently are in the very large scale integration era. This term was coined over 40 years ago though we pushed the size a transistor even further down.

History of Integration

Jack Kilby of Texas Instruments patented the principle of integration, created the first prototype ICs and commercialized them.

Kurt Lehovec of Sprague Electric Company invented a way to electrically isolate components on a semiconductor crystal.

Robert Noyce of Fairchild Semiconductor invented a way to connect the IC components (aluminium metallization) and proposed an improved version of insulation based on the planar technology by Jean Hoerni.

FinFET Technology

Standard 32nm MOSFETs were the predecessors of the FinFETs. The MOSFETs were now large and to keep up with Moore's Law, the FinFETs were developed. The FinFETs that are present in Apple's A12 Bionic chip uses the 7nm technology. The term 7nm used to once describe the width of the channel but in today's context, 7nm indicates the degree of precision achievable or the smallest change that can be made inside the transistor.

The challenge to keep up with Moore's Law is ever growing the technology today is so sophisticated that our transistors are close to the size of a single Si atom i.e., 0.23nm. Intel is currently working with the 7nm technology, along side research to shrink the size of the transistor even further down.

VLSI design flow

Speaking of a billion 7nm transistors on a 3cm x 3cm chip, how is it actually done?

This process can be classified into 2 main blocks, Design and Synthesis.

In Depth, there are 10 steps in fabricating an Integrated Chip

- 1. Design Entry- Designing the model in a HDL design environment using VHDL or Verilog.
- 2. Logic Synthesis- Converting the design to a Gate Level Diagram
- 3. System Partitioning- Dividing the existing blocks of the design into smaller blocks
- 4. Pre-layout Simulation- To simulate the circuit, check the layout and obtain a desired output.
- 5. Floor Planning- assigning different blocks a specific place on the chip

- 6. Placement- Placing these blocks on the chip
- 7. Routing- Connecting these blocks and isolating the metal layers
- 8. Circuit Extraction- Removing additional and other parasitic elements.
- 9. Post Layout simulation- Testing the design
  - 10. Synthesis- Implementing the design on an actual Si Chip

# NEW HORIZON COLLEGE OF ENGINEERING

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

23/10/2018

### **GUEST LECTURE**

This is to inform VII semester students of ECE that the guest lecture is arranged on 24/10/18 at Falconry Seminar Hall between 11AM to 1PM.

Topic to be delivered:

Career growth in Electronics Industry

Name of the Speaker:

Bipin Malhan

Designation

**Managing Director** 

Company

**INVECAS Technologies** 

Respective subject teachers are hereby informed to be present at Falconry Seminar Hall during the lecture.

All the faculty members are invited to attend the same without affecting the regular classes.

Guest Lecture Coordinator

HOD-FCF





### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "A" Sec

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3	1NH16EC003	ADITYA C	Holana.
4	1NH16EC004	AFZAL HUSSAIN	Amel
5	1NH16EC005	AKHIL JOSEF	N2m
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32	1NH16EC039	KIRAN N	
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63	1NH17EC423	RAJESHA.T.A	1000
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### EPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING VII Sem "B" Sec

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1	1NH13EC138	D Ramya	Ramy
2	1NH13EC729	Mohammed Sabah . M	, act of
3	1NH14EC037	Geetha M	
4	1NH14EC132	Srustik Subhash	
5	1NH14EC147	Yerrabapu Rohit Reddy	
6	1NH14EC722	Nishanth Reddy K	
7	1NH14EC723	Nithya V	
8	1NH15EC002	Abburi Greeshma	100
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13	1NH15EC009	Anusha M	10 200000
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17	1NH15EC013	Chandini C	(1. 10 E
18	1NH15EC014	Chandra Shekhar.K.R	Chamarine
19	1NH15EC016	Deepak S	
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24	1NH15EC027	Hemanth Kathick R	Harshitha.
25	1NH15EC027	Hemanth Kumar.R	
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42	1NH15EC056	Nagarjun K S	Magagur. Kul
43	1NH15EC057	Nalini.K	11/00/9
44	1NH15EC058	Priyanka Nallagouni	
45	1NH15EC060	Naveen.K.R	av -t
46	1NH15EC061	Naveen.P	
47	1NH15EC070	Venkata Sai ganesh	

48	1NH15EC071	Preeti .K.Mehtry	
47	1NH15EC073	Punarva.A	
50	1NH15EC077	Radhakrishna.C	
51	1NH15EC081	Rukmini.N	Rukminia
52	1NH15EC085	Saahil Rai	CAAD
53	1NH15EC086	Sachin Yaday	Sugher
54	1NH15EC089	Samarpan Chakraborty	Suggest
55	1NH15EC102	Shubha.A	
56	1NH15EC113	Subramanaya Ganesh	Suho, 1, 0, 0.
57	1NH15EC117	Swathi.R	Sublange family
58	1NH15EC121	Tirumala Satheesh Himakeerthi	9
59	1NH15EC132	Deepti S	
60	1NH15EC135	Archana M	to Show
61	1NH15EC435	Thyagaraj M	Orane
62	1NH15EC703	Ashwin S	
63	1NH15EC714	Jishma Asmi. V	
64	1NH15EC724	Prasanna Kumar.D	Place Ku D
65	1NH15EC755	Syed Zahid Ulla	1 - willow po
66	1NH16EC401	Dilip Kumar R	
67	1NH16EC411	Madhavi Iranna Sajjan	
6.	1NH16EC412	Mohammed Faizulla D K	
69	1NH16EC413	Mohan Kumar V	
70	1NH16EC415	Namitha Reddy V	
71	1NH16EC417	Padmini M E	
72	1NH16EC419	Praveenkumar R Jadhav	
73	1NH16EC420	Priyanka P	
74	1NH16EC423	Ramitha B	
75	1NH16EC426	Sachin K B	
76	1NH16EC427	Shashikumar M R	
77	1NH16EC428	Sowmya P	

#### NEW HORIZON COLLEGE OF ENGINEERING EPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERIN VII Sem "C" Sec

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1	SL.No	USN	NAME	
3		1NH14EC026	D Vedavathi Kiranmai	
1.00		1NH14EC052	Priyanka Milton	
1	3	1NH15EC020		
5         1NH15EC041         Kolli Srinivasa Reddy           6         1NH15EC042         Konuru Gopi           7         1NH15EC044         Madala Kalyan Kumar           8         1NH15EC055         Nagabhyru Kushvanth Chowdary           9         1NH15EC064         Noti DineshKumar Reddy           10         1NH15EC069         Pavan Kumar S           11         1NH15EC069         Pavan Kumar S           12         1NH15EC074         Putti Reddy Akhil Reddy           13         1NH15EC082         Rushali Raina           14         1NH15EC084         Rushali Raina           15         1NH15EC084         Sakkirala Satwik Yadav           16         1NH15EC094         Sanjay Kumar-A           19         1NH15EC090         Sanjay Kumar-A           20         1NH15EC091         Sanjeev Jayasurya.S           21         1NH15EC092         Sanjeev Jayasurya.S           22         1NH15EC093         Shaik Sohail           22         1NH15EC094         Shaik Sohail           23         1NH15EC095         Shrigas           24         1NH15EC096         Shreyas S           25         1NH15EC101         Shreta P           28		1NH15EC021		
6         1NH15EC042         Konuru Gopi           7         1NH15EC044         Madala Kalyan Kumar           8         1NH15EC055         Nagabhyru Kushvanth Chowdary           9         1NH15EC059         Nanditha S           10         1NH15EC064         Noti DineshKumar Reddy           11         1NH15EC064         Noti DineshKumar Reddy           12         1NH15EC074         Putti Reddy Akhil Reddy           13         1NH15EC080         Roopa D Almeida           14         1NH15EC081         Rushali Raina           15         1NH15EC084         Sakirala Satwik Yadav           16         1NH15EC094         Sakirala Satwik Yadav           17         1NH15EC091         Sanjaev Jayasurya.S           18         1NH15EC091         Sanjeev Jayasurya.S           20         1NH15EC092         Sanjeev Jayasurya.S           21         1NH15EC094         Satish.J           22         1NH15EC096         Shaik Sohail           23         1NH15EC096         Sharath.N           24         1NH15EC098         Shilpa.V           25         1NH15EC101         Shruti Jana           26         1NH15EC103         Shea P           31	5	1NH15EC041		
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### REPORT ON CAREER GROWTH IN ELECTRONICS INDUSTRY

## Future Job Scope of Electronics and Communication Engineering

Even imagining a life bereft of electronic gadgets seems impossible in today's world. There is no field left across the globe where one cannot find the usage of electronics and communication engineering. Perhaps that is why electronics have become the vertebrae of digital technology. However, as a student planning to pursue Electronics and Communication Engineering (ECE) courses in India, one needs to walk around and try to see things from all frames.

Undoubtedly, selecting a branch of engineering from myriad options is like searching a needle from a needle stack. It is bewildering and that is why most of the students end up making the wrong choice. To avoid this, here the potential of ECE courses in paving a successful career has been discussed. So, drive through the write-up and unfold the different chords attached to ECE courses.

### **Gleaming Future Prospects of ECE**

Communication Engineering courses and what makes it an ideal option.

It is predicted that the jobs for engineers will grow by 7% from 2016 to 2026. Furthermore, it has also been found that the employment in the ECE sector has amplified significantly in the last few years. The reason behind this positive change of wind is the growing nexus between the electronics industry and the digital technology. The application of ECE in fields, such as satellite and mobile communication, digital telecommunication, power electronics, etc. has created amazing career options. Apart from this, manufacturing companies, Multinational Corporations (MNCs), research-based or industrial corporations, government authorities, aerospace manufacturing companies, consumer electronics manufacturing, healthcare tools manufacturing, Armed forces (Army/ Air Force/Navy), many others look for candidates with ECE background.

ECE engineers expertise in managing large scale research, conceptualizing, designing, developing and testing of the electronic equipment used in latest communication devices and other handy technological equipment, such as smartphones, tablets, processors, smart wrist watches, smart LED TV's, etc. has only led to the surge in their demand.



#### 1. Electronics Engineer

With the emerging rise in the electronics industry, new leading firms are springing up to meet the needs of the consumer market. The profile of electronics engineer has a great demand in the market and this has created a lot of opportunities for the ECE aspirants.

### 2. Electronics and Communication Engineer

ECE students could also pursue in the communications, networking, and broadcast, transmission systems work area. This work is mainly targeted to consolidate electronics, conceptualize the communication network framework, and ensure its flawless execution.

#### 4. Desktop Support Engineer

The profile of Desktop Support Engineer is required in almost every IT and non-IT company. This means there will be no scarcity of job options for ECE graduates in this position.

#### 5. System Control Engineer

System control engineer job includes not only the designing of complicated systems but also testing of it. This is one of the reasons why the candidates for this profile are recruited as technical supervisors, system control managers, etc.

#### Winding Up

As aforementioned, one can see that electronics are a part of our everyday life, and the ECE field shows no—signs of slowing down in the near future. This is a blessing for aspiring ECE graduates, as it opens up endless opportunities for them in top-notch manufacturing and IT industries. The only thing that one needs to do is to select the right place to study Electronics and Communication Engineering curses, which will determine their fate in the mainstream.

# NEW HORIZON COLLEGE OF

## **ENGINEERING**

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION **ENGINEERING**

20/09/2018

### **GUEST LECTURE**

This is to inform to V semester students of ECE that the guest lecture is arranged on 22/09/18 at Chanakya Seminar Hall between 10AM to 11AM.

Topic to be delivered: Robotics: History, current Trends, Future

Name of the Speaker : Akshatha Pai

Designation

: Project Manager

Company

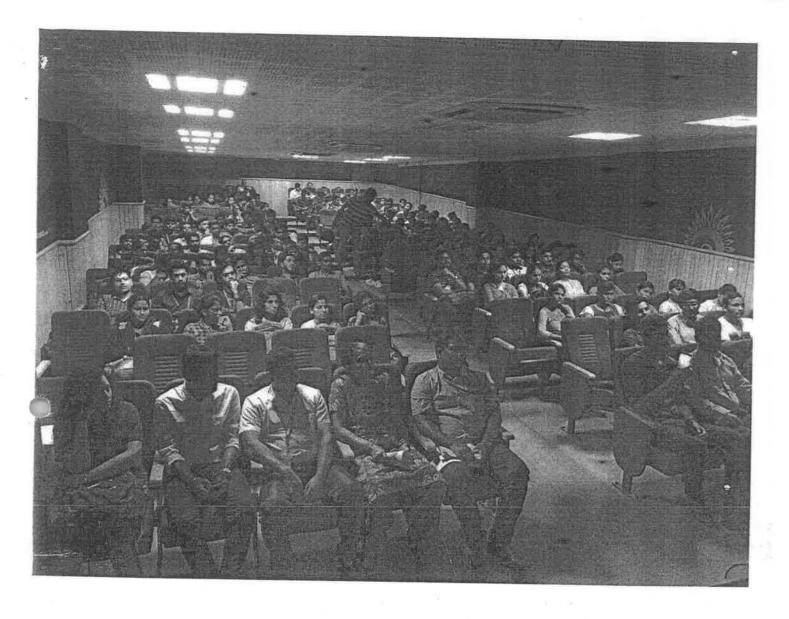
: Sirena Technologies

Respective subject teachers are hereby informed to be present at Chanakya Seminar Hall during the lecture.

All the faculty members are invited to attend the same without affecting the regular classes.

Guest Lecture Coordinator





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# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "A" Sec

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		SUPRITHA H.D	EXE	A1	
			EXE	A3	1-

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#### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem"B" Sec

SL.No	USN	NAME	Stream	BATCH	7,
1	1NH16EC064	NEHA MAHESH	EXE	B1	relendes
2	1NH16EC066	NISHU DUBEY	EXE	B1	I to use
3	1NH16EC067	NITESH K	PROF	B1	No Jeeh . k
4	1NH16EC068	NITHIN.E	EXE	B1	MIND
5	1NH16EC070	MAHALAKSHMI P	EXE	B1	Mahalakahmi
6	1NH16EC071	DEEPAK P S	EXE	B1	Deapek P.S
7	1NH16EC072	PARITHOSH VEMA	PROF	BI	
8	1NH16EC075	POOJA SHANTARAM NAYAK	EXE	B1	Parach
9	1NH16EC076	PRAJWAL.T.J	PROF	B1	any and
10	1NH16EC077	PRANAY REDDY S	EXE	B1	0 - 21 B
11	1NH16EC078	PRASHANTH GOWDA.R.S	PROF	B1	Varalhanturis
12	1NH16EC079	PRAVEEN KUMAR S	PROF	BI	(9, 4)
13	1NH16EC080	PRESHIKA J M	EXE	B1	Proshit
14	1NH16EC081	PRITHIPA A	EXE	B1	Pol-
15	1NH16EC082	R.PAVAN RAJ	PROF	B1	-
16	1NH16EC083	RAJVARDHAN	EXE	B1	Rajvanle
17	1NH16EC084	RAKSHITHA N	EXE	B1	10-L
18	1NH16EC085	ROSHINI.M	EXE	BI	Dal
19	1NH16EC086	RUSHAB.S.G	EXE	Bl	101
20	1NH16EC087	SAI KIRAN	EXE	B2	o due
21	1NH16EC088	SAI PRASHANTH	PROF	B2	\$ E
22	1NH16EC089	S P SAAJU	EXE	B2	& con the
23	1NH16EC090	SAGAR	EXE	B2	50/
24	1NH16EC092	SANNAPANENI PAVANI	GLOBAL	B2	& Paurant
25	1NH16EC093	SHAIK NOORULLA BASHA	EXE	B2	ON BUS
26	1NH16EC094	SHAIKH ASIF	EXE	B2	Sid
27	1NH16EC095	SHASHANK B	EXE	B2	8
28	1NH16EC096	SHRAVIN.R.SEKHAR	EXE	B2 (	We -
29	1NH16EC097	SHRIPAD AITHAL	EXE	B2	1 De la constante de la consta
30	1NH16EC098	SIDDESH JALAGERI	EXE	B2 (	100
31	1NH16EC099	JAGAN MOHAN REDDY	EXE	B2	五月
32	1NH16EC100	SIDRAMAPPA	EXE	B2	demalla
33	1NH16EC101	SINDHU C R	EXE	B2	arrothy
34	1NH16EC102	SIVA CHALLA	EXE	B2	Will
35	1NH16EC103	SPOORTHY.G	PROF	B2	mould
36	1NH16EC105	SUSHMA CHIKKUR	PROF	B2	OT BE
37	1NH16EC106	T.E.HABISHEK	PROF	B2	Bank
38	1NH16EC107	T.SAI DEEPTHI	EXE	B2	Oubl.

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39	TIVITOEC109	UDIT BAHUGUNA			3
40	TAHTOECTIO	VARUN MISHRA	EXE	B2	Udit ( Bed
41	TIVITIOECT 12	VEENA K	EXE	В3	Quin
42	1NH16EC113	VIDYA VISHNU	EXE	B3	
43	1NH16EC114	VIJAY C	PROF	B3	Ofu
44	1NH16EC115	VIJAY KUMAR C	EXE	B3	may ?
45	INH16EC116	VIJAYS	EXE	B3	
46	1NH16EC118		PROF	В3	1 Com
47	1NH16EC119	VISHWANATH VEERANNA	PROF	В3	Tun
48	1NH16EC120	W M MAGDOOM FUAAD YATHIN S	GLOBAL	B3	000×14
49	1NH16EC122		PROF	B3	97.5
50	1NH16EC124	YESHWANTH J M	EXE	B3	The To
51	· 1NH16EC125	YESHWANTH.M	EXE	B3	- Yeshirana O'
52	1NH16EC128	YESHWANTH.M.L	EXE	B3	- 79
53	1NH16EC129	SIMON CHAUHAN	EXE	B3	784
54	1NH16EC130	ASHOK CHOUDHARY	GLOBAL	B3	M
55	INH16ECi31	M.V.K BHARATH	GLOBAL		- Janasa
56		NAMRATHA PRAKSAH	PROF	B3	Much
57	1NH16EC132	PREM KUMAR	EXE	B3	Namy
58	1NH16EC133	SRIDHAR. P		B3	700
	1NH17EC401	AKSHAY GAMBHIR SADALAGI	EXE	В3	Pan
59	1NH17EC407	GURURAJ SHIVASHANKAR NATIKAR	EXE	B3	Algebe
60	1NH17EC410	JYOTHI CHANDRASHEKAR	EXE	B2	G.S. NWIKEL
61	1NH17EC426	SACHIN. M	2ROF	_ D2	Tythoc.
62	1NH17EC427	SHAMBULING V. PATIL	EXE	В3	Solhin M
63	1NH17EC430	SUSHMITHA	EXE	В3	
			EXE	В3	Quehmitte

## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "C" Sec

SL.No	USN	NAME	Stream	BATCH	
1	1NH14EC001	Abhijith S Raj	EXE	C3	Aphi Ra
2	1NH14EC019	BHARATH P M	EXE	C3	
3	1NH14EC082	NAGA SIVA KUMAR REDDY	EXE	C3	1
4	1NH14EC717	KEERTHANA A	EXE	C3	Ran
5	1NH14EC726	P Likith Chowdary	EXE	C3	dilit
6	1NH14EC748	Narendra Kumar U	EXE	C1	1,02
7	1NH15EC007	A VIKRAM KUMAR REDDY	EXE	C1	1
8	1NH15EC024	HARSHA E	EXE	C1	1
9	1NH15EC063	NISHANT S	PROF	C3	
10	1NH15EC072	Pulibandla Harish	EXE	C2	1
11	1NH15EC075	R RAGHAV SRIVATSAV	PROF	C3	
12	1NH15EC093	Sare Sai Sandeep	EXE	C2	o As
13	1NH15EC434	SUDHANYA S	EXE	C1	J. J.
14	1NH15EC701	ABHISHEK VP	EXE	C1	
15	1NH15EC707	Deepak Tomslee	EXE	C1	
16	1NH15EC715	Manoj Chandrashekar	EXE	C1	
17	1NH15EC717	Venkatasai Balakrishna	EXE	C2	
18	1NH15EC720	Pavan Kumar S	EXE		meanteur
19	1NH15EC736	RISHAV PAUL	EXE	C3	Qi Ban Pa
20	1NH15EC747	Shreyas.H	EXE	C2	B0.40.05
21	1NH15EC759	Vishnu.N.U	EXE	C2	
22	1NH16EC430	Vijay Kumar Y Valmiki	Prof	C1	
23	1NH16EC702	ABHISHEK KHOT	EXE	C3	Q-1-
24	1NH16EC703	ABHISHEK PATTAJOSHI	EXE	C3	N. Sw
25	1NH16EC704	ABU BAKAR SIDDIQ	EXE	C3	OK.
26	1NH16EC705	AKSHAY.V	EXE	C3	Jongen
27	1NH16EC706	AMMIDAL VISHNU VIKAS	EXE	C3	della
28	INH16EC707	ANITA CHAUHAN	EXE	C3	Miche
29	1NH16EC708	ANJU GOPINATH	EXE	C3	Ansch
30	1NH16EC709	ASHUTOSH MANISH	PROF	C3	M. J. 81
31	1NH16EC710	B VAMSHI KRISHNA REDDY	EXE	C3	1012 2-
32	1NH16EC711	BALAJI L	PROF	C3	L. Balaji
33	1NH16EC713	CHIRAG S	EXE	C3	(Many
34	1NH16EC714	GAGANA M R	EXE	C3	Carewa
35	1NH16EC715	GOWRI SNEHA PRIYA S	EXE	C3	Cala
36	1NH16EC716	GURRAM VENKATA NIKESH	GLOBAL	C3	alvia.
37	1NH16EC717	HARSHITA.P	EXE	C3 2	De or
38	1NH16EC718	JEEVAN.K	EXE	C3	3
39	1NH16EC719	MITHUN KUMAR	PROF	C2	HALL
40	1NH16EC720	KIRAN SAGAR N P	EXE	C2 .	Luly
41	INH16EC722	LINGRAJ JAMKHANDI	PROF	C2	To at

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42	1NH16EC723	N. A. MOYY.			- 1 - 1 - M
43		M ANISHA	EXE	C2	Anusuci
44	1NH16EC724	M PRIYADARSHINI	EXE	C2	Hitelie
45	1NH16EC725	MSVV RITVIK	EXE	C2	Naturas
46	1NH16EC726	MANJUNATH T	EXE	C2	A Dan 1
_	1NH16EC727	MANSOOR ELAHI	PROF	C2	Jest de
47	1NH16EC728	MOHAMMED ZEESHAN ALI	GLOBAL	C2	Leady
48	1NH16EC729	SIVA NAGAMUNI REDDY	PROF	C2	Selva-
49	1NH16EC730	MOUNIKA E	EXE	C2	Mourilees
50	1NH16EC731	THILAK PRASAD	PROF	C2	
51	1NH16EC733	NAWAZ KHAN	PROF	C2	James
52	1NH16EC734	NISHA ANANDU NAIK	EXE	C2	Queits
53	1NH16EC735	NIVEDITHA N	EXE	C2	nl Diha
54	1NH16EC736	NIVEDITHA R	PROF	C2	nividahar
55	1NH16EC738	PAVITHRA.N	PROF	C2	Paville
56	1NH16EC739	PAWAN KUMAR SRIVASTAVA	EXE	C2	1
57	1NH16EC740	POOJA P CHOUHAN	EXE	C1	Roofs
58	1NH16EC741	PRAVEEN KUMAR	PROF	C2	The state of the s
59	1NH16EC742	RACHANA.S	PROF	C2	Yarlan
60	1NH16EC743	K RAGHAVENDRA	EXE	C2	1
61	1NH16EC744	RAMYA.R	EXE	C2	Parent
62	1NH16EC745	REDDY SHEKAR.B.S	EXE	C3	12.00
63	1NH16EC746	RUCHIKA PRADEEP	PROF	C1	To like
64	1NH16EC747	S.SARAVANA	EXE	C3	31.72-
65	1NH16EC748	S SHYAM	EXE	C3 _	1 Ohman
66	1NH16EC749	SHRIYA.G	EXE	C1	Shriya
67	1NH16EC750	SINDHU A	PROF	C1	Sindly
68	1NH16EC751	SMITHA B S	EXE	C2	1.4
69	1NH16EC752	SWATHI K	EXE	C1	Swint ha K
70	1NH16EC753	T.R. BHOOMIKA	PROF	C1	Bhl
71	1NH16EC754	TARUN SAI REDDY	EXE	C3	Tour
72	1NH16EC755	ULTHI KEDARNATH	EXE	CI	de broth
73	1NH16EC756	YESHASWINI K M	EXE	CI	Marchan as
74	1NH16EC757	YUVASHREE R	GLOBAL	Cl	D
75	1NH16EC758	PRAVEEN.S	PROF	Cl	ynamic.
76	1NH16EC759	SHASHANK RAO.M	EXE	C1	1 al Lak
77	1NH17EC404	CHIRANJEEVI . K	EXE		000
78	1NH17EC405	B.N CHITHRASREE	EXE	Cl	O . I. M. Jac. May
79	1NH17EC408	JANARDHANA . T	PROF	C1	8-winning
80	1NH17EC409	JAYANTH . V		C1	Tool
81	1NH17EC411	B. J KOWSALYA	EXE	C1	SAID
82	1NH17EC415	MANASA . C	EXE	C1	SW.C
83		MANOJ.R	EXE	C1	Maria
84	1NH17EC418	NAVEEN KUMAR. J	EXE	C1	Monay K.
85	1NH17EC418	RAGHU.R	EXE	C1	1 Sich
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NAME OF THE EVENT/CLUB: Robots

FEEDBACK ON (Mention the topic): Nivo

DATE: 22(8) (8

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Comments/Remarks/Suggestions:
1. It can put to make Use in outer Space

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Name & USN No. (optional)

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Comments/Remarks/Suggestions:

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1	m4 - 0 -	demonstrations
	-aruse	Annon I Vallery
		Control La Control

2. more on coding.
3. more interactions, Bublic test.

Name & USN No.(optional)

Kishore N (INH 16 EQ40)

Signature (optional) Kishole N

NAME OF THE EVENT/CLUB: Returne lecture	
FEEDBACK ON (Mention the topic): Rollation	DATE: 00 0 10

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent	/	138			
Good				-	
Average		1.2	*		
Satisfactory					

Comments/Remarks/Suggestions:

1. More interaction with students

2. More visual demonstrations

3.

Name & USN No. (optional) Kiran N(INH 16 EC 039)

Instruction: Please put a tick mark at the appropriate column.

Signature (optional) Koran

NAME OF THE EVENT/CLUB: Robotics

FEEDBACK ON(Mention the topic):

DATE: 22/09/18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Comments/Remarks/Suggestions:

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Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Robotius

**FEEDBACK ON(Mention the topic):** 

DATE: 22/ 09/2018

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Comments/Remarks/Suggestions:

information to all. Thank you for in information cloud 2.

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Charran. S. Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Robotic

FEEDBACK ON(Mention the topic):

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Instruction: Please put a tick mark at the appropriate column.

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Instruction: Please put a tick mark at the appropriate column.

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Comments/Remarks/Suggestions:

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Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Robotics

FEEDBACK ON(Mention the topic):

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Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
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Satisfactory		ii l			

Comments/Remarks/Suggestions:

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Name & USN No.(optional)

### REPORT ON ROBOTICS

A **robot** is a machine—especially one programmable by a computer capable of carrying out a complex series of actions automatically. Robots can be guided by an external control device or the control may be embedded within. Robots may be constructed to take on human form but most robots are machines designed to perform a task with no regard to how they look.

### HISTORY OF ROBOTS

The word "robot" conjures up a variety of images, from R2D2 and C3PO of StarWars fame. Some people may alternatively perceive robots as dangerous technological ventures that will someday lead to the demise of the human race, either by outsmarting or outmuscling us and taking over the world, or by turning us into completely technology dependent beings who assively sit by and program robots to do all of our work. Russian-born American science-ction writer Isaac Asimov first used the word in 1942 in his short story "Runabout." Asimov had a much brighter and more optimistic opinion of the robot's role in human society than-did Capek. He generally characterized the robots in his short stories as helpful servants of man and viewed robots as "a better, cleaner race."

### **CURRENT TRENDS IN ROBOTICS**

- 1. Robot Training: Al is making robots easier to train, which in turn makes them a more viable investment for smaller companies as it reduces the cost of installation, training and ongoing programming.
- 2. 3D Vision: Even the simplest tasks a robot performs will depend on 3D machine vision to feed data into AI technology
- 3. Cloud Robotics: Robotic deep learning using image classification and speech recognition often relies on huge datasets with millions of examples. Cloud robotics allows intelligence to be shared across all robots in a connected environment.

### FUTURE TRENDS IN ROBOTICS

- 1. Adoption of Industrial Internet of Things (IIoT) Technology: Robots will increasingly deploy smart sensors at the edge of production to collect data previously inaccessible to manufacturers. This trend is currently underway and will lead to new levels of productivity and efficiency.
- 2. Industrial Cybersecurity as a Priority: As robots become more connected to internal systems for data collection, the cybersecurity risks increase. Manufacturers will be forced to address vulnerabilities in their processes and invest heavily in cybersecurity to ensure safe, reliable production.

- 4. Open Automation Architectures Will Be Implemented: As robotic automation gains widespread adoption, the need for open automation architectures grows. Large industry players will work with industry organizations to produce standards and open documentation that make robotic integration easier while improving product compatibility.
- 6. Collaborative Robots Will Continue to Grow in Popularity: Collaborative robots can work safely alongside humans and are often far cheaper than their industrial counterparts. As collaborative robots become more capable in tough industrial settings, they will see greater adoption by manufacturers with strict ROI requirements.

## NEW HORIZON COLLEGE OF **ENGINEERING**

Department of Electronics and Communication Engineering

18/8/2018

## **GUEST LECTURE**

This is to inform to VII semester students of ECE that the guest lecture arranged on 18/8/18 at Falconry Seminar Hall between 11AM-1PM.

Topic to be Delivered: "PATHWAY TO SEMICONDUCTOR INDUSTRY"

Name of the Speaker: Ms. Neha Bharti

Designation:

**ASIC Design Engineer** 

Company:

Altran Technologies India Pvt. Ltd., Bangalore

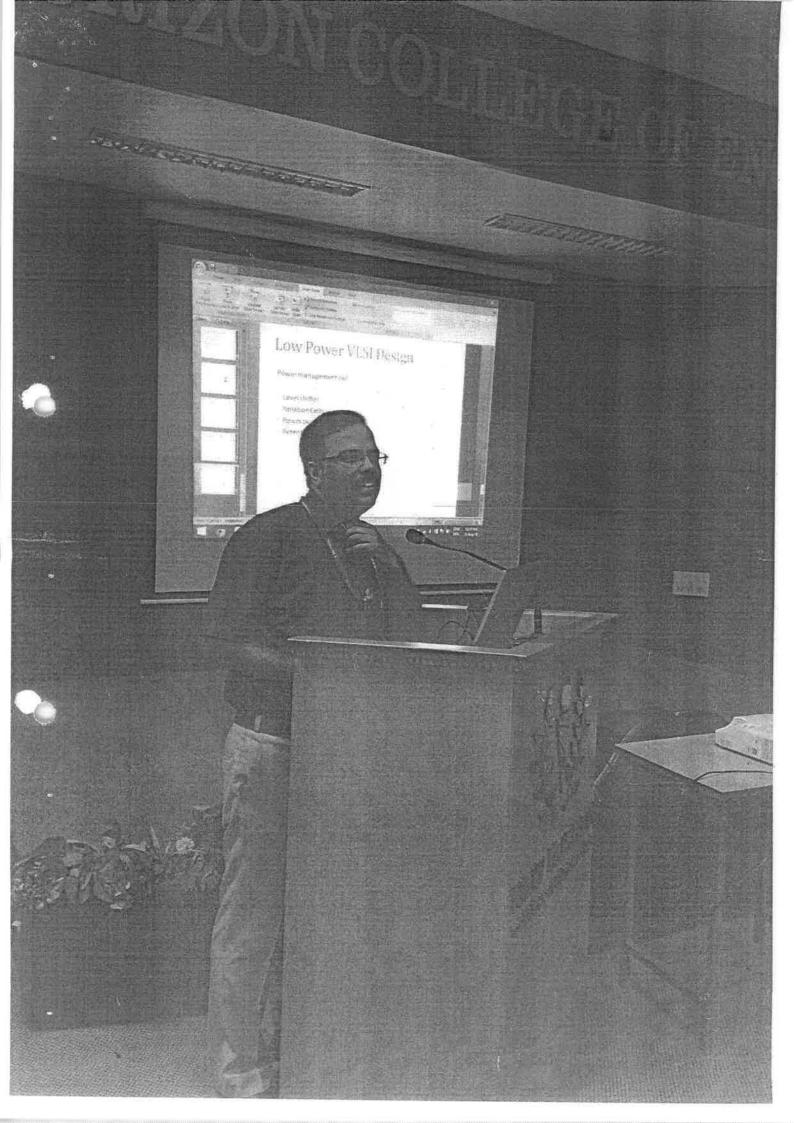
Respective subject teachers are hereby informed to be present at Falconry Seminar Hall during the lecture.

All faculty members are invited to attend the same (without affecting the regular

Guest lecture Coordinator

# Agenda

- Introduction to VESI design
- \* FPGA flow
- \* ASIC flow
- Components on CHIP
- · RTL design
- · Low Power VLSI Design



## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING VII Sem "A" Sec

SL.No	USN	NAME	Stream	Batch
1	1NH13EC022	C Akshay Reddy	Prof	A2
2	1NH14EC019	Bharath P M	Exe	A2
3	1NH14EC061	K S DivyaTej	EXE	A2
4	1NH14EC082	Nagashivakumar Reddy	EXE	A2
5	1NH14EC726	P Likith Chowdary	EXE	A3
6	1•NH14EC737	Abhishek Reddy V	EXE	A2
7	1NH14EC744	Gourav K	EXE	A2
8	1NH14EC748	Narendra Kumar U	EXE	A2
9	1NH15EC001	Vinay Kumar Reddy	Prof	A1
10	1NH15EC018	Deexith S	Prof	A2
11	1NH15EC019	Denzel Abraham George	Prof	A1
12	1NH15EC022	Greeshma. R	Prof	A1
2	1NH15EC030	Janardhan.S.P	Glob	A3
	1NH15EC031	Jayanth M K	Prof	A2
15	1NH15EC036	Karthik R	Prof	A2
16	1NH15EC040	Kesav. S	Prof	A2
17	1NH15EC045	Manikanta.K	Prof	A2
18	1NH15EC047	Mari Setti Venkata Ramakrishna	Prof	A3
19	1NH15EC048	Megha.D.R	Prof	A1
20	1NH15EC062	Nikhil Riyaz	Prof	A1
21	1NH15EC063	Nishant S	Prof	A1
22	1NH15EC065	Obili Srinidhi	Prof	A1
23	1NH15EC066	Pamisetty Udayabhanu	Prof	A1
24	1NH15EC076	Rachitha.M.R	Prof	A2
25	1NH15EC078	Radhika.B	Prof	A1
26	1NH15EC079	Riya Dey	Prof	A1
27	1NH15EC083	S P Rahul Kumar	Prof	A1
28	1NH15EC088	Saleh Junaid Ahmed	Prof	A1
	1NH15EC100	Shruthi Rajamani	Glob	A1
<u></u>	1NH15EC105	Shyla Shree.R	Prof	A2
31	1NH15EC106	Siddharth P	Prof	A3
32	1NH15EC112	SREE RENUKAAKSHITHA	EXE	A3
33	1NH15EC114	Sumanth.R	Prof	A2
34	1NH15EC116	Surendra Kumar	Glob	A2
35	1NH15EC120	Thribhuvan L	Glob	A2
36	1NH15EC125	Veda.J	Prof	A2
37	1NH15EC126	Vidhut Sharma	Prof	A1
38	1NH15EC127	Virender Singh	Glob	A3
39	1NH15EC129	Yakshika.A	Prof	A1
40	1NH15EC131	S Reshma	Prof	A3
41	1NH15EC406	Gayathri V	EXE	A1
42	1NH15EC413		EXE	A2
43	1NH15EC423		Exe	A1
44	1NH15EC430		EXE	A2
45	1NH15EC705		Prof	A2
46	1NH15EC709		Prof	A3
47	1NH15EC719		Glob	A2

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48	1NH15EC727	R. Hari Raj	Prof	A3	7
49	1NH15EC728		Glob	A2	-
50	1NH15EC732		Prof	A2	=
51	1NH15EC734		Prof	A2	
52	1NH15EC738	Rushika Ujjwal	Prof	A2	
53	1NH15EC741	Sanjana Ranjan	Glob	A1	
54	1NH15EC748		Prof	A2	Aurom Pal
55	1NH15EC752	Suraj Kumar Sharma	Prof	A1	(Remay)
56	1NH15EC754	Syed Saqlain Ahmed	Glob	A1	
57	1NH15EC756	Venkatesh T	Glob	A1	
58	1NH16EC400	Chethan B R	Prof	A3	-
59	1NH16EC402	Harsha Vardhan R	EXE	A3	8
60	1NH16EC404	Hemavathi S	Prof	A3	4
61	1NH16EC405	Kavyashree M	Prof	A2	-
62	1NH16EC406	Kushal D Pravarda	Glob	A3	1
63	1NH16EC407	Lavanya C Y	Glob	A2	
61.	1NH16EC409	LOKESHARAO V	Exe	A3	
~	1NH16EC410	M. Mrinal	Prof	A3	المعادا
υG	1NH16EC414	Muralidharan E	Prof	A3	Both
67	1NH16EC416	Naresh Babu	Prof	A3	1000 =
68	1NH16EC421	Rakshitha N	Exe	A3	No
69	1NH16EC424	Rohit R	EXE	A3	
70	1NH16EC429	Ullas M S	Exe	The second second	TURE
71	1NH16EC430	Vijay Kumar Y Valmiki	Prof	A2 A2	W.
72	1NH16EC431	Aravind K	EXE	AZ A3	

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HOD - ECE

**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

## NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING VII Sem "B" Sec

SL.No	USN	NAME	Stream	Batch	7
1	1NH13EC138	D Ramya	EXE	B3	Banks
2	1NH13EC729	Mohammed Sabah . M	EXE	B3	Bany
3	1NH14EC033	D Subba Reddy	EXE	B1	3900
4	1NH14EC037	Geetha M	EXE	B3	1
5	1NH14EC132	Srustik Subhash	EXE	B2	
6	1NH14EC147	Yerrabapu Rohit Reddy	EXE	Out of the case of	
7	1NH14EC722	Nishanth Reddy K	EXE	B2 B2	When and
8	1NH14EC723	Nithya V	EXE	B3	$\mathcal{M}_{ij}$
9	1NH15EC002	Abburi Greeshma	EXE	B1	
10	1NH15EC003	B Achal	EXE	B3	Auch:
11	1NH15EC004	Akash Mallappa Mantur	EXE	B2	DEANS
12	1NH15EC005	Akhil Chowdary. M	EXE		14 India
_1	1NH15EC006	Akshitha.KS	EXE	B1	
4	1NH15EC009	Anusha M	EXE	B1	
15	1NH15EC010	Aparna.M	EXE	B2.	
16	1NH15EC011	Bhavana Savanth	EXE	B1	bland.
17	1NH15EC012	Bujja Ajay	EXE	B3	The same
18	1NH15EC013	Chandini C		B1	thans chot
19	1NH15EC014	Chandra Shekhar.K.R	EXE	B1	00
20	1NH15EC016	Deepak S	EXE	B2	Cho_
21	1NH15EC017	Deepika.S		B2	
22	1NH15EC023	Gurushree Bhat U	EXE	B1	Phi.
23	1NH15EC024	Harsha E	EXE		(1)
24	1NH15EC025	Harshitha J R	EXE	B2	
25	1NH15EC026	Harshitha N	EXE	B1	
26	1NH15EC027	Hemanth Kathick R	EXE	B1	
27	1NH15EC028	Hemanth Kumar.R	EXE	B2	
28	1NH15EC029	Irfan	EXE	B2	
29	1NH15EC032	Jayanth S	EXE	B2	
30	1NH15EC033	Jitin Jain Mathew	EXE	B1	
31	1NH15EC034	K B Hithesh	EXE	B1	
32	1NH15EC035	K.R.Amrutha	EXE	B2	1.R. Am
33	1NH15EC037	Karthik.V	EXE	B3 /	K.R. dm
34	1NH15EC038	Kavya.D.R	EXE		4
35	1NH15EC039	Keerthi.U.M	EXE	B1	
36	1NH15EC043	Laxmi Sah	EXE	B1	
37	1NH15EC046	Manamohan	EXE	B1	
38	1NH15EC049	Mithun.V	EXE	B1	N. V
39	1NH15EC050	Mohammed Musaveer	EXE	B2	V/Mr
40	1NH15EC051	Mohammed Shabaz	EXE	B2 N	Masay
41	1NH15EC052	Monika Reddy	EXE	B2 5	habay
42	1NH15EC053	Monisha.M	EXE	B1	,
43		Mukesh	EXE	B1	1 X 19 C
14	1NH15EC056		EXE	B2	Ton Kake
45	45.00	Nagarjun K S Nalini.K	EXE	B1 4	rukes C
16			EXE	B1	
17	4.000	Priyanka Nallagouni	EXE	B3	1 - 1-
	THITTOLCOOU	Naveen.K.R	EXE	B2 +d	1-9

48	1NH15EC061	Naveen.P	EXE	l na	
49	1NH15EC070	Venkata Sai ganesh	EXE	B2	
50	1NH15EC071	Preeti .K.Mehtry	EXE	B1	- Sala
51	1NH15EC073	Punarva.A	EXE	B3	
52	1NH15EC077	Radhakrishna.C	EXE	B3	- Vistoria
53	1NH15EC081	Rukmini.N	EXE	B2	Piothal !
54	1NH15EC085	Saahil Rai	EXE	B1	-
55	1NH15EC086	Sachin Yaday	EXE	B1	-
56	1NH15EC089	Samarpan Chakraborty	EXE	B1	-
57	1NH15EC102	Shubha.A	EXE	B1	or alles
58	1NH15EC113	Subramanaya Ganesh	EXE	B2	Shilar
59	1NH15EC117	Swathi.R		B1	_
60	1NH15EC121	Tirumala Satheesh Himakeerthi	EXE	B1	-
61	1NH15EC132	Deepti S		B1	_
62	1NH15EC135	Archana M	EXE	B3	-
63	1NH15EC435	Thyagaraj M	EXE	B1	
61	1NH15EC701	Abhishek V P	EXE	B3	_
1	1NH15EC703	Ashwin S	EXE	B1	1
0	1NH15EC707	Deepak Tomslee	EXE	B3	Ashon
67	1NH15EC714	Jishma Asmi. V	EXE	B3	- 48M
68	1NH15EC724	Prasanna Kumar.D	EXE	B3	19000
69	1NH15EC755	Syed Zahid Ulla	EXE	В3	- 53
70	1NH16EC401	Dilip Kumar R	EXE	B2 -	Labore
71	1NH16EC411	Madhavi Iranna Sajjan	EXE	B3	,
72	1NH16EC412	Mohammed Faizulla D K	EXE	B1	1. 10
73	1NH16EC413	Mohan Kumar V	EXE	B1	family
74	1NH16EC415	Namitha Reddy V	EXE	В3	1
75	1NH16EC417	Padmini M E	EXE	В3	
76	1NH16EC419	Praveenkumar R Jadhav	EXE	В3	
77	1NH16EC420	Priyanka P	EXE	B1	
78	1NH16EC423	Ramitha B	EXE	B2	
79	1NH16EC426	Sachin K B	EXE	В3	
80	1NH16EC427		EXE	B2	Sachiff KB
81	1NH16EC428	Shashikumar M R	EXE	B2	
-	141110000420	Sowmya P	EXE	B3	

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HOD - ECE

**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

#### NEW HORIZON COLLEGE OF ENGINEERING **DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING** VII Sem "C" Sec

SL.No	USN	NAME	Stream	Batch	
1	1NH14EC026	D Vedavathi Kiranmai	EXE	C1	
2	1NH14EC052	Priyanka Milton	EXE	C1	9
3	1NH15EC020	Gadipudi Asrith	EXE	C3	1
4	1NH15EC021	Gattu Raghunadham	EXE	C3	1
5	1NH15EC041	Kolli Srinivasa Reddy	EXE	C2	98
6	1NH15EC042	Konuru Gopi	EXE	C2	1
7	1NH15EC044	Madala Kalyan Kumar	EXE	C2	Ne.
8	1NH15EC055	Nagabhyru Kushvanth Chowdary	EXE	C2	3
9	1NH15EC059	Nanditha S	EXE	C1	٦
10	1NH15EC064	Noti DineshKumar Reddy	EXE	C2	3
063	1NH15EC069	Pavan Kumar s	EXE	C3	1
12	1NH15EC072	Pulibandla Harish	EXE	C2	
13	1NH15EC074	Putti Reddy Akhil Reddy	EXE	C3	
14	1NH15EC080	Roopa D Almeida	EXE	C1	9
15	1NH15@C082	Rushali Raina	EXE	C1	
16	1NH15EC084	S.Suhas	EXE	C3	
17	1NH15 EC087	Sakkirala Satwik Yadav	EXE	C.2	
18	1NH1:=:C090	Sandhya B	EXE	C3	
19	1NH1: :::091	Sanjay Kumar.A	EXE	C2	1000
20	1NH11 .0092	Sanjeev Jayasurya.S	EXE	C1	
21	1NH1 1093	Sare Sai Sandeep	EXE	C2	
22	1NH1' 0094	Satish.[	EXE	C2	Ī
23	1NH1 0095	Shaik Sohail	EXE	C2	2
24	1NH1: 1096	Sharath.N	EXE	C2	Ī
25	1NH1 1098	Shilpa.V	EXE	C1	1
26	1NH1 1099	Shreyas S	EXE	C3	Ī
	1NH: 1101	Shruti Jana	EXE	- C2	Ī
28	1NH1 0104	Shwetha.R	EXE	C2	
29	1NH1 C107	Sneha P	EXE	C1	1
30	1NH1 =0108	Sneha.S	EXE	C1	
31	1NH: 110	Soundarya B K	EXE	C1	
32	1NH1 2.111	Sowmya.S	EXE	C1	
33	1NH: :115	Suraj R	EXE	C1	
34	1NH: 118	Tarun Gowda.S.M	EXE	C2	
35	1NH: .119	Thanuja A	EXE	C1	
36	1NH 3122	V.Bharat Raj	EXE	C2	7
37	1NII 1123	V Megha	EXE	C2	
38	1NII 124	Varikuti Harsha Vardhan Reddy	EXE	C3	i
39	1NI 128	Virupaksha	EXE	C2	1
40	1NI 30	Kaliki Poojasri	EXE	C2	
41	1NH 133	Namratha B R	EXE	C1	
42	1NH 34	Manoj Kumar K	EXE	C2	7



43	1NH	0700	Abhishek Gowda.K.M	EXE	C2	1
44	1NH	C702	Anusha.R	EXE	C2	
45	1NH:	C704	Basanagouda Patil	EXE	C3	3
46	1NH:	C706	Vijay Krishna	EXE	C2	7
47	<b>1N</b> H1	C708	Ekta Shukla	EXE	C2	
48	1NH:	C711	Hakesh Kolukonu	EXE	- C1	70
49	1NH	0712	Hari Chandana Veldanda	- EXE	C1	ĪF
50	1NI	€713	l G Kevin Christopher Morris	EXE	C2	7
51	1Ni	C715	Manoj Chandrashekar	EXE	C1	
52	1Ni	C716	Megha N	EXE	C2	7
53	1NI	C717	Venkatasai Balakrishna	EXE	C2	8
54	1NI	C718	Namrata Sudhir Katrale	EXE	C2	7
55	1Ni	C <b>720</b>	Pavan Kumar S	EXE	C2	6
56	1Ni	C722	Pavithra.R	EXE	C2	7
7	1N:	C <b>723</b>	Pradeep.K	EXE	C3	1
58	1N:	C725	Prathap R	EXE	C3	1
59	1N	C726	Praveen P Nair	EXE	C3	1
60	1N	C729	Radhika R	EXE	C1	
61	1N	C730	Rahul S	EXE	C3	
62	1N:	C731	Rakshitha.L	EXE	C3	1
63	1N:	C733	Ramya R	EXE	C1	1
64	<b>1</b> N	C735	Ravula Aswenth	EXE	C3	9
65	<b>1</b> N	2739	S K Mohammad Akhleem Nawaz	EXE	C3	18
66	1N	2740	S Krithika	EXE	C3	1.
67	11	C742	Savita Shivanand Patil	EXE	C3	1
68	1N	C743	Sharath C R	EXE	C3	1
69	<b>1</b> N	C744	Shiva Prasad.H.S	EXE	C3	
70	1N	C745	Shivani Kumari	EXE	C3	1
71	1N	C746	Shivani.V	EXE	C3	1
72	11	2747	Shreyas.H	EXE	C3	1
70	1N	2749	Shwetha.P.S	EXE	C3	
14	11	C750	Snehith .V. Gowda	EXE	C3	1
75	11	C751	Suraj Davis	EXE	C3	1
76	11	C753	Surya V	EXE	C1	1
77	11	C757	Manoj V	EXE	C3	
78	11	:759	Vishnu.N.U	EXE	C3	
79	11	408	Lokesh	Exe	C3	P
80	11	422	Ramesh Bi <b>rgi</b>	Exe	C1	
						100

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HOD - ECE

Note: ghted students should be informed to pay the Account dues. Stude ould not be given attendance till they clear the dues.

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON (Mention the topic): Sent conductor Electronics DATE: 18.08.2018 Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	- V		<b>/</b>	/ -	
Average			2		/-
Satisfactory	TA 3			= 8	

Comments/Remarks/Suggestions:

1. The esteemed speaker could have made it more interesting in team of or smalls of application.

2.

3.

Name & USN No.(optional)

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good		9	<b>/</b>	-	
Average		<u> </u>		<b>-</b>	
Satisfactory	* *				-

Comments/Remarks/Suggestions:

1.

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Name & USN No.(optional)

# GUEST LECTURE REPORT

**Topic: Pathway to Semiconductor Industry** 

Speaker: Ms. Neha Bharti

Date: 19th August 2018

Falconry seminar hall, NHCE

Time: 11AM-1PM

With the exponential growing advancements in various technological domains semiconductor electronics has paved its way as the catalyst which forms the foundation of myriad inventions.

Drawing us closer to semiconductors and giving us a sneak peek into the industrial requirements, Ms. Neha Bharti engaged the young minds of Electronics and Communication Department (7<sup>th</sup> Semester) with her expertise on the subject.

Ms. Neha Bharti is a Post Graduate in Advanced VLSI Design from CDAC Mumbai. She began her Electronics journey in 2013 from Nagpur University where she did her Graduation in Electronics and Telecommunication. With her 4 years' experience in RTL Design with FPGA and ASIC Flow, she emphasized on the need to the have a strong hold on the basics of Digital Electronics such as the understanding of various gates and implementation of functionalities using these gates. Before setting up the presentation, the speaker briefed us on all the basic concepts of ECE. The Speaker also revised the different protocols involved in different processors such as SPI, I2C etc.

In this journey she further threw light on the flow of VLSI technology starting from system specification to fabrication. She also explained the various intricacies of hardware design using hardware descriptive languages (HDL) which include VHDL and Verilog. We were introduced to various terms and aspects of sequential and combinational circuits and also the various aspects that govern them such as the importance of timing and delay that differentiate them. She also took us through the procedures involved in chip design. Based on the industrial requirements of an Electronics and Communication Engineer,

the speaker listed the topics that are essential for the students to be well versed with.

The presentation revolved around the need to understand the management of area, speed, power and functionality. This system on chip (SOC) concept was driven forth with an interactive session with the students.

The seminar also dealt with Low Power VLSI Design which comprised of:-

- Level shifters
- Isolation Cells
- Power switches
- Retention registers

With these concepts she concluded her seminar with a Question and Answer session. This interactive session guided the young inquisitive minds towards a better understanding of what is and what will be in the Semiconductor Industry. The seminar not only enlightened us to keep ourselves updated with the growing technology but also to strengthen our foundations by fostering enhanced learning.

NAME OF THE EVENT/CLUB: Grest Lecture.

FEEDBACK ON(Mention the topic):

DATE: 18/08/2018

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	$\checkmark$	<b>-</b>	~	V-2 "	
Average					
Satisfactory	*				

Comments/Remarks/Suggestions:

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Name & USN No.(optional)

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic):

Instruction: Please put a tick mark at the appropriate column.

DATE: 18 08 18

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent				· ·	
Good		1		~	V
Average			V.	,	y a
Satisfactory	9		4		

Comments/Remarks/Suggestions:

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FEEDBACK ON(Mention the topic):

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Instruction: Please put a tick mark at the appropriate column.

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		-		
	Clarity	Clarity Visuals	Clarity Visuals Skill's	Clarity Visuals Skills Guidance

Comments/Remarks/Suggestions:

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#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic):

DATE: 18/08/18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent		4			
Good				/	
Average					
Satisfactory	-				

Comments/Remarks/Suggestions:

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FEEDBACK ON(Mention the topic):

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Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good					
Average			-		
Satisfactory					~

Comments/Remarks/Suggestions:

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Name & USN No.(optional)

#### **CIRCULAR**

This is to inform the students of V semester that Guest Lecture is arranged on 11-8-18 on the topic "Hardware Design Flow for Chip Design" at Chanakya seminar hall.

Note: Attendance is compulsory

Faculty In-charge

HoD-ECE

# GUEST LECTURE ON HARDWARE DESIGN FLOW FOR SOC DESIGN

#### **V** Semester

The session was handled by Mr. Shriram, SOC design Engineer, Intel India Technologies. He is alumni of NHCE(ECE). The session was on 11/08/2018, started at 10:30am. The session prolonged for an hour or so. He gave us valuable information regarding our branch.

He started up with the history of IC. He shared the information of all the IC's till date. He compared with different IC's and the difference between each. He discussed about the microprocessors, microcontrollers and how they are different from each other.

We also learnt the applications of our subjects studied in the previous semesters, how are they applicable to the current industries. For example HDL, ANALOG ELECTRONICS etc.

We were explained about the processors in android phones. He explained us many types of processors like quad processor, deca processor etc. He briefly explained us about the processor of SAMSUNG J5 model. The explanation on microprocessor of SAMSUNG was real very helpful, and took us to the real time applications of our studies.

We were also discussed with the real time applications of FPGA and ASIC. He also explained us the difference between FPGA and ASIC.

The session was mainly concentrated on the manufacturing, design and layout of IC. The design, layout and design was very clearly explained step by step. He explained us how the planning and subcomponents of the IC. He gave us the ideas of the current requirements of the industries.

We were many suggestions which would help us to grow in our career. Then we had a small interaction with the speaker. He addressed and answered many of our questions. The session was concluded with beautiful and inspirational quotes.

### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "A" Sec

SL.No	USN	NAME	Stream	BATCH	Date	
1	1NH15EC015	DEBBRAT MAHAPATRA	PROF	A1		
2	1NH16EC001	Λ M AMARJITH	EXE	A1	26.7.18	jost,
3	1NH16EC002	ABHISHEK KUMAR DESHETTI	EXE	A1	30.7.18	This
4	1NH16EC003	ADITYA C	EXE	A1	-	July
5	1NH16EC004	AFZAL HUSSAIN	EXE	A1	24.7.18	Stal
6	1NH16EC005	AKHIL JOSEF	EXE	A1		Dec
7	1NH16EC006	AKHILESH VARMA KALIDINDI	EXE	A1		ALBRE
8	1NH16EC007	AKSHAY RAO	PROF	A1		Akde
9	1NH16EC008	ANMOL KUMAR	EXE	A1		Ay was
10	1NH16EC009	ANOOP.A	PROF	A1 -		Aud
11	1NH16EC010	ANUSHA H N	EXE	A1		N-
12	1NH16EC011	ARUN.P	GLOBAL	A1		72/
13	1NH16EC012	ATHIRA AJAYA KUMAR KULLULLY	EXE	A1		- H
14	1NH16EC013	YUGANDHAR REDDY	PROF	A1.		
15	1NH16EC014	B SRIKANTH REDDY	EXE	A1		I. A.
16	1NH16EC015	BHASKAR CHOUDHURY	EXE	A1		- John Marine
17	1NH16EC016	BIPIN DIXIT.H	EXE	A1	R-	
18	1NH16EC017	KIRAN C A	EXE	A1	100	18.11
19	1NH16EC018	ARUNA C	EXE	A1	112	- Frank
20	1NH16EC019	C.LEHARIKA	PROF	A1	-72	- 1
21	1NH16EC020	CHETHAN S	EXE	A1		- Local
22	1NH16EC022	DARSHAN RAJ	EXE			cher
23	1NH16EC023	DEEPAK KU. PRADHAN	EXE	A1		Doub
24	1NH16EC024	DEEPTHI R	EXE	A2		- O.TA
25	1NH16EC025	DENNIS VINCENT PAULRAI	PROF	A2 A2		- 45
26	1NH16EC026	DUDEKUL AKHASIM	EXE	A2	Eli audeeve kus	-
27	1NH16EC027	ELDHO JOSE	EXE			
28	1NH16EC028	ELLINKI JAHNAVI	PROF	A2		Johnan
29	1NH16EC029	VICTOR ABHILASH	EXE	A2	Secretary Secretary	- Dangan
30	1NH16EC030	G AVINASH		A2		- Rahmurk
31	1NH16EC031	GIRISH JATTU GOUDA	EXE	A2	24.7.40	- Garage
32	1NH16EC032	HARI PRASAD.R	EXE	A2	24.7.18	- 600
33	1NH16EC033	HARSH SRIVASTAVA	EXE	A2	41. 1	1000
34	1NH16EC034	JAGADEESH.D	EXE	A2	Haus	- FLYNY
35	1NH16EC035	JANANI.B.R		A2		- (3
36	1NH16EC036	GIRIVARDHAN K	EXE	A2	<u> </u>	- gent
37	1NH16EC037	KALAMADI SREELEKHA	GLOBAL	A2	Criva!	- October
38	1NH16EC037	KIRAN N	GLOBAL	A2		Species
39	1NH16EC040	KISHORE N	EXE	A2		horan
40	1NH16EC040	K. VENKATA MOUNISH REDDY	EXE	A2		Kalena
41	1NH16EC042	KORVI CHANDRA KIRAN REDDY	EXE	A2		Mound
42	1NH16EC042	KUSHI PONNAMMA.K.P	EXE	A2		Chandra
43	1NH16EC043		EXE	A2		Much
4.4	1NH16EC044	LALITHAMBHA S M SACHIT M	EXE	A2	التحاشيات	Toranno P
45	1NH16EC047	MADAN GOWDA.M	GLOBAL PROF	V3		Jack

46	1NH16EC048	MADHU M DEVAMANE	PROF:	A2		Madus
47	1NH16EC049	MANJULA.S	PROF	A'3	KENS GAN	MONT
48	1NH16EC050	MANJUNATH N	PROF	A3		
49	1NH16EC051	MANJUNATHA S	EXE	A3		
50	1NH16EC052	MAURYA REDDY	ÊXE	A3	24.7.18	Mauphyon
51	1NH16EC053	MEGHASHREE H M	EXE	A.3		fulls
52	1NH16EC054	MEGHANA V	EXE	A3		Meghanow
53	1NH16EC055	MEGHASHREE R	EXE	13		Mohabase
54	1NH16EC056	MISBAH TABASSUM AEJAZ	EXE	A3		1
55	1NH16EC057	MITHUN.V	EXE	Λ2	25.7.18	elato.
56	1NH16EC058	MOHAMMED ANAS	EXE	.A3		de
57	1NH16EC059	MOHAMMED FAROOQH PASHA	EXE	A3		bineng.
58	1NH16EC060	MOHIT R	PROF	A3		Flore
59	1NH16EC061	моніт ѕлни	EXE	A3		whiter
60	1NH16EC062	MUJEER PASHA	PROF	A3		Wierley
61	1NH16EC063	MAHESHWARI N	EXE	A3		lenter
62	1NH16EC134	T. HEMANTH KRISHNA	EXE	A1		
63	1NH16EC135	AKULA ANIL BHARATH	EXE	A3		Roil
64	1NH16EC136	ALVIRA SUZANA	GLOBAL	A3		Murse
65	1NH16EC137	ANUPOJU SAI VENKAT THRIMURTHY	GLOBAL	A3		Thriener they
66	1NH16EC138	ASHISH KUMAR . S	PROF	A:3		Ash Elle
67	1NH16EC139	KARTHIK . K. S	EXE	A:3	30.7.18	Kadlih
68	1NH17EC428	SUMAN.K.T	EXE	A1	1 - 10 - 10 - 10	
69	1NH17EC400	AKASH PRASAD C	EXE	A3		<b>3</b>
70	1NH17EC402	ARATHI.V	EXE	A3		Aralhi
71	1NH17EC403	BHAVANI. V	PROF	·A3		Stage
72	1NH17EC412	KIRAN . R	EXE	A1		Brogs.
73	1NH17EC419	PRASANNA KUMAR .A . E	EXE	A3		7,
74	1NH17EC423	RAJESHA.T.A	EXE	A1		Parest
75	1NH17EC429	SUPRITHA H . D	EXE	A3		Seed

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HOD-ECE

**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "B" Sec

L.No	USN	NAME	Stream	BATCH	Date
1	1NH16EC064	NEHA MAHESH	EXE	B1	E SING SICE
2	1NH16EC066	NISHU DUBEY	EXE	B1:	
3	1NH16EC067	NITESHK	PROF	B1:	
4	1NH16EC068	NITHIN.E	EXE	B1	
5	1NH16EC070	MAHALAKSHMI P	EXE	B1	
6	1NH16EC071	DEEPAK P S	EXE	B1	
7	1NH16EC072	PARITHOSH VEMA	PROF	B1	1
8	INH16EC074	PAVAN KUMAR S	EXE	B1	
9	1NH16EC075	POOJA SHANTARAM NAYAK	EXE	B1	26.7.18
1.0	1NH16EC076	PRAJWAL.T.J	PROF	B1	
11	1NH16EC077	PRANAY REDDY S	EXE	B1	
12	1NH16EC078	PRASHANTH GOWDA.R.S	PROF	B1	
13	1NH16EC079	PRAVEEN KUMAR S	PROF	B1	25.7.18
14	1NH16EC080	PRESHIKA J M	EXE	B1	
15	1NH16EC081	PRITHIPA A	EXE	B1	2 5 2
16	1NH16EC082	R.PAVAN RAJ	PROF	B1	AMAZAL ISM
17	1NH16EC083	RAJVARDHAN	EXE	B1	
18	1NH16EC084	RAKSHITHA N	EXE	B1	
19	1NH16EC085	ROSHINI.M	EXE	B1	20.0 E2.00
20	1NH16EC086	RUSHAB.S.G	EXE	B1	
21	1NH16EC087	SAI KIRAN	EXE	B2	
22	1NH16EC088	SAI PRASHANTH	PROF	B2	l.
23	1NH16EC089	S P SAAJU	EXE	B2	
24	1NH16EC090	SAGAR	EXE	B2	
25	INH16EC091	SAM LEANDER D	EXE	B2	
26	1NH16EC092	SANNAPANENI PAVANI	GLOBAL	B2	
27	1NH16EC093	SHAIK NOORULLA BASHA	EXE	B2	
28	1NH16EC094	SHAIKH ASIF	EXE	B2	
29	1NH16EC095	SHASHANK B	EXE	B2	
30	1NH16EC096	SHRAVIN.R.SEKHAR	EXE	B2	
31	INH16EC097	SHRIPAD AITHAL	EXE	B2	
32	1NH16EC098	SIDDESH JALAGERI	EXE	B2	
33	1NH16EC099	JAGAN MOHAN REDDY	EXE	B2	
34	1NH16EC100	SIDRAMAPPA	EXE	B2	26.7.18
35	1NH16EC101	SINDHU C R	EXE	B2	
36	iNH16EC102	SIVA CHALLA	EXE	B2	24.7.18
37	1NH16EC103	SPOORTHY.G	PROF	B2	
38	1NH16EC105	SUSHMA CHIKKUR	PROF	B2	

375

39	1NH16EC106	T.E.HABISHEK	PROF	B2		J. Holling
40	1NH16EC107	T.SAI DEEPTHI	EXE	B2		- Holling
41	1NH16EC108	TANUJ.A	EXE	B2		200 bth
42	1NH16EC109	UDIT BAHUGUNA	EXE	B2		
43	1NH16EC110	VARUN MISHRA	EXE	B3	Marina (Sa.	E
44	INH16EC111	VEDA MILIND KULKARINI	EXE	В3		
45	1NH16EC112	VEENA K	EXE	B3	21.025.00.275.00	
46	1NH16EC113	VIDYA VISHNU	PROF	B3		7
47	1NH16EC114	VIJAY C	EXE	B3		
48	INH16EC115	VIJAY KUMAR C	EXE	B3		5
49	INH16EC116	VIJAY S	PROF	B3	7	Pengl
50	INH16EC118	VISHWANATH VEERANNA	PROF	B3		
51	1NH16EC119	W M MAGDOOM FUAAD	GLOBAL	B3		desta-
52	INH16EC120	YATHIN S	PROF	B3		Wathin-S
53	1NH16EC121	YATHISH K	PROF	B3		
54	1NH16EC122	YESHWANTH J M	EXE	B3		Velanda
55	INH16EC124	YESHWANTH.M	EXE	B3	23.7.18	- Us
56	INH16EC125	YESHWANTH.M.L	EXE	B3		Yes
57	INH16EC128	SIMON CHAUHAN	EXE	B3		- 7
58	1NH16EC129	ASHOK CHOUDHARY	GLOBAL	B3		More
59	1NH16EC130	M.V.K BHARATH	GLOBAL	B3-		Mary Navy
60	1NH16EC131	NAMRATHA PRAKSAH	PROF	В3		Marati
61	1NH16EC132	PREM KUMAR	EXE	B3		
62	1NH16EC133	SRIDHAR. P	EXE	B3:		DR ISL
63	1NH17EC401	AKSHAY GAMBHIR SADALAGI	EXE	B3		A.A.L.
64	1NH17EC406	DEEPAK KUMAR . R	EXE	В3		- Lada
65	1NH17EC407	GURURAJ SHIVASHANKAR NATIKAR	EXE	B2	- Out is the line	
66	1NH17EC410	JYOTHI CHANDRASHEKAR	PROF	B2	23.7.18	Total
67	1NH17EC414	LINGARAJ G. B	EXE	B3	100	- Short
68	1NH17EC417	MONIKA T. M	PROF	B2		
69	1NH17EC426	SACHIN.M	EXE	B3		State
70	1NH17EC427	SHAMBULING V. PATIL	EXE	В3		And
71	1NH17EC430	SUSHMITHA	EXE	B3		1

HOD - ECE

**Note:** Highlighted students should be informed to pay the Account dues. Students should not be given attendance till they clear the dues.

# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING V Sem "C" Sec

SL.No	- ODIN	NAME	Stream	DAME	77
1	1NH13EC73		EXE		H Dat
2	1NH14EC00	1 Abhijith S Raj		C3	111.0
3	1NH16EC702	ABHISHEK KHOT	EXE	C3	Mbhije
4	1NH16EC703	ABHISHEK PATTAJOSHI	EXE	C3	Stell
5	1NH16EC704	ABU BAKAR SIDDIQ	EXE	C3	Offe !
6	1NH16EC705		EXE	C3	-8
7	1NH16EC706	AMMIDAL VISHNU VIKAS	EXE	C3	Albuqj.
8	INH16EC707	ANITA CHAUHAN		C3	dall
9	1NH16EC708		EXE	C3	A C
10	1NH16EC709		EXE	C3	(Olyl)
11	1NH16EC710	B VAMSHI KRISHNA REDDY	PROF	C3	Oche
12	1NH16EC711	BALAJI L	EXE	C3	Vanist
13	1NH16EC712	CHETHAN,D.R	PROF	C3	Palas
14	1NH16EC713	CHIRAG S	EXE	C3	
15	1NH16EC714	GAGANA M R	EXE	C3	-danie
16	1NH16EC715	GOWRI SNEHA PRIYA S	EXE	C3	Ca an Mile
17	1NH16EC716	GURRAM VENKATA NIKESH	EXE	C3	Xmen
18	1NH16EC717	HARSHITA.P	GLOBAL	C3	Cop
19	1NH16EC718	JEEVAN.K	EXE	C3	ment
20	1NH16EC719	MITHUN KUMAR	EXE	C3	Jeeran
21	1NH16EC720	KIRAN SAGAR N P	PROF	C2	Missens
22	1NH16EC722	LINGRAJ JAMKHANDI	EXE	C2	10 miles
23	1NH16EC723	M ANISHA	PROF	C2	Rello
24	1NH16EC724	M PRIYADARSHINI	EXE	C2	chustra
25	1NH16EC725	MSVV RITVIK	EXE	C2	Paige
26	44.4	MANJUNATH T	EXE	C2	Colored
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	13 19 19	SIVA NAGAMUNI REDDY	GLOBAL	C2 7	egg red
		MOUNIKA E	PROF	C2	Sim
		THILAK PRASAD	EXE	C2	M
	2.70	NAWAZ KHAN	PROF	C2 1	hotels
		NISHA ANANDU NAIK	PROF	C2	Variable
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		AWAN KUMAR SRIVASTAVA OOJA P CHOUHAN	EXE	C2 J	Pawan
		RAVEEN KUMAR	EXE	C1 &	of the second
	DV E	ACHANA.S	PROF	C2 -1	DIRECTA-
	YYY CO		PROF	C2 R	ochow S
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42	1NH16EC744	RAMYA.R	EXE	C2	N 11=
43	1NH16EC745	REDDY SHEKAR.B.S	EXE	C3	Vary
44	1NH16EC746	RUCHIKA PRADEEP	PROF	CI	Redelfo
45	1NH16EC747	S.SARAVANA	EXE	C3	Then
46	1NH16EC748	S SHYAM	EXE	C3	Sal-
47	1NH16EC749	SHRIYA.G	EXE	C1	chyain
48	1NH16EC750	SINDHU A	PROF	C1	houses
49	1NH16EC751	SMITHA B S	EXE	C2	J. Mari
50	1NH16EC752	SWATHI K	EXE	C1	mattis K
51	1NH16EC753	T.R. BHOOMIKA	PROF	C1	12/20 miles
52	1NH16EC754	TARUN SAI REDDY	EXE	C3	AGEN OUN
53	1NH16EC755	ULTHI KEDARNATH	EXE	C1	Tobaceus
54	1NH16EC756	YESHASWINI K M	EXE	C1	Medernath
55	1NH16EC757	YUVASHREE R	GLOBAL	C1	7 2000
56	1NH16EC758	PRAVEEN.S	PROF	C1	ynasnik
57	1NH16EC759	SHASHANK RAO.M	EXE	CI	Mrarcen
58	1NH17EC404	CHIRANJEEVI . K	EXE	C1	Car
59	1NH17EC405	B. N CHITHRASREE	EXE	C1	A Tallora
60	1NH17EC408	JANARDHANA . T	PROF	Cl	Semeralhan
61	1NH17EC409	JAYANTH. V	EXE	C1	Catalonia
62	1NH17EC411	B. J KOWSALYA	EXE	C1	Howarden
63	1NH17EC413	LIKITH B. M	EXE	C1	The second
64	1NH17EC415	MANASA . C	EXE	CI	Marabal
65	1NH17EC416	MANOJ.R	EXE	C1	Nucle B
66	1NH17EC418	NAVEEN KUMAR. J	EXE	CI	11/
67	1NH17EC420	PRATHAMESH KADAM	EXE	C1	
68	1NH17EC421	RAGHU.R	EXE	Cl	Raduel
69	1NH17EC422	RAHUL JAIN	EXE	C1	000
70	1NH17EC424	RAKESH KUMAR . T	EXE	C1	and.
71	1NH17EC431	VASANTH.R	EXE	C1	V.

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Comments/Remarks/Suggestions:

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DATE: 18-08-8018

Instruction: Please put a tick mark at the appropriate column.

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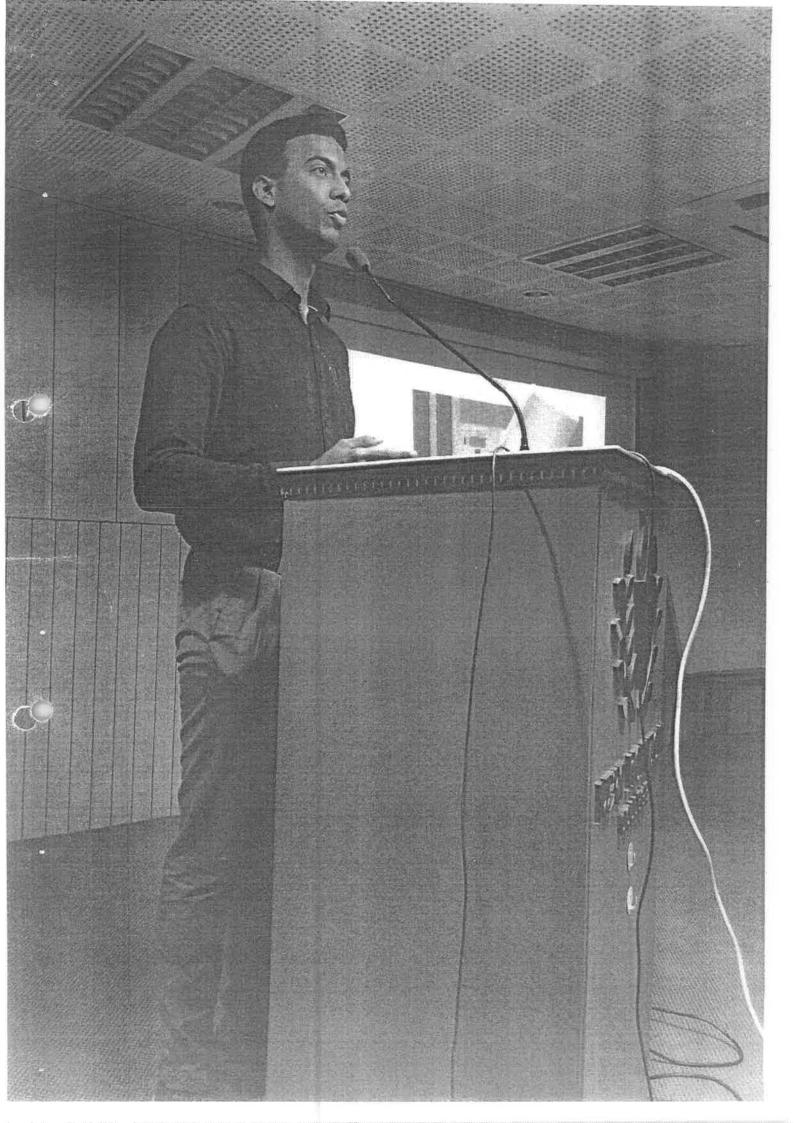
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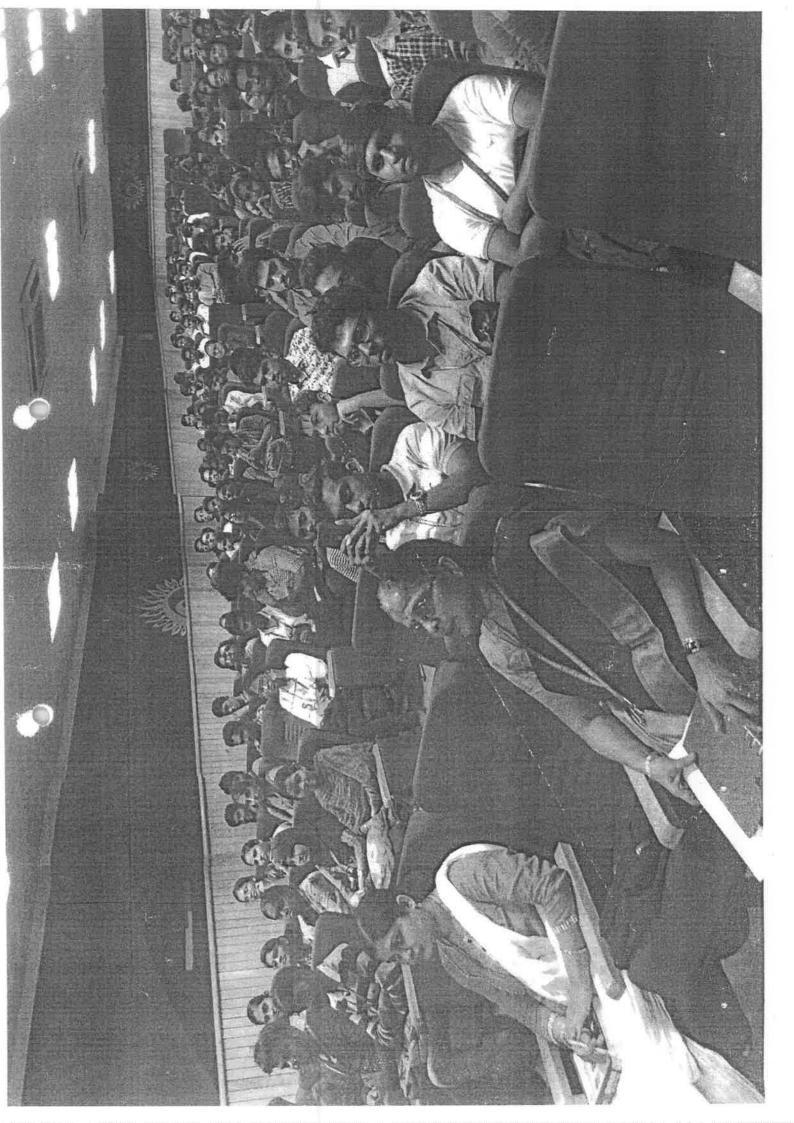
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#### **CHIP DESIGN**

Presented by:-Mr. Shriram

Working at: - Intel

The session was conducted in chanakya seminar hall on 11/08/2018 at 10:00 AM.

Mr. Shriram started his presentation by going briefly into the history of the transistor. The transistor was invented in 1947 by William Shockley, John Bardeen and Walter Houser Brattain in BELL laboratories.

It was the first point contact transistor. William Shockley was only a supervisor of John Bardeen and Walter Houser and his participation in the invention of the transistor was very less. A patent image of the Point contact transistor was shown. All the three scientists got noble prize in 1956 for their invention.

"Gordan Moore" the co-founder of Intel who made an observation and gave a statement that every 18 months the number of transistors in an integrated circuit doubles. He also showed us a image containing graph of how Moore's law is obeyed in the past years.

By the current rate at which transistor size is reduced the Moore's law may be valid for two decades and after which it may not hold good, as the channel length decreases we will decrease it so much that the fundamentals of physics do not allow us to go beyond the atomic size. This may give rise to a new technology.

Intel is the largest chip manufacturing company and their chips are used all over the world. He showed us different chips that have been manufactured by Intel in the past from the very first 4004 processor to the latest i-9 processor.

A introduction to the current technology that Intel is working on was explained, products containing 5nm and 7nm technology will be launched in 2020.

The major difference between the micro processor and the micro controller and various places where they are being used such as washing machine, oven etc,

The various approaches for designing a chip such as bottom up and top down was explained. The different methods such as ASIC and FPGA.

FPGA is suitable for prototyping and testing and it is field programmable as per the needs of the user. ASIC is factory or application specific programmed. He also explained the complications that are faced during the design of micro controller and micro processor, by giving us the example of the Samsung S3 micro processor.

Overall, the session was informative and inspirational and the basic fundamentals and problems faced in chip design were well presented.

# Expert guest lecture

The lecture was held on 11<sup>th</sup> August 2018, Saturday in chanakya seminar hall at 12pm. Sir Shreeram , alumina of new horizon college of engineering shared his experience and thoughts with the students of ECE department .

It gave us a lot of knowledge about the outer world. He helped us to stream line our focus on our interest. He briefed us through the "art of engineering" which was a very apt title. He gave us over view of our syllabus. The whole content was divided into three main streams

- 1. Analog electronics
- 2. Digital electronics
- 3. Embedded systems

The lecture helped us to put our thoughts in the correct direction .

More significance was given on

What to study?

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- How to study?
- Mini projects

We were briefed on how to approach the chapters .We understood that more importance must be given to understanding and applying the theory concepts in practical application . We got an idea on what the industries expect from the students .We got tips to prepare for the interview , an awareness was created that it was the right time to start thinking about our carer seriously and self study was very much important.

A beautiful idea was shared through a thought "we under estimate what we can do in a day and we over estimate what we can do in a year". Sir also share a small exercise of reading about one new technology a day . If we could do this for a year then we will be learning about 365 new things which will add a lot to our technical knowledge . We can note it and share our knowledge with others and help them as well . We can put them in the form of blogs or journals and can publish it as well which will add up to our resume .

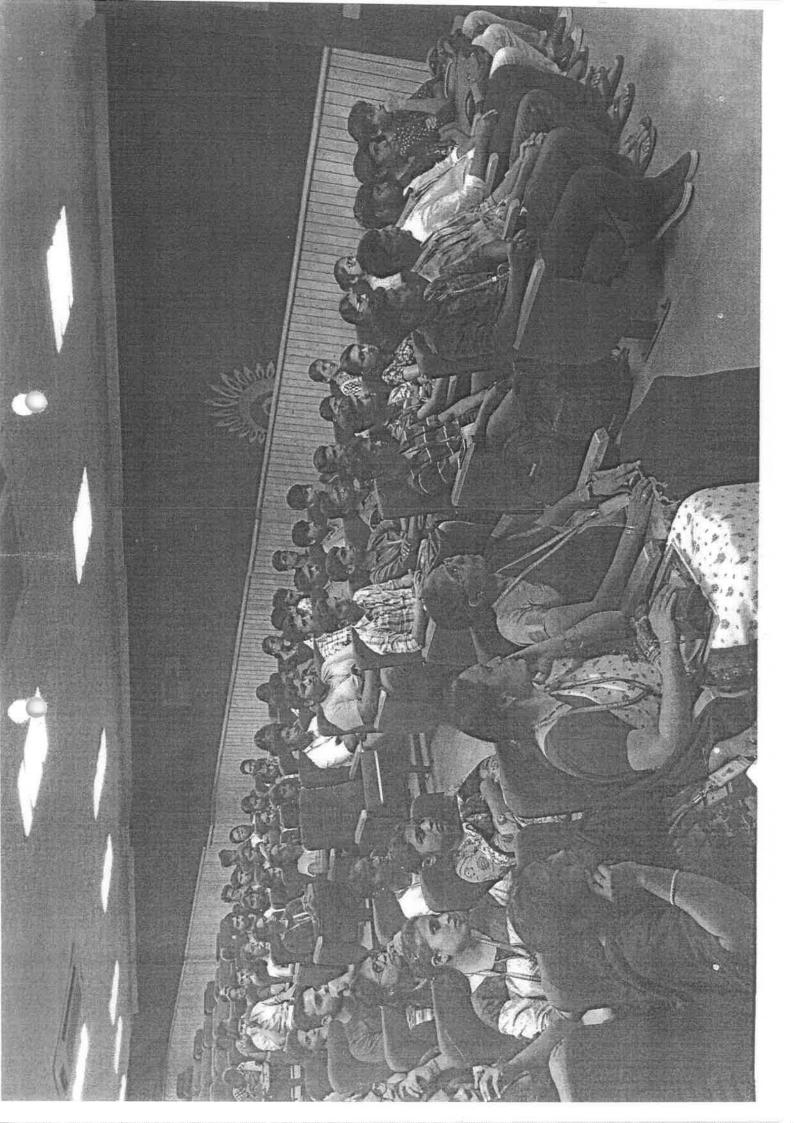
Points like how to select our topics for the upcoming mini projects where focused. The projects that we do can help us find our area of interest. It helps to understand more about a particular technology. These can be developed into trainer kits and sold as well. When projects related to current crises are made, it gives a solution to the problems which society is facing. Having complete information about what we do is very important. Doing a background study of our topic is essential while presenting a project.

Apart for the knowledge we acquire from our faculties, effort has to be put to gather more information. Making best use of the available resources is essential for our development .We should not be satisfied with the present techniques , but aim to minimise the drawbacks of them . Questioning everything is very important . Questions help us in finding large number of possible answers and gives a better understanding .Nothing should be accepted without a satisfactory explanation.

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Relating the concepts to the real world makes it easier to understand them. These few tips and techniques will surely guide us through the tough path of life.

iramspark.wordpress.co ramspark@gmail.com Reach out



# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMF≥T OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "A" Sec

SL.No	USN	NAME	Stream	Batch	Date
1	1NH16EC038	KARROTU PRUDHVI	EXE	A1	
2	1NH17BT002	ANISHA DEVI	EXE	A1	
3	1NH17BT005	BATHULA SRI SAI KRISHNA	EXE	A1	
4	1NH17BT007	GANGADHARA VARSHITHA	PROF	A1	bolds
5	1NH17BT016	MURAKONDA AVINASH	EXE	A1	
6	1NH17BT020	PAGADALA VAMSI KRISHNA	EXE	A1	
j	1NH17BT023	SAI KARTHIK P R	PROF	A1	
8	1NH17BT024	SATYA MANOJ BASINA AGASTYA	EXE	A1	
9	1NH17BT025	SUHAIL PASHA	EXE	A1	
10	1NH17BT026	SUMAN PAL	EXE	A1	
11	1NH17BT031	THURPUNATI SURYA PRAKASH REDDY:	EXE	A1	
10	1NH17EC001	AARTHY P JAITH	EXE	A1	
13	1NH17EC002	ADESH V	PROF	A1	
14	1NH17EC003	ADITYA OMPRAKASH CHOUDHARY	EXE	A1	
15	1NH17EC004	AKSHITHA R	EXE	A1	
16	1NH17EC005	ANAND T	EXE	A1	
17	1NH17EC006	ANAND T	PROF	A1	
18	1NH17EC007	ANKIT KUMAR	EXE	A1	
19	1NH17EC008	ARUN Y S	EXE	A1	
20	1NH17EC009	В S АВҢІЅНЕК	EXE	A1	
21	1NH17EC010	BYAMINI	EXE	A1	48
22	1NH17EC011	BHARATH M	PROF	A1	
23	1NH17EC012	BHARATH Y R	EXE	A1	
24	1NH17EC013	BHASKAR K	EXE	A1	
250	1NH17EC014	BOGGARAPU MOURYA YASWANTH BOREDDY NAGA MAHESWAR	EXE	A2	
26	1NH17EC015	REDDY	EXE	A2	
27	1NH17EC016	C O PRUTHVI	EXE	A2	-
-28	1NH17EC017	CHANDANA N	PROF-G	A2	
29	1NH17EC018	CHETHAN B R	PROF-G	A2	
30	1NH17EC019	DEEKSHITH N REDDY	PROF-G	A2	
31	1NH17EC020	DEEPAK S	EXE	A2	
32	1NH17EC021	DEVASHRUTHA S	PROF	A2	1 Devash
33	1NH17EC022	DHANYASHREE V REDDY	EXE	A2	Danja Sh
34	1NH17EC023	DIVYA SAGAR REDDY	EXE	A2	Dist
35	1NH17EC024	DUDDUKURI VAISHNAVI	EXE	A2	
36	1NH17EC025	G ASAD	EXE	A2	
37	1NH17EC026	G R VARSHA	EXE	A2	Poul
38.	1NH17EC027	GAGAN B K	EXE	A2	
39	1NH17EC028	GAUTAM SINHA	EXE	A2	Gos
40	1NH17EC029	GIRIDHARAN P K	EXE	A2	0
41	1NH17EC030	H K BHASKAR	EXE	A2	
42	1NH17EC031	HARSHITHA G	EXE	A2	

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# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING III Sem "B" Sec

SL.No	USN	NAME	Stream	Batch	Date
1	1NH17EC060	NISHMITHA C SHETTY	EXE	B1	Newloa
2	1NH17EC061	NITHIN DEVANG B S	PROF	B1	
3	1NH17EC062	NITHIN R KALLIGUDI	EXE	B1	
4	1NH17EC063	PALAGIRI JYOTHI KRISHNA	EXE	B1	prikarefun
5	1NH17EC064	PANUGANTI VIKAS	EXE	B1	eller
6	1NH17EC065	PARTHASARATHI N	EXE	B1 (	(00)-
7	1NH17EC066	PASSAVULA PHANINDRA	EXE	B1	P. Moul
8	1NH17EC067	PATAN BABA SOHAIL KHAN	EXE	B1	1
9	1NH17EC068	PRANAY CHATURVEDY	EXE	B1	Pe
10	1NH17EC069	PRAVEEN KUMAR S	EXE	B1 -	00
11	1NH17EC070	PREETHU C	EXE	B1	Phas
12	1NH17EC071	PRINCE CHAURASIYA	PROF	B1	1
13	1NH17EC072	PUNEETH REDDY V Pures	EXE	B1	0 -
14	1NH17EC073	R BARATH	EXE	B1	16000
15	1NH17EC074	R KISHAN KUMAR SAI	EXE	B1	M
16	1NH17EC075	R RAGHAVA	PROF-G	B1	MSK
17	1NH17EC076	RAHUL MONDAL	PROF	B1	Remonda
18	1NH17EC077	RAHUL V	EXE	B1	134/
19	1NH17EC078	RAVI KIRAN M B	EXE	B1	Qni 1
20	1NH17EC079	RENUKA D N	EXE	B1	PANA
21	1NH17EC080	RIYA RAKESH	EXE	B1	Piva
2.2	1NH17EC081	ROHITH P GOWDA	EXE	B1	On LIVA
23	1NH17EC082	RUPSA DATTA	EXE	B1	RUME
24	1NH17EC083	S RISHITA	EXE	B1	Commit
25	1NH17EC084	SAIKAT SAMANTA	EXE	B2	35
26	1NH17EC085	SAIMANTI SAHA	EXE	B2	6.001
27	1NH17EC086	SAMYUKTHA V	EXE	B2	Demos
28	1NH17EC087	SATEESH R HEGDE	EXE	B2	51840
29	1NH17EC088	SHАКТНІ A	EXE	B2	2/10
30	1NH17EC089	SHALINI P N	EXE	B2	Sand
31	1NH17EC090	SHARAN KUMAR K S	EXE	B2 _	2
32	1NH17EC091	SHIVA S	PROF	B2	a 1
33	1NH17EC092	SHOAIB AHMED	EXE	B2	00011
34	1NH17EC093	SHRESTHA PATNAIK	EXE	B2	Match
35	1NH17EC094	SHREYANKA S	EXE	B2	Kuyant
36	1NH17EC095	SHWETA S	EXE	B2	01
37	1NH17EC096	SOUVIK DAS	EXE	B2	2 work
38	1NH17EC097	SOWMIYA A	EXE	B2	humina
39	1NH17EC098	SRINIDHI DAMODHAR	PROF	B2	TOWN TOWN
40	1NH17EC099	STANISLAUS LASRADO	PROF	B2	100

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# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING JII Sem "C" Sec

SL.No	USN	NAME	Stream	Batch	Date
1	1NH16EC065	NILOTHPAL	EXE	J. 5.	Rejoinee
2	1NH16EC732	NAVEEN ALLAM	EXE	C1	
3	1NH17EC700	AATHIRA VIJAYAN	EXE	C1	Atai
4	1NH17EC701	AJAYS S	EXE	C1	
5	1NH17EC702	AKSHAJA SHIMALNA O K	EXE	C1	
6	1NH17EC703	ARAVIND P M	PROF	C1:	Again
7	1NH17EC704	ASHIKA P	EXE	C1	tetal
8	1NH17EC705	BHARGAVI	EXE	C1	17
9	1NH17EC706	CHANDU V J	PROF-G	C1	charte
10	1NH17EC707	DHANUSH S	EXE	C1	
11	1NH17EC708	DHINAKARAN S	EXE	C1	
12	1NH17EC709	DISHANTH M P	PROF	C1	
13	1NH17EC710	G DEEPIKA	EXE	C1	Der Big
14	1NH17EC711	GAGANASHREE P S	EXE	C1	601 000
15	1NH17EC712	GRACE GLADYS A	EXE	C1	gaace
16	1NH17EC713	HARSHITH NAIDU J	EXE	C1	W - J
17	1NH17EC714	J BHAVANA	EXE	C1	Bronow &
18	1NH17EC715	JAYADEEP G P	EXE	C1	Jan do
19	1NH17EC716	JILLELLA RUPASAGAR REDDY	EXE	C1	TARIN
20	1NH17EC717	K AISWARYA	EXE	C1	4
21.	1NH17EC718	KONDURU SIVARAMA RAJU	EXE	C1	Janela
22	1NH17EC719	KOTEPALLI RAMCHARAN	PROF	C1	1
23	1NH17EC720	LAVANYA S N	EXE	C2	Que
24	1NH17EC721	MANISHA M N	EXE	C2	Maristo
25	1NH17EC722	MANOHAR G	EXE	C2	
26	1NH17EC723	MANOJ M R	PROF	C2	Nanoi
27	1NH17EC724	MUNAGALA BHARATH KUMAR REDDY	EXE	C2	m Bluel
28	1NH17EC725	NAINI NARAMA	EXE	C2	
29	1NH17EC726	NAVEEN KUMAR M S	EXE	C2	Dayces
30	1NH17EC727	NEHA KRISHNA	EXE	C2	Webst.
31	1NH17EC728	NIDUJUVVI CHAKRADHAR	EXE	C2	Hay)
32	1NH17EC729	NIKHIL DWIVEDI	EXE	C2	During.
33	1NH17EC730	P K KANAKA SHARANYA	EXE	C2	Law
34	1NH17EC731	PALVADHI VENKATA SAI PAVAN KALYAN	PROF-G	C2	p-plus
35	1NH17EC732	PAVAN U	EXE	C2	and:
36	1NH17EC733	PRACHI VERMA	PROF	C2	Prachi
37	1NH17EC734	PRAJWAL P	EXE	C.2	RanaPP
38	1NH17EC735	R PAVITHRA	EXE	C2	Inqual'
39	1NH17EC736	RAHUL BALAJI P S	EXE	C2	P. E. Belia A.
40	1NH17EC737	RISHIKA POTEPALLI	EXE	C2	Children
41	1NH17EC738	RITWIK SHOME	PROF	C2	18
42	1NH17EC739	S DENILA GRACELIN	EXE	C2	terilar
43	1NH17EC740	S RAHUL YADAV	EXE	C2	9/

NAME OF THE EVENT/CLUB:

Art of electronics

FEEDBACK ON(Mention the topic): And of electronics Instruction: Please put a tick mark at the appropriate column.

DATE:

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	4				
Average			· ·	~	
Satisfactory			V 10 10		V.

Comments/Remarks/Suggestions:

1.

2.

3.

Name & USN No.(optional)

NAME OF THE EVENT/C	LUB: Art of	Electronics	

FEEDBACK ON(Mention the topic):

Instruction: Please put a tick mark at the appropriate column.

DATE: 16 & 18

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good					
Average		1	5/:		
Satisfactory					

Comments/Remarks/Suggestions:

1.

2.

3.

Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Digital Analog	
FEEDBACK ON(Mention the topic):	DATE: 16/3/13

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent	V	- 1 2			"
Good				T	
Average			<b>V</b> .		
Satisfactory	- 1 Ex			V.	

Comments/Remarks/Suggestions:

1.

2.

3.

Name & USN No.(optional) INHITECO 77

Signature (optional)

Robel V

			711 7 01017		
NAME OF TH	IE EVENT/CLU	JB: API O	P elect	Hours (	Guest lecture
	ON(Mention the lease put a tick		ropriate colun	DATE:	16/08/18.
Rating	Clarity	Visuals	Skills	Guidance	Overall

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent			×		
Good					
Average					
Satisfactory	4	100			_

Comments/Remarks/Suggestions:

1.

2.

3.

Name & USN No.(optional)

	FEEDDACK	a OKWI	
NAME OF THE EVENT/CLUB:	Art of	Electronics	

FEEDBACK ON(Mention the topic):

DATE: 16/8/18.

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent			_		
Good					
Average					
Satisfactory	- 1 163		, "		

Comments/Remarks/Suggestions:

1.

2.

3.

Name & USN No.(optional)

NAME OF THE EVENT/CLUB: Gvest Lecture

FEEDBACK ON (Mention the topic): At of Electronic DATE: 118 | 8 | 118 Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent	c				
Good					1
Average			~	).t	
Satisfactory	h.				

Comments/Remarks/Suggestions:

1. —

2. —

3.

Name & USN No.(optional) INHITEC 737
INHITEC 733

Signature (optional)

WE:

NAME OF THE EVENT/CLUB: A GUEST LECTURE

FEEDBACK ON(Mention the topic): ART OF ELECTRONICS DATE: 18-08-18 Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	V		/	ÞI	
Average					
Satisfactory			2 3		

Comments/Remarks/Suggestions:

1. Good

2.

3.

Name & USN No.(optional)

NAME OF THE EVENT/CLUB: GUEST LECTURE

FEEDBACK ON (Mention the topic): ART OF Electronics DATE: 11 08 / 18
Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent	$\checkmark$	V	V	~	V
Good		14			
Average			4		
Satisfactory	· ·				

Comments/Remarks/Suggestions:

1. Good.

2.

3.

Name & USN No.(optional)

quest lecture. NAME OF THE EVENT/CLUB:

FEEDBACK ON (Mention the topic): Art of electronics DATE: 11 8 16.

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent		<b>✓</b>			
Good					
Average				N.	
Satisfactory	3.59				

Comments/Remarks/Suggestions:

1. Nice lecture.

Lisnals go could have then Inited.

3.

Name & USN No.(optional)

NAME OF THE EVENT/CLUB:	Genest	Sotwe
-------------------------	--------	-------

DATE: 16-8-18

FEEDBACK ON (Mention the topic): This design Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good					
Average		5	÷:		
Satisfactory	(#)	,			

Comments/Remarks/Suggestions:

1. decture was informational

2. Levent about sun micro fraccior

3.

Name & USN No. (optional) Kiran N (INH 16EC039)

Signature (optional)

Kiran N

FEEDBACK ON(Mention the topic):

DATE: 11.8.18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent	V.				
Good	4				
Average	******				
Satisfactory					

Comments/Remarks/Suggestions:

He really motivated us and made us realize the value of today's technology.

The audience could have been more interactive

Name & USN No. (optional) Kushi Pouramma K.P (INH16 EC043)

Signature (optional)

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quest Lecture

FEEDBACK ON(Mention the topic):

DATE: 11 8 18

Instruction: Please put a tick mark at the appropriate column.

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					1
Good	1	1		V NE 2	
Average			-		
Satisfactory	30		1:		-

Comments/Remarks/Suggestions:
1. The audience could have been managed letter

2.

3.

Name & USN No.(optional) LEHARIKA INHIBEC019

#### NAME OF THE EVENT/CLUB:

FEEDBACK ON(Mention the topic):

Instruction: Please put a tick mark at the appropriate column.

DATE: 16/8/18

Rating	Clarity	Visuals	Skills	Guidance	Overall
Excellent					
Good	1			V	
Average					
Satisfactory					Good

Comments/Remarks/Suggestions:

1. The topics were brief and understanding

2. It was Interacting and good.

3.

Name & USN No. (optional) NH16E (010) Anusha, H. N