

NEW HORIZON COLLEGE OF ENGINEERING
Autonomous College Affiliated to VTU, Accredited by NAAC with 'A' grade and Accredited by NBA
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR EVEN SEMESTER 2022-23

Section : A
Semester: IV

Class Teacher : Mr.Nagendra R

W.E.F

Room No. : A214

15.05.2023

| Day/ Time | 8.00 -9.00 | 9.00 -10.00 | 10.00 -11.00 | 11.00 -11.10 | 11.10 -12.10 | 12.10 -1.10 | 1.10 -1.50 | 1.50 -2.50 | 2.50 -3.50 | 3.50 -4.50 | |
|-----------------|---|---|--------------|------------------|---|-----------------------------------|------------|-----------------------|---|------------|--|
| Monday | | 2IECE46A | 2IECE41A | BREAK | 2IHSS421A | | LUNCH | 2IHSS441A | 2IECE45A (A1)/2IECE46A (A2)/2IECE47A (A3) | | |
| Tuesday | | 2IECE45A (A2)/2IECE46A (A3)/2IECE47A (A1) | | | 2IECE41A | 2IHSS431A | | 2IECE45A | 2IECE47A | Library | |
| Wednesday | | 2IECE47A | 2IECE46A | | 2IECE45A | 2IECE41A | | 2IECE46A | 2IECE48A | | |
| Thursday | | 2IECE45A | 2IECE47A | | 2IECE45A (A3)/2IECE46A (A1)/2IECE47A (A2) | | | 2IECE48A | 2IDMAT41A | | |
| Friday | | 2IECE45A | 2IECE47A | | 2IECE41A | 2IECE46A | | 2IECE48A | 2IDMAT41A | | |
| Saturday | | Summer Internship - I | | | | | | Summer Internship - I | | | |
| Subject details | | | | | LAB details | | | | | | |
| Course code | Course | | | Name of Faculty | Lab Code | Lab | Batch | Name of Faculty | | | |
| 2IECE41A | Applied Mathematics - IV | | | Mr Ananda K | 2IECE45A | Hardware Description Language Lab | A1 | Mr Parag Jain | | | |
| 2IHSS421A | Life Skills for Engineers | | | Prof. Sabari | | | A2 | Ms Monika G | | | |
| 2IHSS431A | Entrepreneurship Development - 2 | | | Ms Shalini S N | | | A3 | Mr Ajay Bale | | | |
| 2IHSS441A | Constitution of India & Professional Ethics | | | Ms.A.Vijaya | 2IECE46A | Microprocessors Lab | A1 | Mr Nagendra R | | | |
| 2IECE45A | System Design using HDL | | | Ms Ramanamma P | | | A2 | Ms Swati N | | | |
| 2IECE46A | Microprocessors & Interfacing | | | Mr Nagendra R | | | A3 | Mr Nagendra R | | | |
| 2IECE47A | Digital Signal Processing | | | Ms Ishani Mishra | 2IECE47A | Digital Signal Processing Lab | A1 | Mr Parag Jain | | | |
| 2IDMAT41A | Basic Applied Mathematics - II | | | Mr Ananda K | | | A2 | Ms Ishita D | | | |
| 2IECE48A | Summer Internship - I | | | | | | A3 | Ms Amarjeet P | | | |

NSA
Timetable In-charge

Ananda K
HOD - ECE

DEAN ACADEMICS

Nagendra R
PRINCIPAL

NHCE/DTI/005

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR EVEN SEMESTER 2022-23

Room No. : A215
15.05.2023

Section : B
Semester: IV

Class Teacher : Ms.Amarjeet P

W.E.F

| Day/ Time | 8.00 -9.00 | 9.00 -10.00 | 10.00 -11.00 | 11.00 -11.10 | 11.10 -12.10 | 12.10 -1.10 | 1.10 -1.50 | 1.50 -2.50 | 2.50 -3.50 | 3.50 -4.50 | |
|--------------------|---|-----------------------|--------------|--------------|---|-----------------|---|---|------------------------|------------|--|
| Monday | | 21ECE45A | 21ECE46A | | 21ECL45A (B1) /21ECL46A (B2) / 21ECL47A (B3) | | LUNCH | 21ECE41A | 21ECE48A | Library | |
| Tuesday | | 21ECE41A | 21HSS421A | | 21HSS441A | | | 21ECL45A (B2) /21ECL46A (B3) / 21ECL47A (B1) | 21ECE48A | | |
| Wednesday | | 21ECE45A | 21ECE46A | BREAK | 21ECL45A (B3) /21ECL46A (B1) / 21ECL47A (B2) | | | 21ECE47A | 21HSS431A | 21ECE48A | |
| Thursday | | 21ECE46A | 21ECE47A | | 21ECE41A | 21ECE45A | | 21ECE48A | 21DMAT41A | | |
| Friday | | 21ECE47A | 21ECE45A | | 21ECE47A | 21ECE41A | | 21ECE46A | 21DMAT41A | | |
| Saturday | | Summer Internship - I | | | | | | Summer Internship - I | | | |
| LAB details | | | | | | | | | | | |
| | Subject details | | | | Name of Faculty | Lab Code | Lab | Batch | Name of Faculty | | |
| Course code | Course | | | | | | | | | | |
| 21ECE41A | Applied Mathematics - IV | | | | Mr Prashanth K K | 21ECL45A | Hardware Description Language Lab | B1 | Mr Mahendra S N | | |
| 21HSS421A | Life Skills for Engineers | | | | Prof. Suneetha | | | B2 | Mr. Richard P | | |
| 21HSS431A | Entrepreneurship Development - 2 | | | | Ms Shalini S N | | | B3 | Ms Monika G | | |
| 21HSS441A | Constitution of India & Professional Ethics | | | | Ms.A.Vijaya | 21ECL46A | Microprocessors Lab | B1 | Mr Avinash N J | | |
| 21ECE45A | System Design using HDL | | | | Ms Monika G | | | B2 | Mr Nagendra R | | |
| 21ECE46A | Microprocessors & Interfacing | | | | Dr. Piruthiviraj P | | | B3 | Mr Avinash N J | | |
| 21ECE47A | Digital Signal Processing | | | | Ms.Amarjeet P | 21ECL47A | Digital Signal Processing Lab | B1 | Ms Ishita D | | |
| 21DMAT41A | Basic Applied Mathematics - II | | | | Mr Ananda K | | | B2 | Mr. Parag Jain | | |
| 21ECE48A | Summer Internship - I | | | | | | | B3 | Ms.Swati N | | |

NSA
Timetable In-charge

Amarjeet P
HOD - ECE

DEAN ACADEMICS

Majeth
PRINCIPAL

NHCE/DTT/005

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR EVEN SEMESTER 2022-23

Room No. : A216

Section : C

W.E.F 15.05.2023

Semester: IV

Class Teacher : Mr. Parag Jain

| Day/ Time | 8.00 -9.00 | 9.00 -10.00 | 10.00 -11.00 | 11.00 - 11.10 | 11.10 -12.10 | 12.10 -1.10 | 1.10 -1.50 | 1.50 -2.50 | 2.50 -3.50 | 3.50 -4.50 |
|-----------------|---|---|------------------|---------------|--------------|---|------------|---|------------|------------|
| Monday | | 21ECL45A (C1)/21ECL46A (C2)/ 21ECL47A (C3) | | BREAK | 21ECE45A | 21ECE41A | LUNCH | 21ECE46A | 21ECE48A | 21ECE48A |
| Tuesday | | 21ECE45A | 21ECE41A | | 21ECE46A | 21ECE47A | | 21ECE47A | 21ECE48A | 21ECE48A |
| Wednesday | | 21ECE47A | 21ECE45A | | 21HSS421A | | | 21ECL45A (C2)/21ECL46A (C3)/ 21ECL47A (C1) | | Library |
| Thursday | | 21ECE46A | 21ECE45A | | 21ECE47A | 21ECE41A | | 21HSS441A | 21DMAT41A | |
| Friday | | 21ECL45A (C3)/21ECL46A (C1)/ 21ECL47A (C2) | | | 21ECE41A | 21ECE46A | | 21HSS431A | 21DMAT41A | |
| Saturday | Summer Internship - I | | | | | | | | | |
| Subject details | | | | | LAB details | | | | | |
| Course code | Course | | Name of Faculty | | Lab Code | Lab | Batch | Name of Faculty | | |
| 21ECE41A | Applied Mathematics - IV | | Mr Prashanth K K | | 21ECL45A | Hardware Description Language Lab | C1 | Mr Parag Jain | | |
| 21HSS421A | Life Skills for Engineers | | Prof. Sabari | | | | C2 | Mr Nathan K | | |
| 21HSS431A | Entrepreneurship Development - 2 | | Ms Shalini S N | | | | C3 | Dr Gurulakshmi | | |
| 21HSS441A | Constitution of India & Professional Ethics | | Ms.A Vijaya | | 21ECL46A | Microprocessors Lab | C1 | Mr Poverajan T | | |
| 21ECE45A | System Design using HDL | | Dr Aravinda K | | | | C2 | Ms Swati N | | |
| 21ECE46A | Microprocessors & Interfacing | | Dr Jayanthi M | | | | C3 | Ms Swati N | | |
| 21ECE47A | Digital Signal Processing | | Mr. Parag Jain | | 21ECL47A | Digital Signal Processing Lab | C1 | Ms Shrawna K | | |
| 21DMAT41A | Basic Applied Mathematics - II | | Mr Ananda K | | | | C2 | Ms Shrawna K | | |
| 21ECE48A | Summer Internship - I | | | | | | C3 | Ms Anurag P | | |

NJA
Timetable In-charge

Aravinda K
HOD - ECE

DEAN ACADEMICS

PRINCIPAL

NHCE/DTT/05