

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

HANDS ON WORKSHOP ON VLSI DEVICE, CIRCUIT AND SYSTEM DESIGN TOOLS

Date :12-01-2024 Duration :10 am-12 pm

Workshop topic : Hands on Workshop on "VLSI Device, Circuit and System Design Tools"

Number of Participants : 37

Resource person : Mr. Amber Nilabh,

Senior Director and General Manager,

(PESG) India, Intel

This report details the proceedings and outcomes of the One-Day workshop on "VLSI Device, Circuit and System Design Tools". This event was conducted by the Department of Electronics & Communication, in association with the Centre for Life Skills and Skill Development and the Industry Institute Interaction Cell, under the auspices of the Cappemini Engineering VLSI Centre of Excellence.

The workshop featured a comprehensive agenda, including:

- Keynote addresses from renowned experts in the field of VLSI design.
- Technical sessions covering topics such as advanced design tools, emerging trends, and challenges in VLSI.
- Hands-on sessions for participants to gain practical experience with cutting-edge design tools.
- Q & A session providing a platform for interaction and discussion among participants and experts.

The primary objectives of the workshop were:

- 1. To provide insights into the latest developments in VLSI device, circuit, and system design tools.
- 2. To facilitate knowledge exchange and collaboration among participants.
- 3. To enhance practical skills through hands-on sessions and demonstrations.
- 4. To strengthen the connection between academia and industry, fostering a collaborative learning environment.

The One-Day workshop on "VLSI Device, Circuit, and System Design Tools" was a resounding success, achieving its goals of knowledge dissemination, practical skill enhancement, and industry-academia collaboration. The event not only provided a platform for learning but also fostered a sense of community among participants, paving the way for future collaborations and advancements in VLSI design.





Department of Industry Institute Interaction Cell Centre for Like Skills and Skill Development Capgemini Engineering VLSI Centre of Excellence

Attendance for One Day Workshop on VLSI Device, Circuit and System Design Tools

12/01/2024

S. No.	Name	USN No.	Section	Signature
1	B Sushma	1NH21EC027	A	
2	B ROHAN SRIVATSAV	1NH21EC026	-	
3	Dhimanth M	1NH21EC045	Α	Thinoth
4	GANAPATHI C K	1NH21EC058	Α	Gargathi
-5-	Deepak H	1NH22EC403	-A	1-1-
-6-	Athul Satheesh	-1NH21EC025	-A-	
-7	Mahesh Leshappa Herakal	1NH22EC407	Α	Rhous
8	Gnanesh H Nayak	1NH21EC059	Α	Grane Sh!
9	Priyanka Ninganagoudra	1NH22EC408	Α	6.
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-17	Sushmath R	1NH22EC414	_B_	
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25	Shashank C R	1NH21EC146	C	
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41	M. Chandara	NHRIECOSA	FCEB	UC
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