

## **DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

### HANDS ON WORKSHOP ON INTRODUCTION TO STA

Date	: 27-06-2024
Duration	: 2:00 PM - 4:00 PM
Workshop topic	: Hands on Workshop on "Introduction to STA"
Number of Participants	: 20
Resource person	: Mr. Shoaib Ahmed, Senior RTL Design Engineer, Smartsoc Solutions

#### Introduction

The workshop on "Introduction to Static Timing Analysis (STA)" was conducted to provide participants with a comprehensive understanding of STA, a crucial aspect of VLSI design. STA is essential for ensuring that a digital circuit operates correctly at its intended speed. The workshop aimed to equip attendees with the knowledge and skills to perform timing analysis effectively.

#### Objectives

- i) To understand the fundamentals of Static Timing Analysis.
- ii) To learn the importance of STA in VLSI design.
- iii) To explore the tools and techniques used in STA.
- iv) To gain hands-on experience with STA tools.

#### Workshop Overview

The workshop was divided into several topics, each focusing on different aspects of STA. The workshop included lectures, demonstrations, and hands-on exercises. The key topics covered were:

- 1 Introduction to STA
- 2 Timing Paths and Constraints
- 3 Clocking and Timing Models
- 4 Timing Analysis and Optimization
- 5 Hands-on Session
- 6 Tools and Software

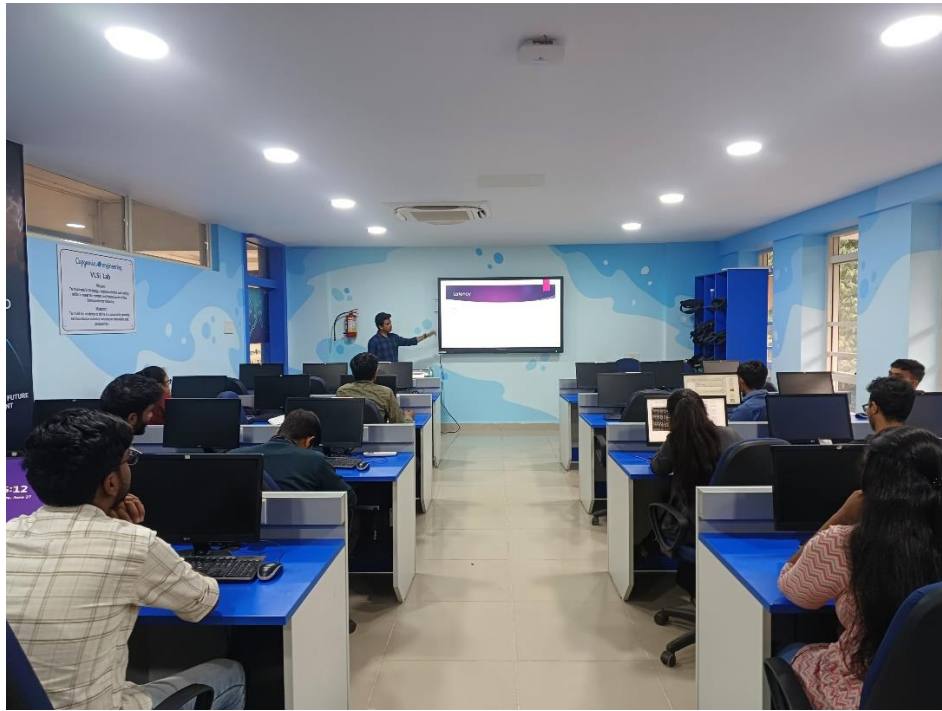
#### Outcomes

The workshop successfully achieved its objectives. Participants gained:

- i) A solid understanding of the fundamentals of STA.
- ii) Practical experience with STA tools and techniques.
- iii) The ability to perform basic STA on VLSI designs.
- iv) Knowledge of how to interpret timing reports and optimize timing performance.

#### Conclusion

The "Introduction to Static Timing Analysis (STA)" workshop was a valuable learning experience for all attendees. It provided a strong foundation in STA and prepared participants to apply these skills in their VLSI design projects. The workshop's success highlighted the importance of continuous learning and hands-on practice in the ever-evolving field of VLSI design.



FACULTY CORDINATOR  
Dr. Monika Gupta

HOD-ECE

