| Title                             | Alumni Talk on "Books to Boards"   |                                    |
|-----------------------------------|--|------------------------------------|
| Department                        | Electronics and Communication Engineering  |                                    |
| Date                              | From:12 <sup>th</sup>  | To: 12 <sup>th</sup> November 2024 |
|                                   | November 2024  |                                    |
| Time                              | From: 2 PM   | To: 3 PM                           |
| Brief Description (4-5 Lines Max) | Department of Electronics and Communication Engineering conducted an International alumnus talk on "Books to Boards" on 12th November 2024 at Falconry Seminar Hall. The speaker, Lakshya Sharma is an alumnus of ECE department (Batch-2019-2023). Mr. Lakshya is currently an Intern in Product Engineering at Cadence Design Systems, with over 2 years of experience in VLSI design. He possesses extensive knowledge of ASIC Design Flow, including Floor Planning, Power Planning, Placement, CTS, Routing, and STA stages.  During the session, Lakshya provided an in-depth overview of the IC design flow and timing analysis. He also discussed the essential skill sets required for securing placements in the VLSI industry. The session concluded with an interactive Q&A, where students sought advice and clarified their doubts about placement opportunities in the core domain.  Overall, the session was highly informative and beneficial for the students. |                                    |







