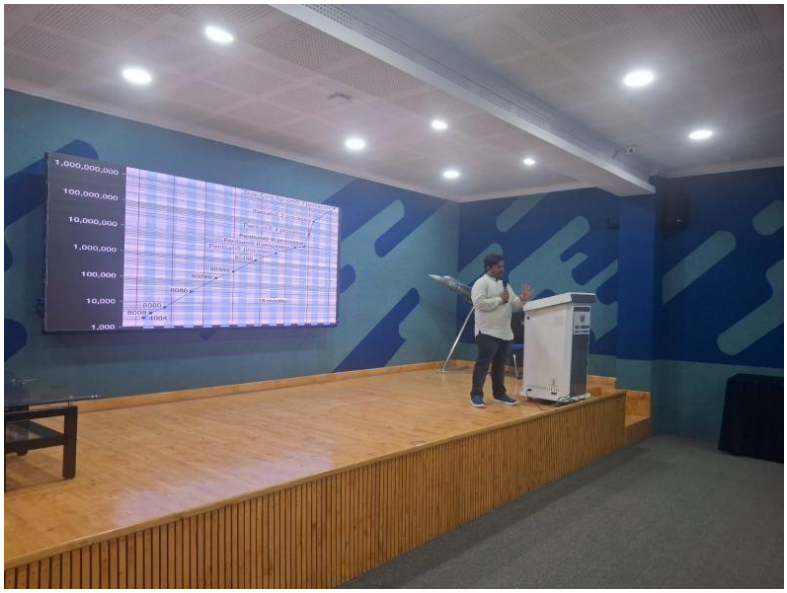
Title	Alumni Talk on "**Introduction to VLSI Design Flow:Unravel the mysteries of VLSI Design**"	
Department	Electronics and Communication Engineering	
Date	From :6th December 2024	To : 6th December 2024
Time	From : 2 PM	To: 3 PM
Brief Description (4-5 Lines Max)	The Department of Electronics and Communication Engineering hosted an international alumnus talk titled "Introduction to VLSI Design Flow: Unravel the Mysteries of VLSI Design" on December 6, 2024, at Tejas Seminar Hall. The speaker, Vikas R, an alumnus from the ECE department (Batch of 2019-2023), is currently an IP Design Engineer at Insemi Technology Services Pvt Ltd. With over 2 years of experience in VLSI design, Vikas has extensive knowledge in ASIC Design Flow, Floor Planning, Power Planning, Placement, CTS, Routing, and STA stages. During the session, Vikas discussed the IC design flow and timing analysis, and highlighted the skill sets necessary for securing placements in the VLSI industry. The event concluded with an interactive Q&A session, where students sought advice and clarified their doubts regarding placement opportunities in the core domain. Overall, the session was highly informative.	

NEW HORIZON
COLLEGE OF ENGINEERING