Name of the Event	Expert Lecture						
Date	04-04-2024						
Beneficiary	IV SEMESTER STUDENTS						
Faculty Coordinator	Dr.M.Jayanthi, Dr.P.Sudhakiran						
Write up	The Department of Electronics and Communication Engineering organized an expert lecture on "AI for Electronic Design Automation" on April 4 th 2025. The session was delivered by Mr. Aloke Das, Founder of <i>Lab and Lectures</i> and Chair of IEEE CASS Bangalore.						
	The expert talk aimed to provide valuable insights into the transformative role of Artificial Intelligence (AI) in Electronic Design Automation (EDA) and its significant impact on modern semiconductor design.						
	Key Highlights of the Lecture:						
	Mr. Aloke Das elaborated on the integration of AI in EDA tools, emphasizing the following key areas:						
	1. Automation in Chip Design: AI-driven algorithms are revolutionizing traditional chip design processe by enhancing design accuracy, reducing errors, and improving overall efficiency.						
	2. EDA Tools with AI Capabilities: Advanced tools such as <i>Synopsys DSO.ai</i> , <i>Cadence Cerebrus</i> , and <i>Siemens AI-powered tools</i> are reshaping digital IC design, Layout Automation, and Verification processes.						
	3. AI for Physical Design Optimization: Machine learning models are being utilized to optimize Placement, Routing, and Timing Closure, significantly reducing design cycle times.						
	4. Fault Prediction and Debugging: AI enables early Fault Detection, Predictive Maintenance, and Automated Debugging, thereby enhancing chip reliability.						
	5. Emerging Job Roles in AI-Based Chip Design:						
	 AI/ML Engineer in EDA: Develops AI models for semiconductor design. 						

- o **EDA Tool Developer:** Works on AI-powered Algorithms for layout optimization and circuit simulation.
- **Design Verification Engineer (AI-Enhanced):** Utilizes AI for automated testing and verification.
- Hardware-Software Co-Design Engineer: Integrates AI techniques with hardware acceleration.
- **AI Architect for the Semiconductor Industry:** Designs AI frameworks for chip manufacturing.

The session was attended by **121 students from the 4th Semester**, who actively participated in discussions on the future of AI in VLSI and the semiconductor industry. The lecture provided valuable insights into career opportunities and technological advancements in AI-driven chip design.



Department of Electronics and Communication Engineering

Expert Lecture





for Electronic Design Automation



04 April, 2025



2:00 PM to 4:00 PM



Falconry Seminar Hall



4th Semester



Aloke Das

Founder - Lab and Lectures, Chair, IEEE CASS Bangalore

Faculty Coordinators

Dr. M. Jayanthi

Associate Professor - ECE

Dr. P. Sudhakiran

Associate Professor - ECE

Convenor

Dr. Aravinda K

Professor & HOD - ECE

Photos from the Expert Lecture









Attendance



NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELCTRONICS AND COMMUNICATION ENGINEERING

EXPERT LECTURE

Expert Lecture Attendance

Date:04-04-2025

Time: 2 P.M. to 4 P.M.

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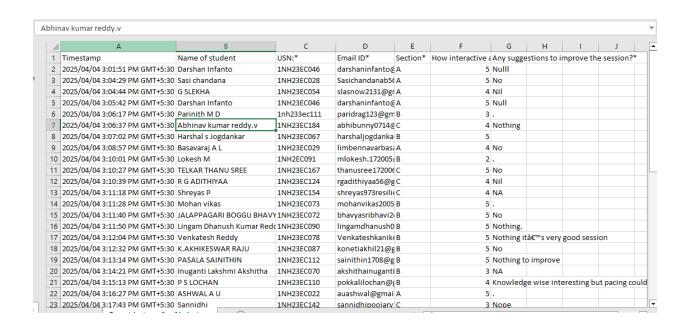
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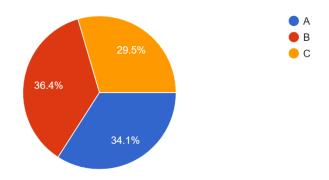
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Section* 44 responses



How interactive and engaging was the session?* 44 responses

