

Name of the	Expert Lecture
Event	Empere Ecoure
Date	21-11-2025
Beneficiary	III Semester Students
Faculty Coordinators	Dr.P. Sudhakiran and Mr.Vinay R
Write up	The Department of Electronics and Communication Engineering organised an expert lecture on "Recent Trends in VLSI and Job Opportunities in ASIC" for III semester students of Digital Electronics (24ECE33). The session was delivered by Mr. Nagireddy Veeranji Reddy, Member of Technical Staff, AMD India Pvt. Ltd.
	The speaker discussed key advancements in CMOS and VLSI technologies, challenges in deep submicron design, and emerging trends such as FinFET, GAAFET, 3D ICs, and Chiplet architecture. He also explained the ASIC design flow from RTL design to physical verification and highlighted the importance of industry-leading EDA tools.
	He provided detailed guidance on career opportunities in VLSI, including roles such as ASIC Front-End Engineer, Verification Engineer, Physical Design Engineer, DFT Engineer, and Post-Silicon Validation Engineer.
	A total of 138 students attended the lecture. The session was informative, interactive, and highly beneficial, offering students clear insights into semiconductor technologies and industry expectations. The department thanked Mr. Nagireddy Veeranji Reddy for delivering an excellent and insightful talk.
	Students Gained
	<ul> <li>Awareness of current semiconductor trends.</li> <li>Understanding of submicron design challenges.</li> <li>Clear knowledge of ASIC design flow.</li> <li>Insight into VLSI job roles and required skill sets.</li> <li>Motivation to explore careers in the semiconductor sector.</li> </ul>

#### Conclusion

The expert lecture was highly informative and industry-oriented, providing III semester students with valuable exposure to real-world VLSI practices and career pathways. The department thanked Mr. Nagireddy Veeranji Reddy for delivering an impactful and enriching session.

#### **Poster of the Event**



Department of Electronics and Communication Engineering



# Recent Trends in VLSI and Job Opportunities in ASIC

- 21 November, 2025
- 10:00 AM to 12:00 PM
- Falconry Seminar Hall
- 3rd Semester (ECE)

**Faculty Coordinators** Dr. Sudhakiran P Associate Professor

Mr. Vinay R Senior Assistant Professor

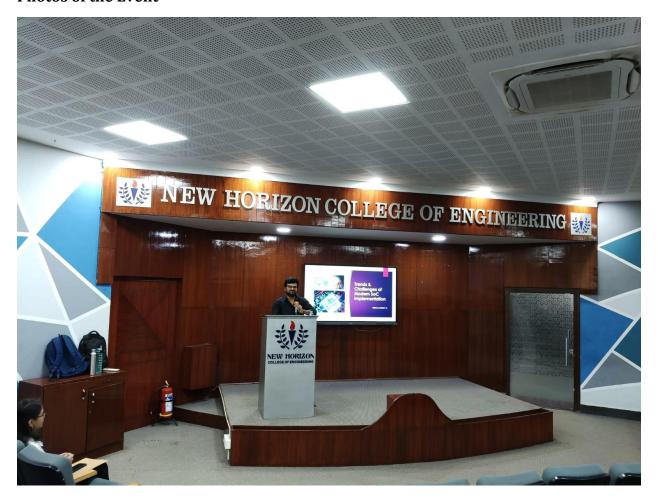


Mr. Nagireddy Veeranji Reddy Member of Technical Staff AMD India Pvt. Ltd.

Convenor Dr. Aravinda K HoD - ECE



## **Photos of the Event**













#### NEW HORIZON COLLEGE OF EGINEERING

## DEPARTMENT OF ELCTRONICS AND COMMUNICATION ENGINEERING

Topic name: Recent Trends in VLSI and job opportunities at ASIC

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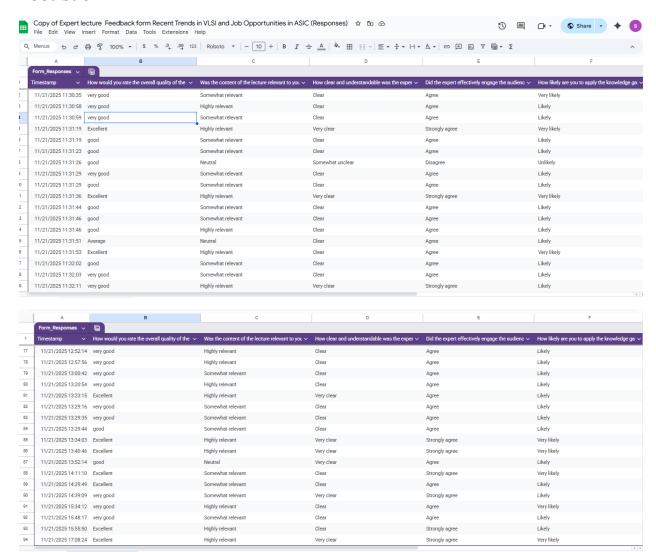
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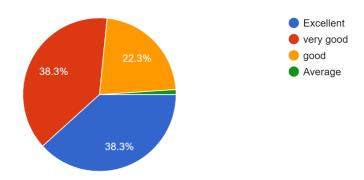
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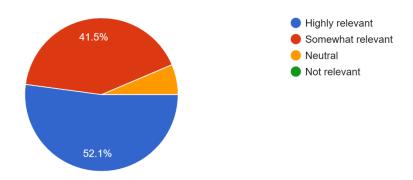
### **Feedback**



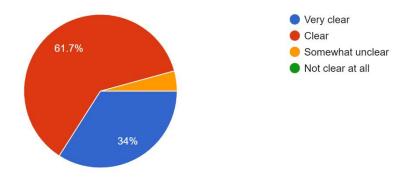
How would you rate the overall quality of the expert lecture session? 94 responses



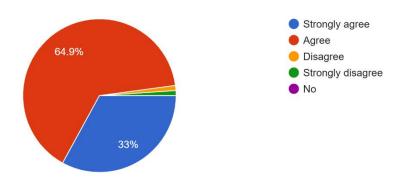
Was the content of the lecture relevant to your interests or field of study? 94 responses



How clear and understandable was the expert's presentation? 94 responses



Did the expert effectively engage the audience during the session? 94 responses



How likely are you to apply the knowledge gained from this lecture in your work or studies? 94 responses

