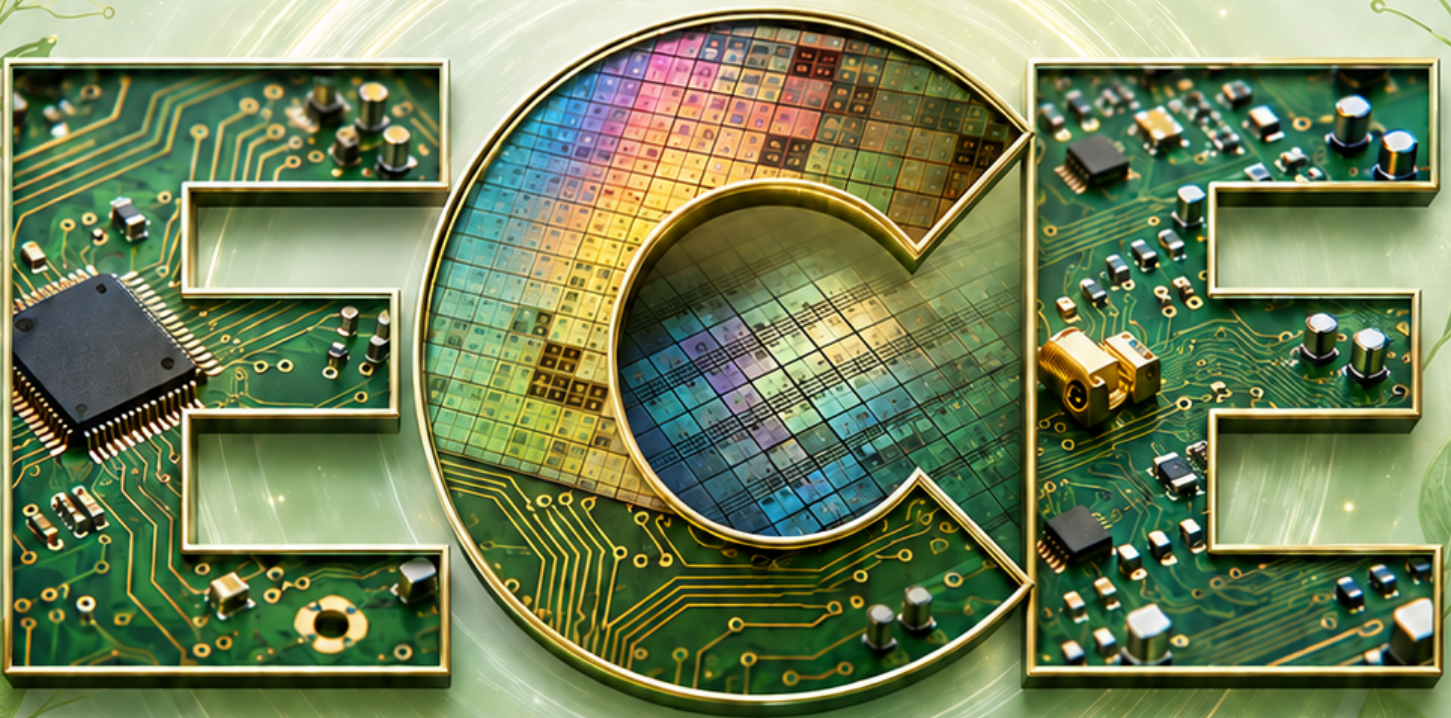




NEW HORIZON
COLLEGE OF ENGINEERING

ECHOES OF IMPULSE

Rewiring Innovation for a Sustainable Future



Department of Electronics and
Communication Engineering

ODD SEMESTER

ISSUE 22

FROM E-ELECTRONICS TO GREEN ELECTRONICS

— Rethinking every electron—from raw material to responsible recovery —



Electronics have transformed how we communicate, learn, work and heal. Yet their linear journey—extract, manufacture, consume and discard—carries a growing environmental cost. Green electronics replaces that one-way model with a circular system: fewer hazardous materials, cleaner fabrication, lower operating energy, longer product life, repairable architecture and recovery of valuable components.



62 Mt

e-waste generated
worldwide in 2022



22.3%

formally collected
and recycled



82 Mt

projected global
e-waste by 2030

THE GREEN ELECTRONICS REVOLUTION

Five shifts are changing how electronic products are conceived, fabricated, powered and recovered.

2024–2026 DEVELOPMENT WATCH

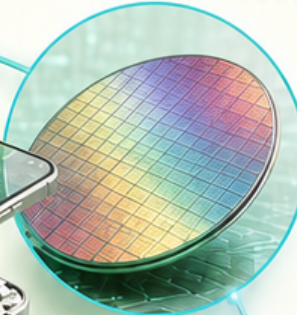
01 • DESIGN TO LAST

Modular construction, replaceable parts and longer software support are turning durability into an engineering requirement. Since June 2025, EU rules for smartphones and tablets include battery durability, spare-part access and an A–E repairability score.



02 • CLEANER CHIP FABRICATION

Semiconductor sustainability now targets energy, ultrapure water, process gases and persistent chemicals. In 2025, imec reported promising high-resolution trials of PFAS-free EUV photoresists, while work continues on rinses, underlayers and waste-stream monitoring.



03 • CIRCULAR & BIO-BASED BOARDS

Vitrimer PCBs can be repaired, separated and reused; research reported recovery of 98% of the polymer. Plant-fibre boards made from flax and jute are also being developed for wireless and high-speed electronics.



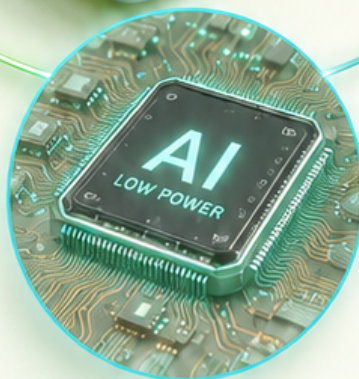
05 • HIGH-EFFICIENCY POWER

GaN and SiC switches operate at higher frequencies, voltages and temperatures than conventional silicon in many applications. Their lower conversion losses can shrink chargers, improve EV powertrains and raise the efficiency of renewable-energy systems.



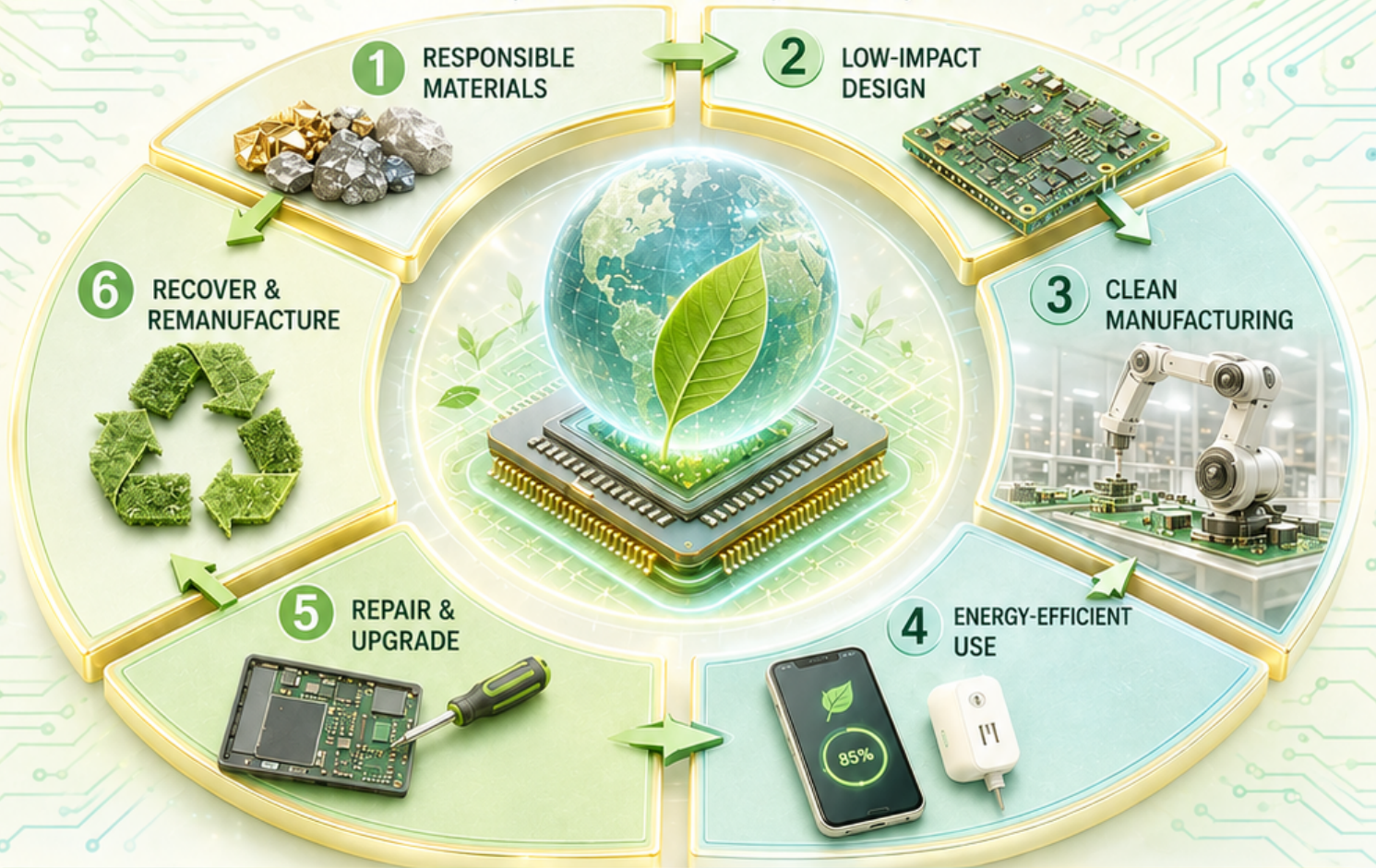
04 • ENERGY-SMART COMPUTING

Edge AI, in-memory computing and neuromorphic hardware reduce costly data movement and process information closer to the sensor. New 2D-material research programmes are targeting efficient inference and training for low-power intelligent devices.



DESIGNING THE CIRCULAR ELECTRONIC FUTURE

Green electronics is not one material or one device. It is a systems-engineering approach that measures environmental impact across the complete life cycle.



AN ECE DESIGN CHECKLIST

- ☐ Minimise material and component count
- ☐ Select safer, traceable and recyclable materials
- ☐ Design modules for disassembly and replacement
- ☐ Measure power, carbon, water and chemical impact
- ☐ Extend lifetime through repair and software support
- ☐ Plan collection, reuse and material recovery
- ☐ Verify claims with life-cycle assessment

THE ECE OPPORTUNITY

Electronics engineers can connect sustainability with performance from the first schematic onward. The next generation of circuits must be not only faster, smaller and smarter, but also cleaner to manufacture, efficient to operate, easier to repair and valuable at end of life. Green electronics transforms environmental responsibility from an afterthought into a core design specification.

WHAT TO WATCH

-  Digital product passports
-  Repairability labels
-  PFAS-free chip processes
-  Biodegradable RF substrates
-  Neuromorphic and in-memory AI
-  Urban mining and robotic disassembly



THE BEST ELECTRONIC PRODUCT IS ONE DESIGNED FOR A SECOND LIFE.

Evidence base: ITU-UNITAR Global E-waste Monitor 2024; European Commission 2025; imec 2025; IEEE Spectrum 2024; University of Glasgow 2025.

LEADERSHIP MESSAGE

From HOD's Desk

A perspective on learning, innovation and opportunity in Electronics and Communication Engineering



Dr. Aravinda K.

BE, MS (Engg.), MTech, PhD

Professor & Head

Electronics and Communication
Engineering

Today the world has shrunk and the global village is marching towards technological revolution, predominantly due to innovations in Electronics and Communication. The field opens doors to a myriad of opportunities and exciting challenges for the go-getters.

The Department of Electronics and Communication Engineering is accredited by the National Board of Accreditation (NBA). Its vision is to create high-quality engineering professionals who can transform society and earn global recognition.

The department is bestowed with well-designed and well-maintained infrastructure. It is equipped with interactive classrooms, laboratories with modern equipment, and state-of-the-art facilities that encourage students to experiment. The department also offers a VTU Research Centre for Ph.D. and M.Sc. (Engg.) research.

The enthusiastic teaching fraternity is highly qualified and has the acumen to instil in students the urge to do better and bring out their best. Most faculty members have considerable academic and research experience, while a few also bring valuable industrial experience.

PROFESSOR & HEAD Department of Electronics and Communication Engineering

A LEARNING ECOSYSTEM BUILT FOR THE FUTURE

Knowledge that connects. Experience that transforms.

The message continues with the programme's industry focus, practical exposure, placement preparation, student achievements and vibrant campus engagement.

01

INDUSTRY

02

INNOVATION

03

IMPACT

LEADERSHIP MESSAGE • CONTINUED

From HOD's Desk

CONTINUED

INDUSTRY-ALIGNED LEARNING

The Electronics and Communication Engineering programme, with its autonomous status, is redesigned to cater to the needs of industry. The courses focus on intriguing areas such as Embedded Systems, Communication, VLSI, Signal Processing and Information Technologies.

Industry-relevant technology courses are a feather in the department's cap. Experts from reputed organisations such as IBM, HP, Texas Instruments, Sankalp Semiconductors, Audience Communication, Intel, ISRO and IISc, along with other reputed institutes, visit the department. Their interaction with students provides a distinctive advantage in a fast-paced technological world and helps learners develop solutions for real-world problems. Workshops, seminars, competitive events, conferences and industrial visits are also organised regularly.

To gain practical exposure and develop technical and interpersonal skills, ECE students execute projects throughout their studies. They are motivated to publish research papers and participate in national and international conferences. Students also take the lead in planning and executing activities through the Electronics Hobby Club, Technology Sharing Club and Professional Connect Club, gaining an enriching experience.

OPPORTUNITY • ACHIEVEMENT • GROWTH

Students undergo specialised placement training through value-added programmes. Many are placed in reputed organisations such as Intel, Texas Instruments, AMD, Qualcomm, ARM, Schneider Electric, Bosch, Cisco Systems, Juniper Networks, VMware, Sony, Nokia, Accenture, Capgemini, IBM, HP, TCS, Infosys, Wipro and Mindtree. Others pursue higher studies in India and abroad, while some establish their own ventures and contribute to society's growth.

As the famous saying reminds us, "All work and no play makes Jack a dull boy." Students actively engage in cultural, sports and social activities. Many have gone a step further by winning gold medals and trophies at different levels and across several institutions. Overall, the department provides a positive and nurturing environment in which students grow into knowledgeable, skilled and productive Electronics and Communication Engineers.

“

A positive and nurturing environment enables students to become knowledgeable, skilled and productive Electronics and Communication Engineers.

— DR. ARAVINDA K.

DEPARTMENT IDENTITY & ACADEMIC FRAMEWORK

Department Vision & Mission

The purpose and commitments guiding the ECE learning community

VISION

Engineering excellence • Societal purpose • Global recognition

To create high quality engineering professionals who can serve the society and earn global recognition.

M1

To build a strong foundation in Electronics and Communication Engineering aspects by exposing students to state-of-the-art technology and research which helps the student with the application of knowledge.

M2

To strengthen the curriculum through interaction with industry experts and to equip the students with the required competency.

M3

To mould students to share technical knowledge and to practice professional and moral values.

ACADEMIC FRAMEWORK

Program Educational Objectives (PEOs)

Graduate capabilities shaped through the ECE programme

PEO1

To produce graduates with understanding of fundamentals and applications of Electronics and Communication Engineering.

PEO2

To hone graduates with ability to apply, analyze, design and develop electronic systems.

PEO3

To enhance graduates with latest technologies to enable them to engineer products for real world problems in Electronics and Communication Engineering.

PEO4

To build leadership qualities, management skills, communication skills, moral values, team spirit and lifelong learning ability for the graduates.

ACADEMIC FRAMEWORK • CONTINUED

Knowledge & Attitude Profile (WK)

WK1-WK2

WK1

A systematic, theory-based understanding of the natural sciences applicable to the discipline and awareness of relevant social sciences.

WK2

Conceptually-based mathematics, numerical analysis, data analysis, statistics and formal aspects of computer and information science to support detailed analysis and modelling applicable to the discipline.

ACADEMIC FRAMEWORK

Knowledge & Attitude Profile (WK) – Continued

WK3–WK9 • Knowledge foundations supporting responsible engineering practice

WK3

A systematic, theory-based formulation of engineering fundamentals required in the engineering discipline.

WK4

Engineering specialist knowledge that provides theoretical frameworks and bodies of knowledge for the accepted practice areas in the engineering discipline; much is at the forefront of the discipline.

WK5

Knowledge, including efficient resource use, environmental impacts, whole-life cost, re-use of resources, net zero carbon, and similar concepts, that supports engineering design and operations in a practice area.

WK6

Knowledge of engineering practice (technology) in the practice areas in the engineering discipline.

WK7

Knowledge of the role of engineering in society and identified issues in engineering practice in the discipline, such as the professional responsibility of an engineer to public safety and sustainable development.

WK8

Engagement with selected knowledge in the current research literature of the discipline, awareness of the power of critical thinking and creative approaches to evaluate emerging issues.

WK9

Ethics, inclusive behavior and conduct. Knowledge of professional ethics, responsibilities, and norms of engineering practice. Awareness of the need for diversity by reason of ethnicity, gender, age, physical ability etc., with mutual understanding and respect, and of inclusive attitudes.

GRADUATE ATTRIBUTES

Program Outcomes (POs)

PO1-PO6

PO	GRADUATE ATTRIBUTE	OUTCOME STATEMENT
PO1	Engineering Knowledge	Apply knowledge of mathematics, natural science, computing, engineering fundamentals and an engineering specialization as specified in WK1 to WK4 respectively to develop the solution of complex engineering problems.
PO2	Problem Analysis	Identify, formulate, review research literature and analyze complex engineering problems reaching substantiated conclusions with consideration for sustainable development. (WK1 to WK4)
PO3	Design/Development of Solutions	Design creative solutions for complex engineering problems and design/develop systems/components/processes to meet identified needs with consideration for public health and safety, whole-life cost, net zero carbon, culture, society and environment as required. (WK5)
PO4	Conduct Investigations of Complex Problems	Conduct investigations of complex engineering problems using research-based knowledge including design of experiments, modelling, analysis and interpretation of data to provide valid conclusions. (WK8)
PO5	Engineering Tool Usage	Create, select and apply appropriate techniques, resources and modern engineering and IT tools, including prediction and modelling, recognizing their limitations to solve complex engineering problems. (WK2 and WK6)
PO6	The Engineer and The World	Analyze and evaluate societal and environmental aspects while solving complex engineering problems for their impact on sustainability with reference to economy, health, safety, legal framework, culture and environment. (WK1, WK5 and WK7)

PO7-PO11 CONTINUE ON THE NEXT PAGE →

GRADUATE ATTRIBUTES • CONTINUED

Program Outcomes (POs) – Continued

PO7–PO11 • Professional capability, responsibility and growth

PO	GRADUATE ATTRIBUTE	OUTCOME STATEMENT
PO7	Ethics	Apply ethical principles and commit to professional ethics, human values, diversity and inclusion; adhere to national and international laws. (WK9)
PO8	Individual and Collaborative Team Work	Function effectively as an individual, and as a member or leader in diverse and multi-disciplinary teams.
PO9	Communication	Communicate effectively and inclusively within the engineering community and society at large, such as being able to comprehend and write effective reports and design documentation, and make effective presentations considering cultural, language and learning differences.
PO10	Project Management and Finance	Apply knowledge and understanding of engineering management principles and economic decision-making and apply these to one's own work, as a member and leader in a team, and to manage projects and in multidisciplinary environments.
PO11	Life-Long Learning	Recognize the need for, and have the preparation and ability for: i) independent and life-long learning, ii) adaptability to new and emerging technologies, and iii) critical thinking in the broadest context of technological change. (WK8)

ECE SPECIALISATION

Program Specific Outcomes (PSOs)

PSO1 To demonstrate the ability to design and develop complex systems in the areas of next generation Communication Systems, IoT based Embedded Systems, Advanced Signal and Image Processing, latest Semiconductor technologies, RF and Power Systems.

PSO2 To demonstrate the ability to solve complex Electronics and Communication Engineering problems using latest hardware and software tools along with analytical skills to contribute to useful, frugal and eco-friendly solutions.

SUSTAINABILITY IN ACTION

Sustainability is embedded in the programme through responsible design, efficient technology use and eco-friendly engineering solutions.

DEPARTMENT ACTIVITIES

Faculty Achievements

Innovation, academic service and research leadership

DESIGN REGISTRATIONS



GOVERNMENT OF INDIA • PATENT OFFICE

Two faculty innovations receive design registration

Mr Ajay Sudhir Bale, Senior Assistant Professor, Department of ECE, secured registrations for two practical product designs.

- Multipurpose Smart Messenger — Design No. 469699-001
- Flexible Laptop Stand — Design No. 497831-001

MSME IDEA HACKATHON 5.0

AI-integrated IoT Automated Greenhouse

Dr Mukti Yadav received a Government of India grant under the Incubation Component of the MSME Innovative Scheme (Incubation, Design and IPR), MSME Champions Scheme.

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DEPARTMENT ACTIVITIES

Faculty Development & Hands-on Learning

Strengthening academic quality and semiconductor design practice

FIVE-DAY FACULTY DEVELOPMENT PROGRAMME

Building a Culture of Academic Excellence through the New NBA SAR Framework

27-31 JUL 2026

35 FACULTY

Jointly organised by ECE, CSE and CSE (Data Science) with the Vice Principal-Academics, IQAC and Staff Development Centre, the programme translated the revised NBA SAR into actionable academic practice.

- Outcome-Based Education, curriculum, pedagogy and assessment
- CO-PO attainment, documentation and institutional quality
- Expert sessions, hands-on work and stress-management practices
- Greater confidence in planning and evidence-based implementation



Topics Covered:

1. Teaching-Learning Process (TLP) and Outcome-Based Education (OBE)
2. Outcome-Based Course Design
3. Innovative Pedagogical Approaches for Higher Education
4. Active Learning and Student-Centric Teaching Strategies
5. Digital Tools for Assessment and Evaluation
6. NBA Accreditation - New SAR Framework (Criteria 1-6)
7. NBA Documentation and Best Practices
8. Quality Assurance, Continuous Improvement and Academic Excellence

WHO CAN ATTEND?
Faculty Members, IQAC Coordinators, NBA/NMAC & Accreditation Coordinators, Programme Coordinators, and PG Students applying for Academic Careers.

Registration Link:
<https://forms.gle/HFvskBUtOWND32eb>

Registration Fee: ₹1500
Bank Account Details:
Account Name: New Horizon College of Engineering
Account Number: 484700076
Bank: Indian Bank
IFSC Code: IDIB000K012

ORGANIZING COMMITTEE

Chief Patron:
Dr. Mohan Mangrulkar
Chancellor, NHCE

Patron:
Dr. Dharmashankar Mangrulkar
President, NHCE

Patron:
Dr. Anandhi R. J.
Vice Principal-Academics, NHCE, Bangalore

Advisory Committee

Dr. Ravathi V., Dean- R&D
Dr. Babita Jain, Dean- IQAC
Dr. Uma Reddy N.V., Dean- CSE

Co-Chairpersons

Dr. Anand K., Head ECE, NHCE
Dr. B. Swathi, Head CSE/CSE(DS), NHCE
Dr. Anja Joseph, Head CSE, NHCE
Dr. Raju M., Head CSE, NHCE

Coordinators

Dr. M. Jayanthi
ASST. ECE, NHCE - 9994880704
Dr. J. Jyothsna Devi, ASST. ECE, NHCE - 9994880704
Dr. Sankar Kumar B.
Assoc. Head, IQAC, NHCE - 99942990348

Organizing Members

Dr. Lipa Deb, ASST. ECE
Mr. Anand B., ASST. ECE
Mr. Nathan K.V., ASST. ECE

HANDS-ON WORKSHOP • 04 SEPTEMBER 2025

IC Fabrication Process & Building Blocks of VLSI Design

Resource person: Mr Vikranth Manyam, Senior Manager, Capgemini. The workshop connected semiconductor fabrication fundamentals with the complete VLSI design journey.

- Wafer preparation, lithography, doping, etching and metallisation
- Logic gates, flip-flops, memories and VLSI building blocks
- RTL-to-GDSII flow, CAD demonstrations and layout simulations
- Case studies that linked classroom concepts with industry practice



FROM SILICON TO SYSTEM

ALUMNI CONNECT

Alumni Talks 2025-26

Industry experience brought directly into the ECE classroom

Under the coordination of Dr Ishani Mishra, the department organised a series of alumni talks connecting students with accomplished graduates from AI, data science, VLSI, embedded systems, networking and software technologies.



ARTIFICIAL INTELLIGENCE & DATA SCIENCE

Build Your Own AI Assistant

Ms Sneha Srinivasan

Senior Data Scientist • Nielsen Global Solutions

AI assistants, data-science applications, relevant case studies and career opportunities in intelligent systems.



ALUMNI INTERACTION

Exploring Career Paths & Building Industry Skills

Mr Naveen K. H.

Senior Physical Design Engineer • Qualcomm

ASIC design flow, timing analysis, essential VLSI skills and strategies for securing core roles.



ALUMNI INTERACTION

Inside the Brain of Your Gadgets: A Journey into VLSI

Mr Rohanth Hari

Silicon Engineering Analyst • Accenture

Real-world VLSI applications and the value of certifications, projects and hackathons.



ALUMNI INTERACTION

Turning Challenges into Opportunities through Innovation

Mr Amogh R. Naik

R&D Engineer • Telaverge Communications

Embedded systems, innovation, project-based learning, hackathons and career planning.



ALUMNI INTERACTION

Internet Is a Circuit — You Just Don't See the Wires

Mr Abhijeeth Talari

Software Developer • SAP Labs

Coding, APIs, IoT, computer networking, problem-solving and emerging software careers.

DEPARTMENT ACTIVITIES

Expert Lectures: Cybersecurity, 5G & AI

Preparing students for secure, intelligent and connected systems



EXPERT LECTURE • 04 MAY 2026

Cybersecurity Industry Challenges

Dr Vishal Saraswat • Head, R&I, Bosch

The session examined evolving threats, data protection and security challenges in IoT and embedded platforms, while mapping the skills required for cybersecurity careers.

- Career pathways: cybersecurity analyst, network security engineer and ethical hacker
- Faculty coordinators: Dr P. Sudhakiran and Mr Omkar Kanetkar
- Participation: 112 sixth-semester ECE students and three faculty members

EXPERT LECTURE • 07 APRIL 2026

Exploring Open-Source Technologies in 5G and AI

Nitin Jain • Wireless Signal Processing Consultant

Students explored how open ecosystems accelerate wireless innovation and how AI/ML can improve resource allocation, network optimisation, traffic management and quality of service.

- OpenAirInterface and srsRAN for 5G experimentation
- TensorFlow, PyTorch and ONNX for AI-enabled workflows
- SDN and NFV concepts for programmable networks
- 113 sixth-semester students gained career-oriented exposure



DEPARTMENT ACTIVITIES

Frontiers in Electronics & Artificial Intelligence

Emerging technologies, semiconductor careers and national development



INDUSTRY INTERACTION • III SEMESTER

Recent Trends in VLSI & Job Opportunities in ASIC

Mr Nagireddy Veeranj Reddy • Member Technical Staff, AMD India

The Digital Electronics (24ECE33) session connected CMOS and deep-submicron design with FinFET, GAAFET, 3D ICs and chipleths, while explaining the ASIC journey from RTL through physical verification and the role of EDA tools.

EXPERT LECTURE • 20 NOVEMBER 2025

Semiconductors — The Next Generation in Technological Revolution

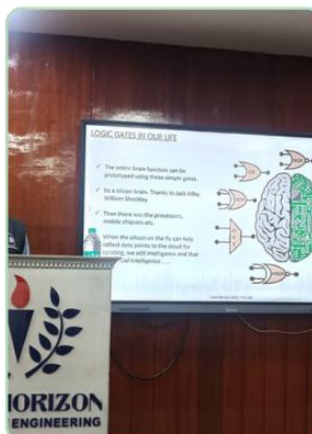
Mr George Jacob

Founder & CEO • Semicon Design Technologies Pvt Ltd

Venue: Falconry Seminar Hall

NEXT-GENERATION SEMICONDUCTORS

Technology • Design • Innovation



GUEST LECTURE • 26 MARCH 2026

Artificial Intelligence for Viksit Bharat

Dr V.J.K. Kishor Sonti

Pro Vice-Chancellor • Jeppiaar University, Chennai

A New Education Policy perspective on AI for a technologically advanced and self-reliant India, with emphasis on emerging technology, critical thinking, innovation and problem-solving.

- Industry applications, careers, employability and interview readiness
- Participation: 180 sixth-semester students



STUDENT ACHIEVEMENTS

Student Achievements

Celebrating excellence beyond the classroom



SPORTS ACHIEVEMENT • 2025-26

Intercollegiate Handball Tournament Winner

Akhil emerged as the winner of the Intercollegiate Level Handball Tournament. His skill, agility, sportsmanship, strategic gameplay and ability to perform under pressure were instrumental in securing this prestigious victory.



NATIONAL INNOVATION CHALLENGE

NHCE Silver Jubilee National Level 48-Hour Hackathon — Third Place

Sixth-semester students P. S. Lochan, Pavithra B., Roshni V. P. and Shivam Puneekar developed a well-structured solution to a real-world problem using cutting-edge technologies. Their teamwork, practical implementation and efficient problem-solving stood out among teams from across the country.



QUANTUM INNOVATION

IISc x Qiskit Quantum Hackathon — First Place

The same team applied quantum-computing principles through the Qiskit framework, designing and implementing quantum algorithms for a real-world problem and demonstrating how emerging quantum technologies can offer advantages over classical approaches.

CAREER OUTCOMES

Placement Highlights

UG placement record for the 2021–2026 cohort

The placement record reflects the department's emphasis on technical competence, industry readiness and career development. Students secured opportunities in reputed core-engineering, IT and technology-driven organisations through sustained training and placement support.

115

STUDENTS PLACED

₹33.17 LPA

HIGHEST LISTED OFFER

40+

RECRUITING ORGANISATIONS

S. NO.	ORGANISATION	STUDENTS	SALARY OFFERED
1	Murata Machinery (Japanese)	1	33,17,144
2	Storeys Real Estate LLC (Dubai)	3	33,00,000
3	Alphonso Labs Pvt Ltd	1	18,00,000
4	Gilbarco, Vontier, Invenco	1	12,00,000
5	West Pharmaceutical Packaging India Pvt Ltd	2	9,00,000
6	Jaro Education	1	10,16,000
7	Eurofins IT Solutions India Pvt. Ltd.	2	8,00,000
8	Intellipaat Software Solutions Pvt Ltd	2	8,00,000
9	TE Connectivity	2	7,06,244
10	Visteon Technical & Services Centre	3	7,00,000
11	Nineleaps	5	6,60,000
12	Ernst & Young	2	6,49,240
13	Temenos India Pvt Ltd	2	6,10,000
14	Fractal.ai	2	6,00,000
15	Merck (People 360 D Solutions LLP)	1	6,00,000
16	Capgemini-VLSI	23	5,75,000
17	Amazon Seller Services Private Limited	2	5,50,000
18	The Math Company Pvt Ltd	3	5,50,000
19	Societe General	2	5,45,000
20	CoreEL Technologies	1	5,22,230

CAREER OUTCOMES

UG Placement Record • 2021-2026

Recruiting organisations and listed salary outcomes

S. NO.	ORGANISATION	STUDENTS	SALARY OFFERED
21	Accenture Solutions Private Limited	3	5,05,000
22	Covallence Research Private Limited	1	5,00,000
23	Siemens Technology & Services Pvt Ltd (Global Business Services)	5	5,00,000
24	Assa Abloy Opening Solutions India Pvt. Ltd.	1	4,50,000
25	HCL Tech	1	4,50,000
26	Siemens Global Service	1	4,50,000
27	Federal Bank	1	4,45,670
28	Capgemini -General	4	4,25,000
29	Global Tech DEFENCE LLP (Sipani Energy Ltd)	2	4,00,000
30	MosChip Technologies Ltd	2	4,00,000
31	Tata Consultancy Services (VLSI)	7	4,00,000
32	Infosys	13	3,60,000
33	SASMOS HET Technologies (TalenTree People Consulting Pvt Ltd)	1	3,75,888
34	Kristalball Smart Solutions Pvt Ltd	2	3,50,000
35	RFSYS Technologies Pvt Ltd	2	3,00,000
36	Wimera systems	1	3,00,000
37	Newjaisa Technologies Ltd	1	1,74,000
38	Micro Land	2	4,00,000
39	QSC India	1	7,00,000
40	Waters Corporation	2	10,00,000
42	Zersys Technologies Pvt Ltd	1	4,00,000

CORE ENGINEERING • VLSI • IT • ANALYTICS • TECHNOLOGY SERVICES

ABOUT THE DEPARTMENT

Department of Electronics & Communication Engineering

Engineering knowledge for a sustainable and connected world

NBA
ACCREDITED

The Department of Electronics and Communication is accredited by the National Board of Accreditation. Electronics and communication engineering offers a world of exciting challenges and opportunities: from automation and device interaction to planetary missions, remote sensing, smart cities, self-driving cars, robotics, smart mobiles and the Internet of Things.

At New Horizon College of Engineering, the department's vision is to create high-quality engineering professionals who can transform society and earn global recognition. Highly qualified faculty members bring rich experience from academics, research and industry.

Technology experts from organisations and institutes such as IBM, HP, Texas Instruments, Sankalp Semiconductors, Audience Communication, Intel, ISRO and IISc interact with students and support industry-relevant technology courses.

ELECTRONICS & COMMUNICATION AT THE CORE

AUTOMATION

REMOTE SENSING

SMART CITIES

ROBOTICS

INTERNET OF THINGS

Connect • Create • Communicate

Building responsible electronics for a sustainable and connected world

CONNECT

Industry and society

CREATE

Technology and solutions

COMMUNICATE

Knowledge and purpose

ADDRESS

New Horizon Knowledge Park

Bellandur Main Road, Near Marathahalli
Bengaluru - 560103

ECHOES OF IMPULSE

Department of ECE • Issue 22