

Title	Alumni Talk on “ Introduction to VLSI Design Flow ”	
Department	Electronics and Communication Engineering	
Date	From :23 rd October2026	To : 23 rd October2026
Time	From :11:40 AM	To: 12:40 P M
Brief Description (4-5 Lines Max)	<p>Department of Electronics and Communication Engineering conducted an alumni talk on “Introduction to VLSI Design Flow” on 23rd October 2026.The speaker, Mr.Chetas E Achar, is an alumnus of ECE department (Batch-2020-2024). He is currently working as RTL design Engineer in Accenture. He had discussed about the importance of VLSI and career opportunities in the corresponding field with some relevant case studies. Students interacted with alumni at the end of the session by taking suggestions and clarifying doubts on identifying career goals in the field of VLSI and achieving them with proper certification courses. The session was overall very informative.</p>	

