Title	Alumni Talk on **"Clock Tree Synthesis and Timing Optimization Techniques"**	
Department	Electronics and Communication Engineering	
Date	From :12th November2026	To : 12th November 2026
Time	From :11:40 AM	To: 12:40 P M
Brief Description (4-5 Lines Max)	Department of Electronics and Communication Engineering conducted an alumni talk on **"Clock Tree Synthesis and Timing Optimization Techniques"** on 12th November 2026.The speaker, **Mr.L Shivashankar, Associate Design Enginner Insemi Technology(Infosys)** , is an alumnus of ECE department (Batch-2019-2023). He had discussed about the importance of VLSI and career opportunities in the corresponding field with some relevant case studies. Students interacted with alumni at the end of the session by taking suggestions and clarifying doubts on identifying career goals in the field of VLSI and achieving them with proper certification courses. The session was overall very informative.	







